

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
-30V	11mΩ @ V _{GS} = -10V	-13A
	17mΩ @ V _{GS} = -4.5V	-9.9A

Description

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- Backlighting
- Power management functions
- DC-DC converters

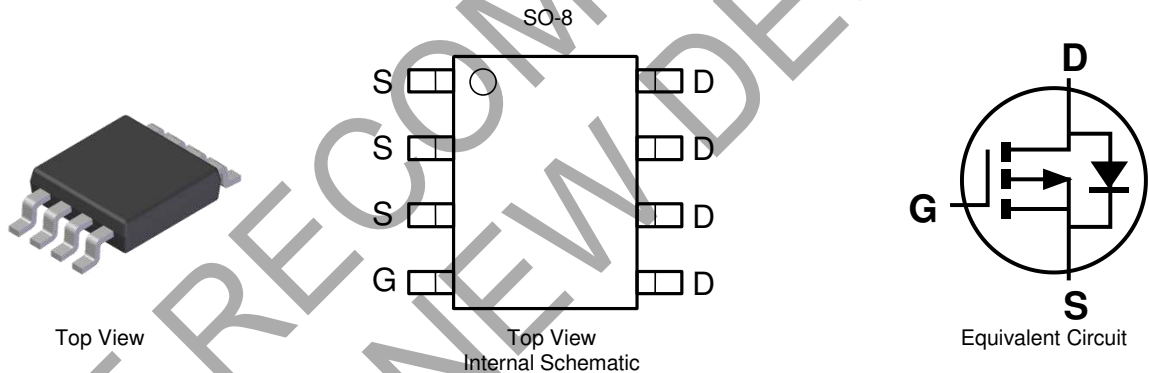
Features and Benefits

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **The DIODES™ DMP3015LSSQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF16949 certified facilities.**

<https://www.diodes.com/quality/product-definitions/>

Mechanical Data

- Package: SO-8
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram
- Terminals: Finish—Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (E3)
- Weight: 0.074g (Approximate)

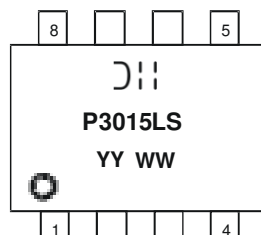


Ordering Information (Note 4)

Part Number	Package	Packing	
		Qty.	Carrier
DMP3015LSSQ-13	SO-8	2500	Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



⑆ = Manufacturer's Marking
 P3015LS = Product Type Marking Code
 YYWW = Date Code Marking
 YY = Year (ex: 22 = 2022)
 WW = Week (01 to 53)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V _{DSS}	-30	V
Gate-Source Voltage			V _{GSS}	±20	V
Drain Current (Note 5)	Steady State	T _A = +25°C	I _D	-13	A
		T _A = +70°C		-9.75	
Pulsed Drain Current (Note 6)			I _{DM}	-45	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P _D	2.5	W
Thermal Resistance, Junction to Ambient	R _{θJA}	50	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Notes: 5. Device mounted on 2oz. copper pads on FR-4 PCB with R_{θJA} = +50°C/W.
6. Pulse width ≤ 10μs, duty cycle ≤ 1%.

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	-30	—	—	V	V _{GS} = 0V, I _D = -250μA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	-1	μA	V _{DS} = -30V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(th)}	-1	—	-2	V	V _{DS} = V _{GS} , I _D = -250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	9	11	mΩ	V _{GS} = -10V, I _D = -13A
		—	14	17		V _{GS} = -4.5V, I _D = -10A
Forward Transconductance	g _{fs}	—	15	—	S	V _{DS} = -15V, I _D = -8A
Diode Forward Voltage (Note 7)	V _{SD}	-0.5	—	-1.1	V	V _{GS} = 0V, I _S = -2.1A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	—	2,748	—	pF	V _{DS} = -20V, V _{GS} = 0V f = 1.0MHz
Output Capacitance	C _{oss}	—	357	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	356	—	pF	
Gate Resistance	R _G	—	2.0	—	Ω	V _{DS} = 0V, V _{GS} = 0V f = 1.0MHz
SWITCHING CHARACTERISTICS (Note 8)						
Total Gate Charge	Q _g	—	30.0	—	nC	V _{DS} = -10V, V _{GS} = -4.5V, I _D = -13A
		—	60.4	—		V _{DS} = -10V, V _{GS} = -10V, I _D = -13A
Gate-Source Charge	Q _{gs}	—	7.2	—	ns	V _{DS} = -10V, V _{GS} = -10V, I _D = -13A
Gate-Drain Charge	Q _{gd}	—	16.4	—		V _{DS} = -10V, V _{GS} = -10V, I _D = -13A
Turn-On Delay Time	t _{D(ON)}	—	11.2	—		V _{DS} = -15V, V _{GS} = -10V I _D = -1A, R _G = 6.0Ω
Rise Time	t _R	—	12.4	—		
Turn-Off Delay Time	t _{D(OFF)}	—	104.9	—		
Fall Time	t _F	—	61.7	—		

Notes: 7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to product testing.

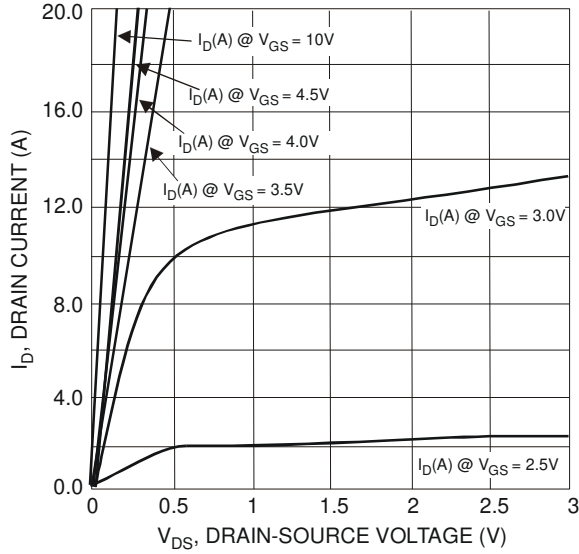


Fig. 1 Typical Output Characteristics

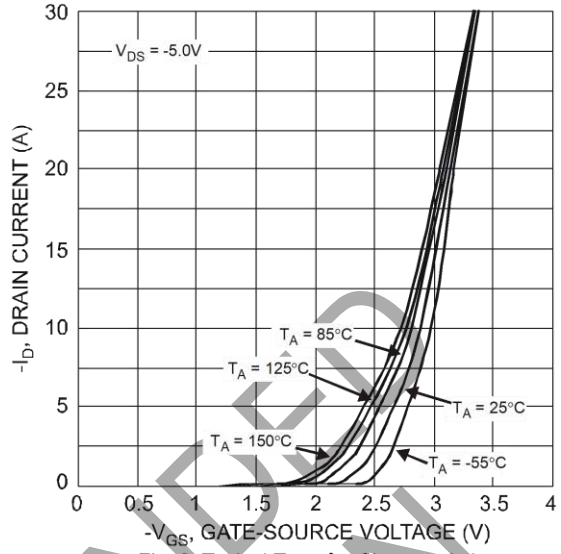


Fig. 2 Typical Transfer Characteristics

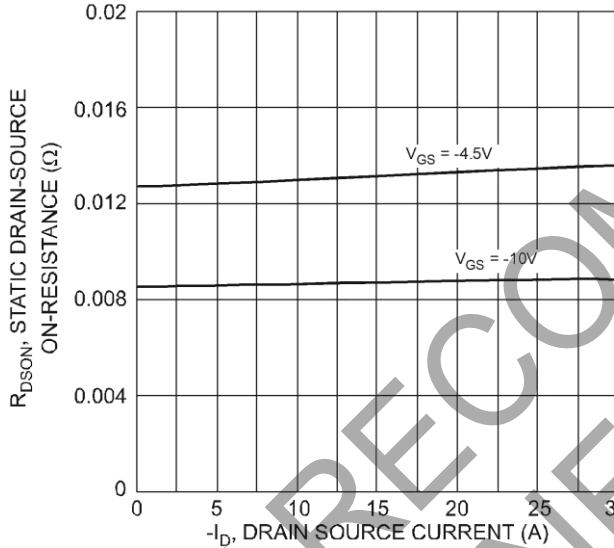


Fig. 3 On-Resistance vs. Drain Current & Gate Voltage

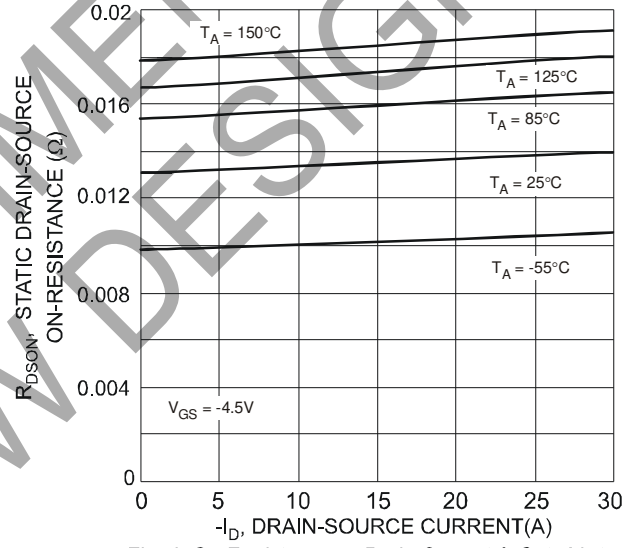


Fig. 4 On-Resistance vs. Drain Current & Gate Voltage

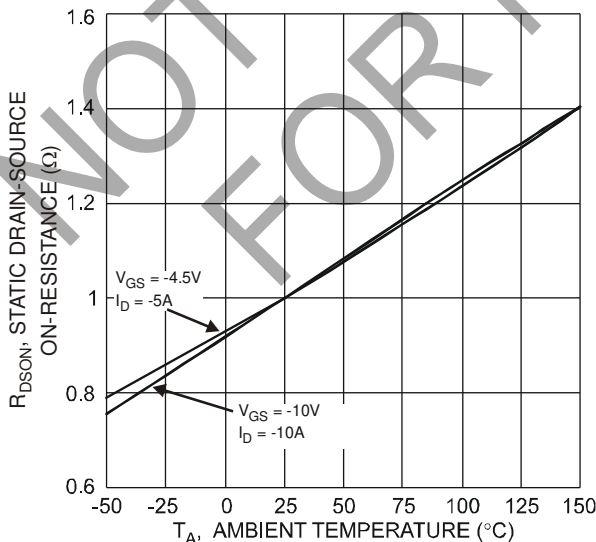


Fig. 5 Static Drain-Source On-Resistance vs. Ambient Temperature

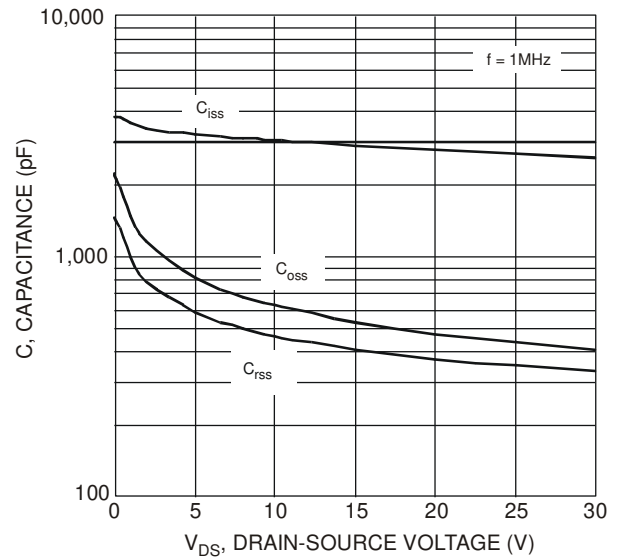


Fig. 6 Typical Total Capacitance

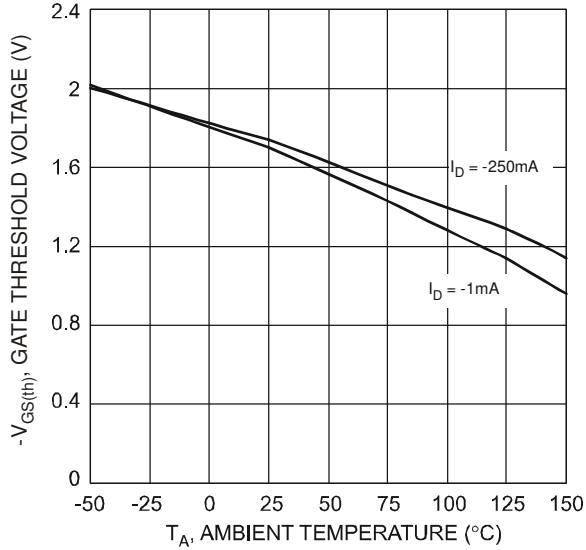


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

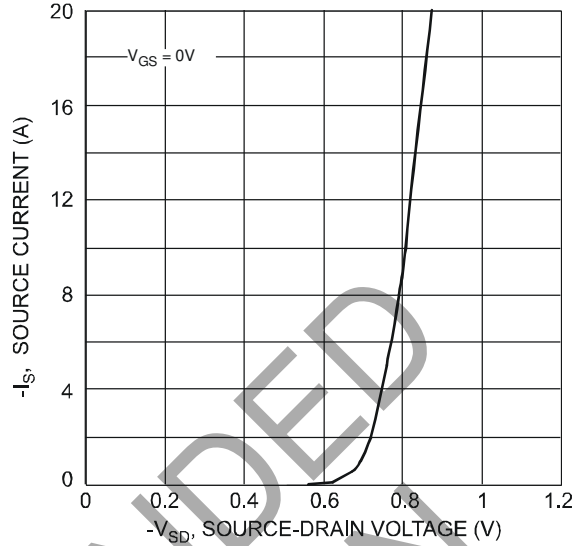


Fig. 8 Forward Drain Current vs. Source-Drain Voltage

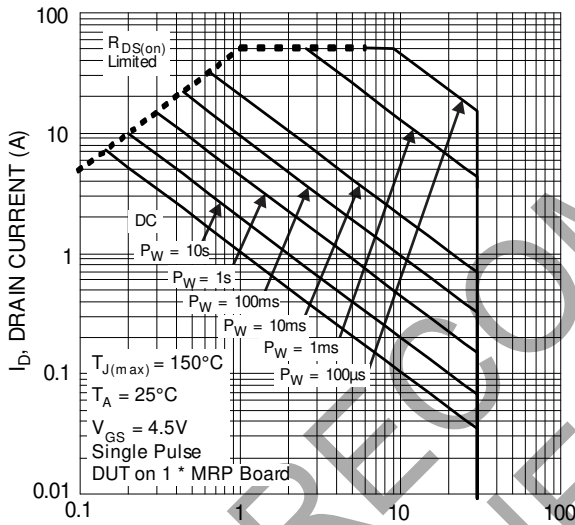


Figure 9 SOA, Safe Operation Area

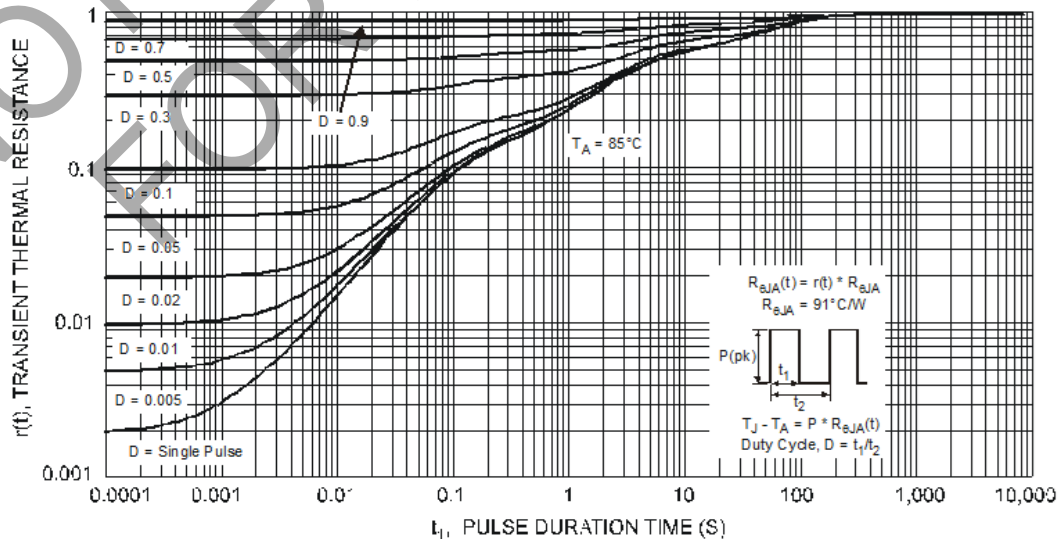
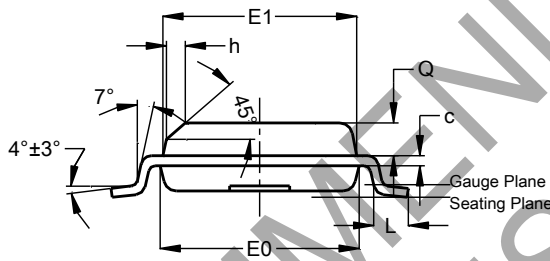
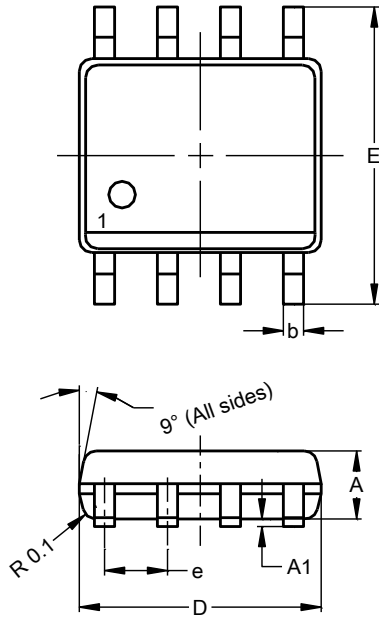


Fig. 10 Transient Thermal Resistance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SO-8

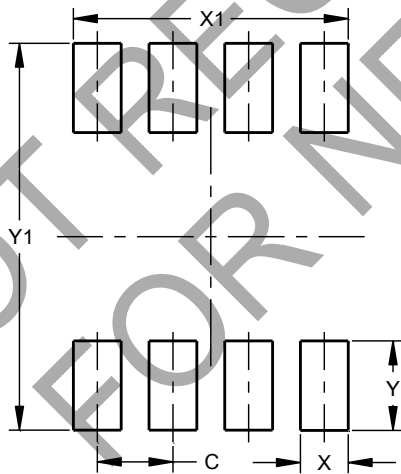


SO-8			
Dim	Min	Max	Typ
A	1.40	1.50	1.45
A1	0.10	0.20	0.15
b	0.30	0.50	0.40
c	0.15	0.25	0.20
D	4.85	4.95	4.90
E	5.90	6.10	6.00
E1	3.80	3.90	3.85
E0	3.85	3.95	3.90
e	—	—	1.27
h	—	—	0.35
L	0.62	0.82	0.72
Q	0.60	0.70	0.65
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SO-8



Dimensions	Value (in mm)
C	1.27
X	0.802
X1	4.612
Y	1.505
Y1	6.50

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