

July. 2007

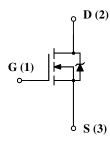
■Features

- •Low on-resistance
- •Low input capacitance
- •Avalanche energy guarantee

Applications

- PDP driving
- •High speed switching

■Equivalent circuit





■Absolute maximum ratings

	(T	a=25°C)	
Characteristic	Symbol	Rating	Unit
Drain to Source Voltage	VDSS	330	V
Gate to Source Voltage	VGSS	±30	V
Continuous Drain Current	ID	±30A	А
Pulsed Drain Current	ID(pulse) ¹⁾	±120A	А
Maximum Power Dissipation	PD	85 (Tc=25°C)	W
Single Pulse Avalanche Energy	EAS ²⁾	500	mJ
Avalanche Current	IAS	30	А
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	-55~150	°C

1) PW $\leq 100\mu s$, duty cycle $\leq 1\%$

2) VDD=20V,L=1mH, ILp=30A,unclamped,RG=50Ω,See Fig.1



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	E	ectrical characteristics				
					(Ta=25°	C)
Characteristic	Symbol	Test Conditions	Limits			11
			MIN.	TYP.	MAX.	Unit
Drain to Source breakdown Voltage	V(BR)DSS	ID=100µA,VGS=0V	330			V
Gate to Source Leakage Current	IGSS	VGS=±30V			±100	nA
Drain to Source Leakage Current	IDSS	VDS=330V, VGS=0V		-0	100	μΑ
Gate Threshold Voltage	VTH	VDS=10V, ID=1mA	3.0		4.5	V
Forward Transconductance	Re(Yfs)	VDS=10V, ID=15A	23	37		S
Static Drain to Source On-Resistance	RDS(on)	ID=15A, VGS=10V		50	63	mΩ
Input Capacitance	Ciss	VDS=25V		4600		
Output Capacitance	Coss	VGS=0V		620		pF
Reverse Transfer Capacitance	Crss	f=1MHz		220		
Turn-On Delay Time	td(on)	ID=15A, VDD≈165V		50		
Rise Time	tr	RL=11 Ω , VGS=10V		60		
Turn-Off Delay Time	td(off)	$RG=5\Omega$		110		ns
Fall Time	tſ	See Fig.2		30		
Source-Drain Diode Forward Voltage	VSD	ISD=30A,VGS=0V		1.0	1.5	V
Gate Threshold Voltage Temp. Coefficient	⊿VTH /⊿Tch	VDS=10V, ID=1mA		-11		mV/°C

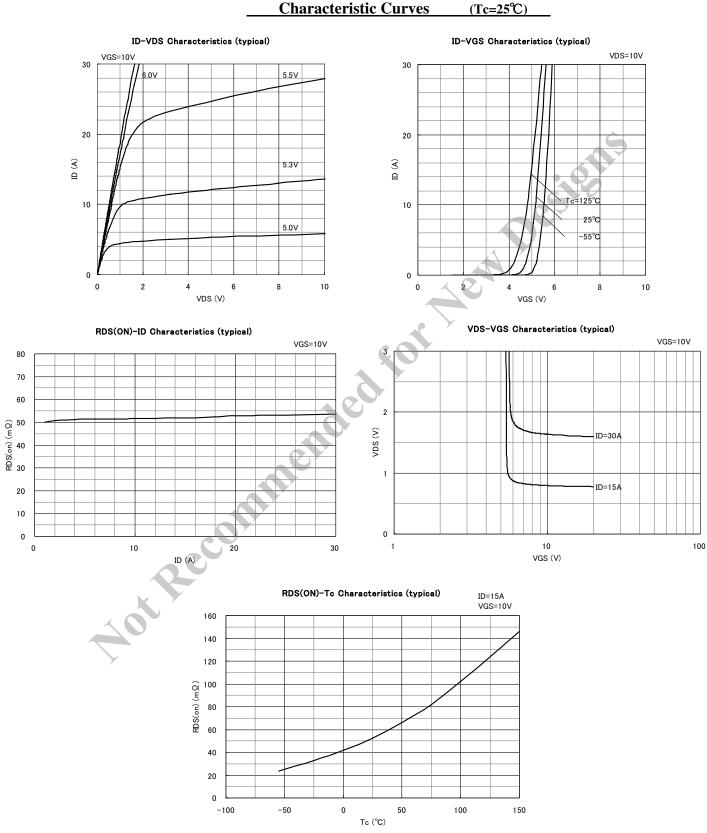
Electrical characteristics

N-Channel MOS FET

FKP330C

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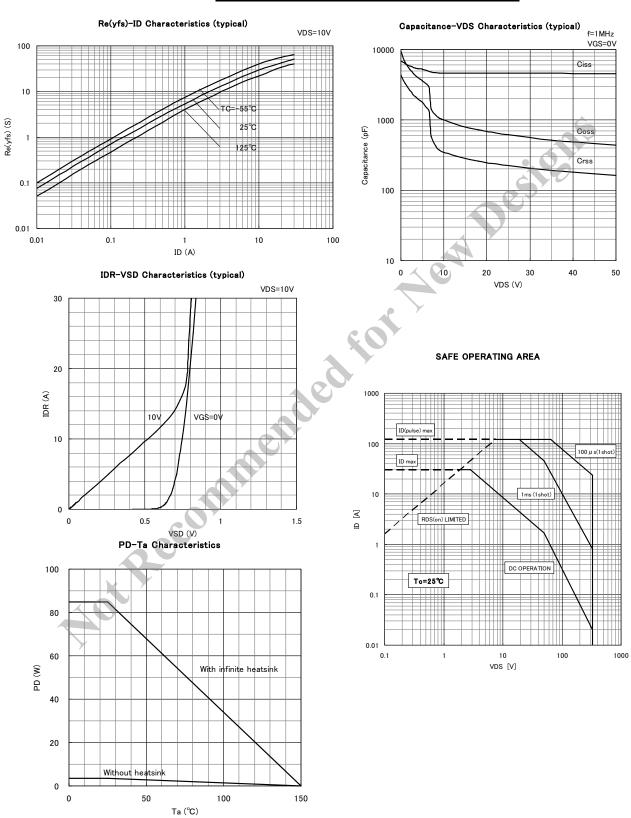


Characteristic Curres (To 25

$\frac{\text{N-Channel MOS FET}}{FKP330C}$

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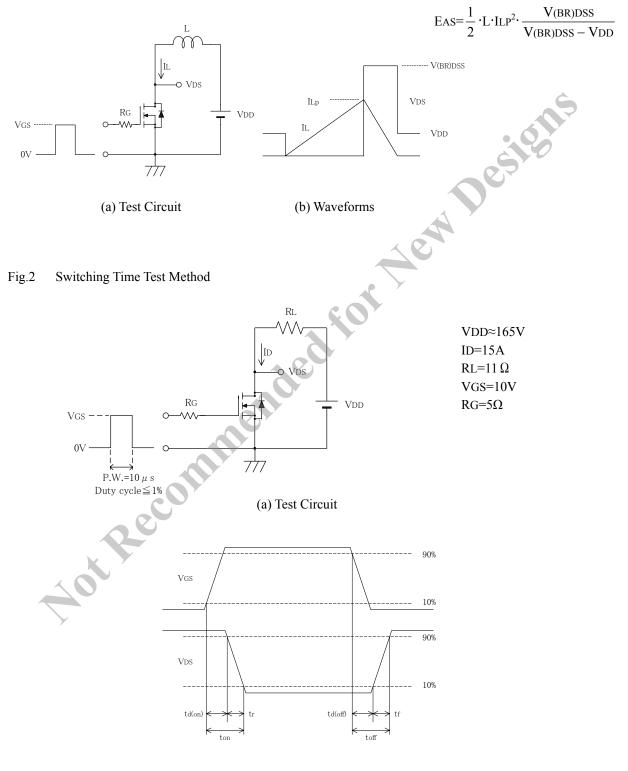
Characteristic Curves (Tc=25°C)



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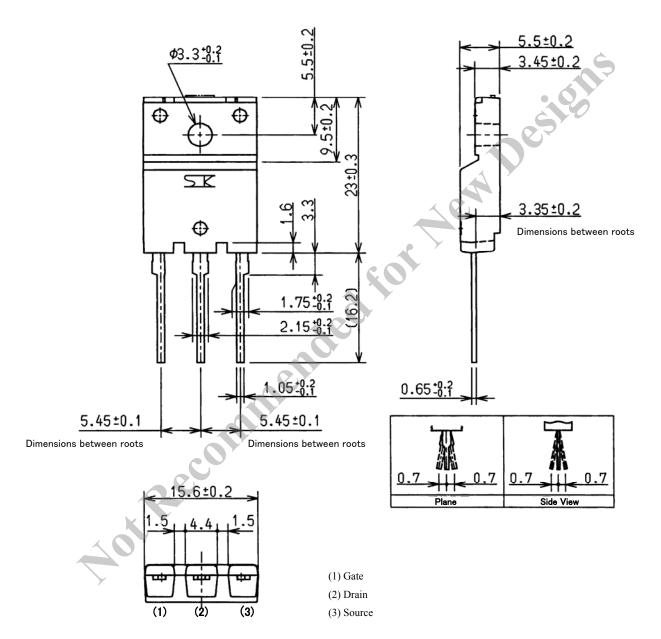
(b) Waveforms



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Outline

FM100 (TO-3P Full Mold)



Weight Approx. 6.5g



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Design

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