

LP5012EVM User's Guide

This user's guide describes the characteristics, setup, and use of the LP5012EVM evaluation module (EVM). The LP5012EVM helps the user evaluate the features of the Texas Instruments LP5012 device, which is an I²C bus-controlled, 12-channel, constant-current LED driver. This user's guide includes setup instructions, a schematic diagram, a bill of materials, printed-circuit board layout drawings, and software instructions.

The LP5012EVM can be converted for testing the LP5009 device by removing the factory-installed device (U1) and replacing it with the LP5009 counterpart. When testing the LP5009 device, select the corresponding LP5009 tag from the menu on the GUI home page (Figure 5) for the evaluation.

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What You Get

1 What You Get

The LP5012EVM kit (Figure 1) contains:

- USB2ANY
 - Ribbon cable
 - USB cable
- LP5012EVM



Figure 1. LP5012EVM Kit



Figure 2 shows the key connectors used to operate LP5012EVM.



Figure 2. LP5012EVM Key Connectors

Table 1	LP5012EVM	Parameters
		rarameters

PARAMETER	RANGE
VCC	3 V–5.5 V
VLED	3 V–5.5 V
VMCU	1.8 V, 3.3 V, 5 V
Channel current	35 mA (maximum)

Note: Channel current is set at 20 mA when the LED supply voltage is 5 V. Although the LP5012 device can withstand 6-V maximum LED supply voltage with 35 mA per channel, make sure that the PCB thermal performance can meet the power dissipation requirement, because increased LED supply voltage increases channel output current.

2 What You Need In Addition

A PC on which to install the LP50EVM GUI (click here to download software) DC supply

3 How to Get Started

3.1 Hardware Setup

What You Need In Addition

Figure 3 shows the hardware setup of the LP5012EVM.

- Connect a 5-V power supply to TP1 (VIN_EXT) and TP6 (GND).
- Using the USB cable, connect the USB2ANY module to the PC.
- Using the ribbon cable, connect the USB2ANY module to the LP5012EVM.



Figure 3. LP5012EVM Hardware Setup







Figure 4. LP5012EVM USB Cable Direction

3.2 Software Installation

Download the GUI software from the product folder. After downloading, install the LP50EVM GUI on the PC. A shortcut to the GUI will be found on the desktop. A shortcut can also be found in the startup menu under the Texas Instruments folder. Figure 6 shows the landing page of the LP50EVM GUI. The landing page contains the LP50xx introduction information and LP5012EVM information. Clicking the menu icon (\equiv) shows more information.



How to Get Started



Figure 5. LP50 Family GUI Home Page



Figure 6. Landing Page of LP5012EVM GUI



3.3 GUI Function

Click the Quick Start button in the lower-right corner. The GUI displays a detailed LED control panel as shown in Figure 7.



Figure 7. LED Control Panel

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How to Get Started

3.3.1 Connection Status

Before using the GUI, make sure the connection status indicator at the bottom left corner is *green*, which means the USB2ANY is connected to the computer successfully. If the status indicator is *red*, try to reconnect the USB2ANY cable and restart the LP50EVM GUI.



Figure 8. LP50 EVM GUI Connection Status

3.3.2 GUI Functions

3.3.2.1 Global Setting

When all the LED outputs are inactive, the LP50xx device is able to enter power-save mode automatically, thus lowering idle-current consumption down to 10 μ A (typical). The automatic power-save mode is enabled when register bit Power_Save_EN = 1 (default) and all the LEDs are off for a duration of > 30 ms. Almost all analog blocks are powered down in power-save mode. If any I²C command to the device occurs, the LP50xx device returns to the NORMAL mode. Set the Power Saver switch to activate the function (see Figure 8).



Figure 9. LP50xx Global Setting

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3.3.2.2 RGB LED Color and Intensity Control

Each output channel has its own individual 8-bit color-setting register (OUTx_COLOR). The device allows every RGB LED module to achieve > 16 million (256 × 256 × 256) color-mixing. When color is fixed, the independent *intensity control*, formerly called brightness control, is used to achieve accurate and flexible dimming control for every RGB LED module. Every three consecutive output channels are assigned to their respective intensity-control register (LEDx_BRIGHTNESS). The LP50xx device allows 256-step intensity control for each RGB LED module, which helps achieve a smooth dimming effect. For human-eye-friendly visual performance, a logarithmic-scale dimming curve is usually implemented in LED drivers. The LP50xx device, with independent color-mixing and intensity-control registers, implements the logarithmic scale dimming control inside the intensity control function, which solves the color distortion issue effectively. Also, the LP50xx device allows users to configure the dimming scale either logarithmically or linearly through the global Log_Scale_EN register. Click the Manage button, shown in Figure 10. to set the color and brightness.



Figure 10. LED Color and Brightness Setting

3.3.2.3 Bank Management

For most LED-animation effects, like blinking and breathing, all the RGB LEDs have the same lighting pattern. Instead of controlling the individual LED separately, which occupies the microcontroller resources heavily, the LP50xx device provides an easy coding approach, the LED bank control. Each channel can be configured as either independent control or bank control through the LEDx_Bank_EN register. When LEDx_Bank_EN = 0 (default), the LED is controlled independently by the related color-mixing and intensity-control registers. When LEDx_Bank_EN = 1, the LP50xx device drives the LED in LED bank-control mode. The LED bank has its own independent PWM control scheme, which is the same structure as the PWM scheme of each channel. See the register map Figure 14 for more details. When a channel is configured in LED bank-control mode, the related color mixing and intensity control is governed by the bank control registers (BANK_A_COLOR, BANK_B_COLOR, BANK_C_COLOR, and BANK_BRIGHTNESS) regardless of the inputs on its own color-mixing and intensity-control registers.













How to Get Started

3.3.3 Lighting Patterns

The GUI also can run the preset lighting patterns and customize the lighting patterns by modifying some parameters.



Figure 13. LED Lighting Pattern



3.3.4 Register Map

The register view appears when the Register icon in Figure 14 is clicked. The register view provides the register values, field values, and descriptions. The registers can be operated directly through the input check box.

Register Map										Auto P	lead Off	Read Register Read All Registers White Register Immed
Retister Name		Address	Veue				В	ita				FIELD VIEW
				3	8	5	4	3	2	1	0	DEVICE CONFIGN
- LP5012		12/70/21	NO. MARK									beviae_ookinas
DEVICE_CONFIG0	0	0x00	0x40		1							LP50127 DEVICE_CONFIG07 RESERVED 1
DEVICE_CONFIG1		Cx01	0x3c			1	1	1	1	8	0	RESERVED 1 0x0
LED_CONFIG0		0x02	0x0B						0	1	1	
BANK_BRIGHTNESS		C×03	Qxπ	1	1	1	1	1	1	1	1	LP9012 / DEVICE_CONFIG0 / Chip_EN
BANK_A_COLOR		0×04	EXFF	1	3	1.	1	3	1	1	3	Chip_EN gr
BANK_B_COLOR		0x05	0:43	0	्य	0	0	0	0	ा	æ	
BANK_C_COLCH		0x06	0x23	0	0	1	0	0	0	1	1	LP5012 / DEVICE_CONFIG0 / RESERVED #
LED0_BRIGHTNESS		0x07	OKDC	1	1	0	1	1	1	0	0	RESERVED 2 0x0
LEDT_BRIGHTNESS		0x06	0x#	1	1	1	1	1	1	1	t	
LEDQ_BRIGHTNESS		0x09	Q×π	1	1	1	1	1	1	1	1	
LEDO_BRIGHTNESS		0x3A	0x#	1	1	T	1	1	1	1	1	
DUT6_COLOR		0x0B	DXFF	1	1	1	1	1	1	1	1	
OUT1_COLOR		000C	0xE2	1	1	1	0	0	0	1	0	
OUT2_COLOR		0x0D	0:(83	3.)	<u>_</u> 0	0	0	3	0	0	1	
OUT3_COLOR		0x0E	CXFF	10	1	10	- S \$ 3	- 31	- T.	31	1	
DUT4_COLOR		0x3F	0059	6	1	- 12	0	- 3F	0	0	- 45	
OUT5_COLOR		6x10	0x60	6	1	- Ť	0	a	0	0	0	
DUT6_COLOR		0x11	CheCl	n	.0	0	10	-0	0	0	α	
0.007_001.09		0x12	Cost	0	0	0	0	0	0	0	CI.	
PO102_87U0		Ex 13	Clock	0	0	0	6	31	0	0	a	
DUTE_COLOR		428.9.4	CaseCl	0	0	0	n	0	α	0	a	
OUT10_COLOR		Exto	(bot)	12	<u>.</u> 9	0	3 (0))	9	0	0	0	
OUTH COLOR		6x16	Coot	R:	30	0	0.0	0	0	0	a	
RESET		0x17	Owd	10	- 0	0	- es	0	0	0	Q.	



Enter the desired hex value in the registers (Value column), perform a bit-wise configuration of any register fields by double-clicking on the corresponding register bit, or configure a register field by entering the desired value as hex in the Value column, binary in the Bits columns, or by using the input boxes for each field under FIELD VIEW to set values for the corresponding variables. Field View displays the description of all fields of the selected register. Each register can be read independently or all registers can be read at once by using the Read Register or Read All Registers button, respectively. The data is written to the registers in one of two ways, depending on the update mode (Immediate or Deferred) button. Clicking the button toggles between the two modes. In Immediate mode, the register data is written immediately following a Current Value, an individual bit, or a Value change. In Deferred mode, the displayed data is written to all registers on depression of the Write Register button. Press the Read All Registers button to read back all the registers and update the values in this table. If any register value must be changed, simply double-click on the individual bit values to change the value in this table in immediate updated mode or press the Write Register button to write all the registers at a time if Deferred is selected instead of Immediate from the drop-down box. Register settings can be saved in text-file format by selecting Save Registers from the file menu. A register settings file in text-file format can be loaded and programmed automatically by selecting Load Register from the file menu.



How to Get Started

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3.3.5 Tool Bar

Registers can be loaded and saved through the tool bar under the File menu.



Figure 15. Tool Bar



4 Board Layout



Figure 16. Top Layer Routing





Figure 17. Bottom Layer Routing (Mirrored)



4.1

5 VRG

4 VBG

6 VR

5 VBG

4 VBG

R

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Schematic VCC TP2 C4 0.1µF C3 1µF R1 4.7k R2 4.7k R3 4.7k VCC /cc VCAP LB0 5 VR LB3 VCAP IN EN R ΕN 5 VBG LB1 LB4 EN C2 1µF SCL SDA SCL SDA 2 LB2 LB5 4 VBG ADDR0 ΕŅ ADDRO ADDR1 Rab Rqb ADDR1 IREF IREF LP5012RUKP 21 GND D3 LB6 6 VR LB9 трз , G **\$**3.57k LB7 LB10 5 VBG VIN EXT GND WR EXT VIN_EXT GND VR_EXT GND GND LB8 4 VBG LB11 Rgb Rgb . VMCU VCC LB0 LB1 LB2 LB3 LB4 LB5 LB6 LB7 LB8 LB9 LB1 LB1 USB2ANY Connections GPIO6/PWM1/SPI(CS) 1 GPIO7/PWM0



Figure 18. LP5012EVM Schematic Diagram



Board Layout

4.2 Bill of Materials

DESIGNATOR	QT Y	VALUE	DESCRIPTION	PART NUMBER	MANUFACTURER
C1	1	10 µF	CAP, CERM, 10 μF, 6.3 V, +/- 10%, JB, 0603	C1608JB0J106K080AB	TDK
C2, C3	2	1 μF	CAP, CERM, 1 μF, 25 V,+/- 10%, X7R, 0603	06033C105KAT2A	AVX
C4	1	0.1 μF	CAP, CERM, 0.1 μF, 16 V,+/- 5%, X7R, 0603	0603YC104JAT2A	AVX
D1, D2, D3, D4	4	Rgb	LED, RGB, SMD	KRTB HFLP71.32	OSRAM
H1, H3, H5, H7	4		Standoff, Hex, 0.5"L #4-40 Nylon	1902C	Keystone
H2, H4, H6, H8	4		Machine Screw, Round, #4-40 x 1/4, Nylon, Philips panhead	NY PMS 440 0025 PH	B&F Fastener Supply
J1, J2, J3, TP4	4		Header, 100mil, 2x1, Gold, TH	TSW-102-07-G-S	Samtec
J4	1		Header, 100mil, 12x2, Gold, TH	TSW-112-07-G-D	Samtec
J5	1		Header, 100mil, 4x2, Gold, TH	TSW-104-07-G-D	Samtec
J6, J8	2		Header, 100mil, 3x1, Gold, TH	TSW-103-07-G-S	Samtec
J7	1		Header, 100mil, 5x2, Gold, R/A, TH	TSW-105-08-G-D-RA	Samtec
J9	1		Header, 100mil, 3x2, Gold, TH	TSW-103-07-G-D	Vishay-Dale
R1, R2, R3	3	4.7k	RES, 4.7 k, 5%, 0.1 W, 0603	CRCW06034K70JNEA	Yageo America
R4	1	3.57k	RES, 3.57 k, 1%, 0.1 W, 0603	RC0603FR-073K57L	Sullins Connector Solutions
SH1, SH2, SH3, SH4, SH5, SH6, SH7, SH8, SH9, SH10, SH11, SH12, SH13, SH14, SH15, SH16, SH17, SH18, SH19, SH20	20	1 × 2	Shunt, 100mil, Flash Gold, Black	SPC02SYAN	Sullins Connector Solutions
TP1, TP6, TP7, TP8	4		Terminal, Turret, TH, Double	1502-2	Keystone
TP2	1		Test Point, Miniature, Red, TH	5000	Keystone
TP3	1		Test Point, Miniature, Black, TH	5001	Keystone
U1	1		12-Channel I2C Constant Current RGB LED Driver, RUK0020B (WQFN-20)	LP5012RUKR	Texas Instruments

Table 2. Bill of Materials (BOM)

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