# DATA SHEET



## SWITCHING N-CHANNEL POWER MOSFET

### DESCRIPTION

NEC

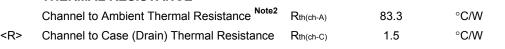
The  $\mu$ PA2723T1A is N-channel MOSFET designed for low side device of synchronous rectifier DC/DC converter.

#### FEATURES

- Low on-state resistance  $R_{DS(on)1}$  = 2.5 m $\Omega$  MAX. (Vgs = 10 V, ID = 17 A)
  - $R_{DS(on)2}$  = 3.5 m $\Omega$  MAX. (V<sub>GS</sub> = 4.5 V, I<sub>D</sub> = 17 A)
- Built-in gate protection diode
- Thin type surface mount package with heat spreader (8-pin HVSON)
- RoHS Compliant

#### ABSOLUTE MAXIMUM RATINGS (TA = 25°C, All terminals are connected.)

Drain to Source Voltage (Vgs = 0 V)	VDSS	30	V
Gate to Source Voltage (VDS = 0 V)	Vgss	±20	V
Drain Current (DC)	D(DC)	±33	Α
Drain Current (pulse) Note1	D(pulse)	±200	Α
Total Power Dissipation Note2	Pt1	1.5	W
Total Power Dissipation (PW =10 sec) Note2	PT2	4.6	W
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	-55 to +150	°C
Single Avalanche Current Note3	AS	33	Α
Single Avalanche Energy Note3	Eas	109	mJ
THERMAL RESISTANCE			



**Notes 1.** PW  $\leq$  10  $\mu$ s, Duty Cycle  $\leq$  1%

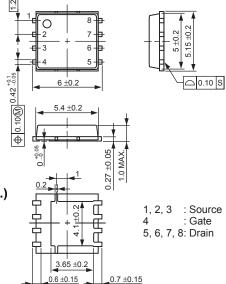
- 2. Mounted on a glass epoxy board of 25.4 mm x 25.4 mm x 0.8 mm
- 3. Starting T<sub>ch</sub> = 25°C, V<sub>DD</sub> = 15 V, R<sub>G</sub> = 25  $\Omega$ , V<sub>GS</sub> = 20  $\rightarrow$  0 V, L = 100  $\mu$ H
- **Remark** The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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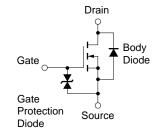
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#### EQUIVALENT CIRCUIT



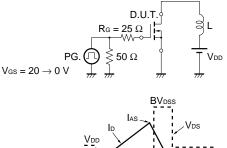
CHARACTERISTICS	SYMBOL	TEST CONDITIONS MIN. 1		TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V			10	μA
Gate Leakage Current	Igss	V <sub>GS</sub> = ±20 V, V <sub>DS</sub> = 0 V			±10	μA
Gate to Source Cut-off Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	1.0		2.5	V
Forward Transfer Admittance Note	yfs	Vds = 10 V, Id = 17 A	17			S
Drain to Source On-state Resistance Note	RDS(on)1	Vgs = 10 V, Id = 17 A		1.9	2.5	mΩ
	RDS(on)2	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 17 A		2.6	3.5	mΩ
Input Capacitance	Ciss	V <sub>DS</sub> = 10 V,		8100		pF
Output Capacitance	Coss	V <sub>GS</sub> = 0 V,		1290		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		610		pF
Turn-on Delay Time	t <sub>d(on)</sub>	Vdd = 15 V, Id = 17 A,		30		ns
Rise Time	tr	V <sub>GS</sub> = 10 V,		40		ns
Turn-off Delay Time	t <sub>d(off)</sub>	R <sub>G</sub> = 10 Ω		160		ns
Fall Time	tr			55		ns
Total Gate Charge	QG	V <sub>DD</sub> = 15 V,		64		nC
Gate to Source Charge	Q <sub>GS</sub>	V <sub>GS</sub> = 5 V,		19		nC
Gate to Drain Charge	Qgd	ID = 33 A		24		nC
Body Diode Forward Voltage <sup>Note</sup>	V <sub>F(S-D)</sub>	IF = 33 A, VGS = 0 V		0.76		V
Reverse Recovery Time	trr	IF = 33 A, VGS = 0 V,		55		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/ <i>µ</i> s		66		nC
Gate Resistance	Rg	f = 1 MHz		1.4		Ω

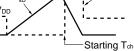
### ELECTRICAL CHARACTERISTICS (TA = 25°C, All terminals are connected.)

Note Pulsed

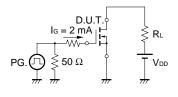
#### TEST CIRCUIT 1 AVALANCHE CAPABILITY

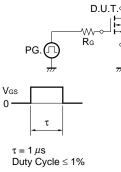
#### **TEST CIRCUIT 2 SWITCHING TIME**

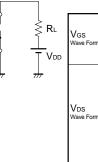


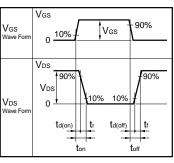


### TEST CIRCUIT 3 GATE CHARGE

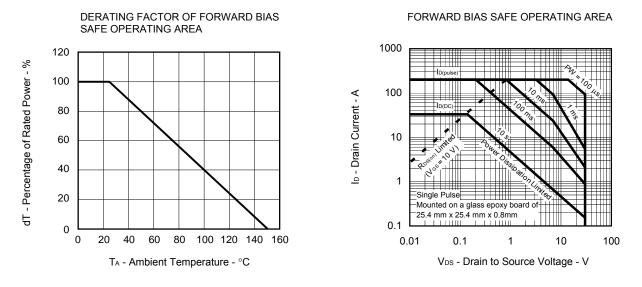




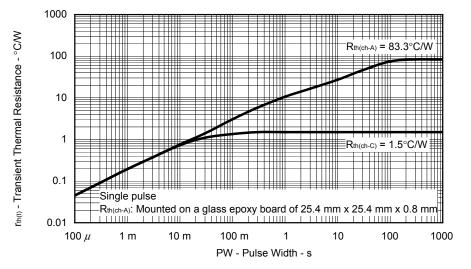




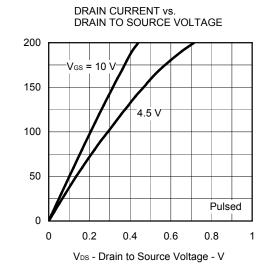
## TYPICAL CHARACTERISTICS (TA = 25°C)



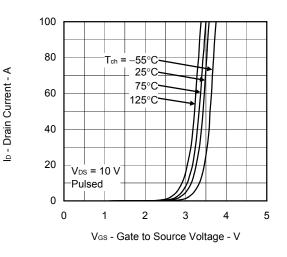
TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



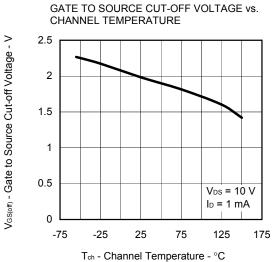


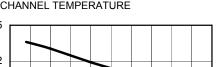


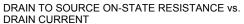


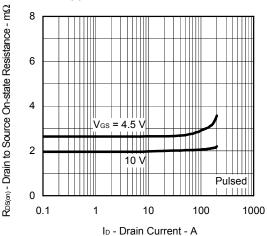


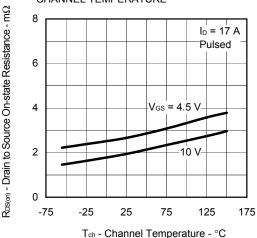
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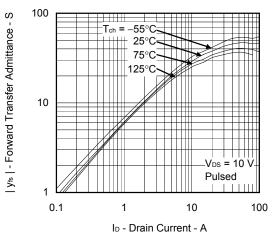




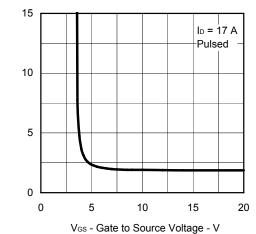


DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE

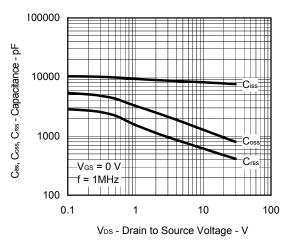
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE

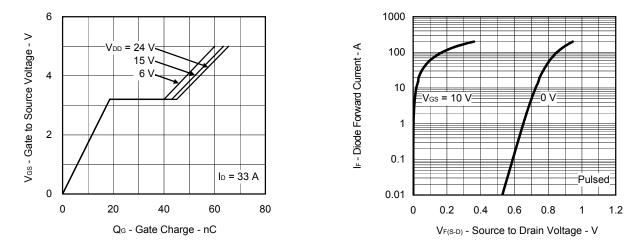


 $R_{DS(or)}$  - Drain to Source On-state Resistance -  $m\Omega$ 

# NEC

DYNAMIC INPUT/OUTPUT CHARACTERISTICS

SOURCE TO DRAIN DIODE FORWARD VOLTAGE



### ORDERING INFORMATION

PART NUMBER	LEAD PLATING	PACKING	PACKAGE	
μΡΑ2723Τ1Α-Ε1-ΑΖ <sup>Note</sup>				
μΡΑ2723Τ1Α-Ε2-ΑΖ <sup>Note</sup>	Sn-Bi		8-pin HVSON	
μΡΑ2723Τ1Α-Ε1-ΑΥ <sup>Note</sup>		Tape 3000 p/reel	0.10 g TYP.	
μΡΑ2723Τ1Α-Ε2-ΑΥ <sup>Νote</sup>	Pure Sn			

Note Pb-free (This product does not contain Pb in the external electrode.)

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