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April 2015

MJD45H11 PNP Epitaxial Silicon Transistor

Features

- General-Purpose Power and Switching such as Output or Driver Stages in Applications
- · D-PAK for Surface-Mount Applications
- · Lead-Formed for Surface Mount Application (No Suffix)
- · Fast Switching Speeds
- · Low Collector Emitter Saturation Voltage



1.Base 2.Collector 3.Emitter

Ordering Information

Part Number	Top Mark	Package	Packing Method
MJD45H11TF	MJD45H11	TO-252 3L (DPAK)	Tape and Reel
MJD45H11TM	MJD45H11	TO-252 3L (DPAK)	Tape and Reel

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value L		
V _{CEO}	Collector-Emitter Voltage	- 80	V	
V _{EBO}	Emitter-Base Voltage	- 5	V	
I _C	Collector Current (DC)	- 8	Α	
I _{CP}	Collector Current (Pulse)	- 16	Α	
В	Collector Dissipation (T _C = 25°C)	20	w	
P_{C}	Collector Dissipation (T _A = 25°C)	1.75	vv	
TJ	Junction Temperature		°C	
T _{STG}	Storage Temperature Range	- 55 to +150	°C	

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Electrical Characteristics

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
V _{CEO} (sus)	Collector-Emitter Sustaining Voltage ⁽¹⁾	$I_C = -30 \text{ mA}, I_B = 0$	- 80			V
I _{CEO}	Collector Cut-Off Current	$V_{CE} = -80 \text{ V}, I_{B} = 0$			- 10	μΑ
I _{EBO}	Emitter Cut-Off Current	$V_{EB} = -5 \text{ V}, I_{C} = 0$			- 50	μΑ
h _{FE}	DC Current Gain ⁽¹⁾	$V_{CE} = -1 V, I_{C} = -2 A$	60			
	Do Guirent Gain	V _{CE} = - 1 V, I _C = - 4 A	40			
V _{CE} (sat)	Collector-Emitter Saturation Voltage ⁽¹⁾	$I_C = -8 \text{ A}, I_B = -0.4 \text{ A}$			- 1	V
V _{BE} (sat)	Base-Emitter Saturation Voltage ⁽¹⁾	$I_C = -8 A$, $I_B = -0.8 A$			- 1.5	V
f _T	Current Gain Bandwidth Product	$V_{CE} = -10 \text{ V}, I_{C} = -0.5 \text{ A}$		40		MHz
C _{ob}	Collector Capacitance	V _{CB} = - 10 V, f = 1 MHz		230		pF
t _{ON}	Turn-On Time			135		ns
t _{STG}	Storage Time	$I_C = -5 \text{ A},$ $I_{B1} = -I_{B2} = -0.5 \text{ A}$		500		ns
t _F	Fall Time			100		ns

Note:

1. Pulse test: pulse width \leq 300 μ s, duty cycle \leq 2%.

Typical Performance Characteristics

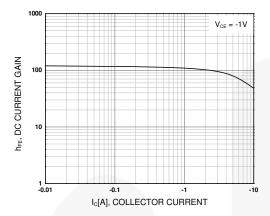


Figure 1. DC Current Gain

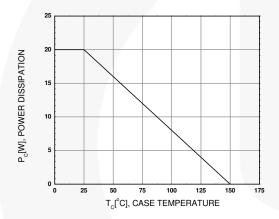


Figure 3. Power Derating vs T_C

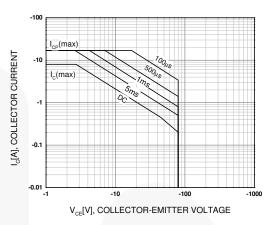


Figure 2. Safe Operating Area

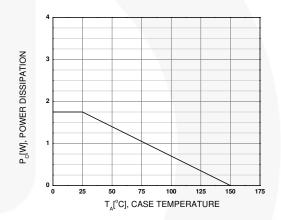
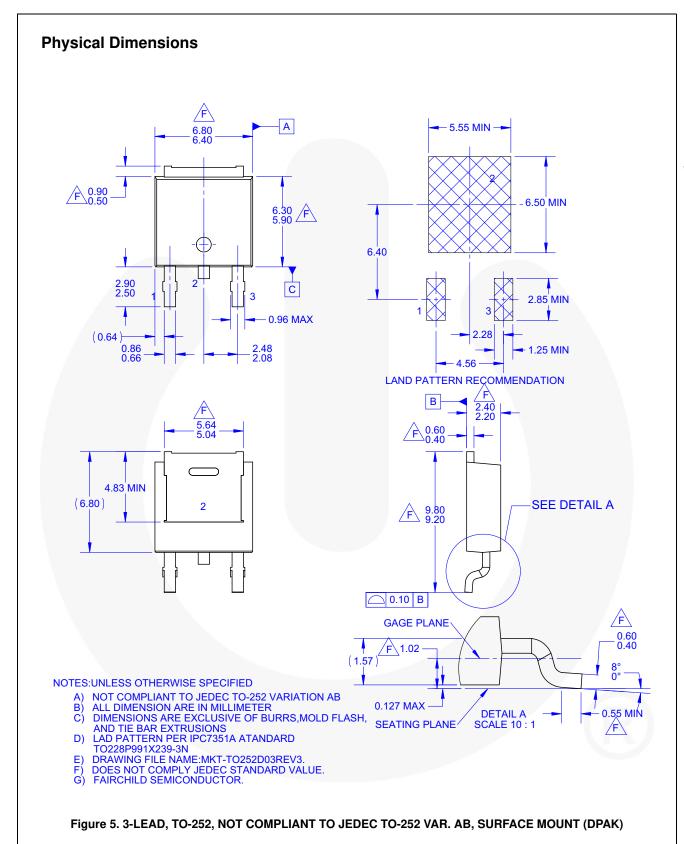


Figure 4. Power Derating vs. TA







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Definition of Terms

Definition of Terms				
Datasheet Identification	Product Status	Definition		
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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.		
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