

TYS -Low Profile SMT Power Inductor TYS3010 Series

FEATURES AND APPLICATIONS

Laird TYS series high current power inductors improve performance, reliability and power efficiency. A lower profile benefits consumer electronics, industrial and telecom design. Products feature extremely low DCR with greater efficiency and enable a large current in a small size. Inductors are of magnetic shielding and wire wound construction and perform in operating temperatures ranging from -40 C to 125 C including self-heating rise in temperature.

FEATURES

- Magnetic shielded structure
- Low DCR and high efficiency
- Low profile and small size
- Ferrite core with high saturation

APPLICATIONS

- DC-DC Converter and Power Suppliers
- LCD TV'S and Gaming Console
- Tablet, Notebooks, Servers and Printers
- Networking and Data storage
- GPS, Set-top-box and Base stations
- Smart meters and Medical instruments

PART NUMBER EXPLANATION



ΤΥ	3010	4 R 7	Μ	- 10
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Product series code

Product size code Inductance value code (i.e. 4R7: 4.7 µH) Tolerance % Standard (i.e. M: ± 20%) Catalog P.N

ELECTRICAL SPECIFICATIONS

- Tolerance: M: ±20% or N: ±30%
- Inductance tested at 100kHz, 1.0Vrms
- Heat Rated Current (Irms) is defined based on temperature rise approximate 40°C (ambient temperature 25±5°C)
- Saturation Current (Isat) is the DC current at which the inductance drops off approximately 30% from its value without current. (ambient temperature 25±5°C)
- Operating temperature range: -40°C~+125°C (including self-heating temperature rise)
- Storage temperature range (packaging conditions): -10°C~+40°C and RH 70%(MAX.)

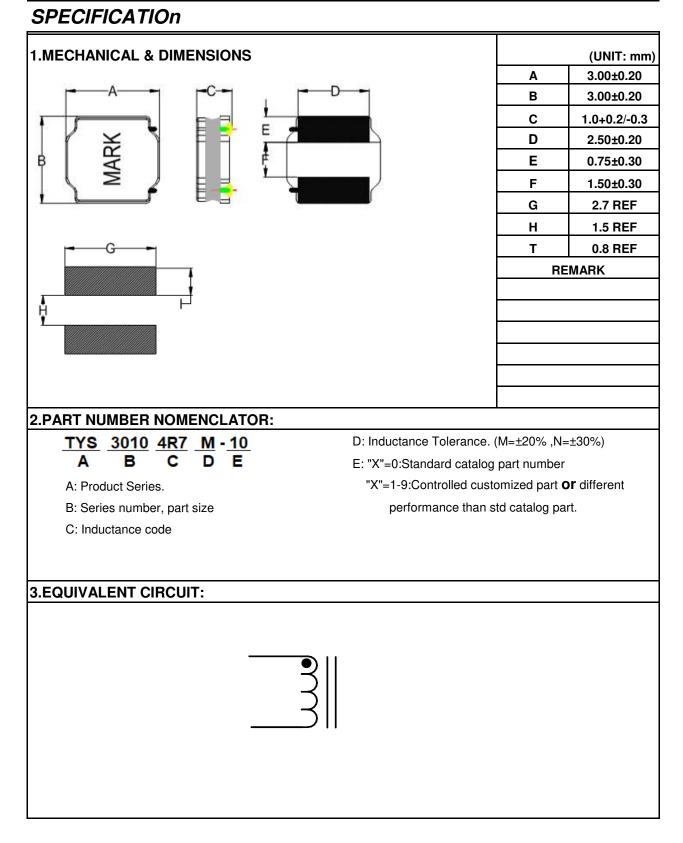
Note: Heat Rated Current (Irms) is tested on a typical PCB and apply a constant current in still air. The temperature rise is dependent on the application system condition including PCB PAD pattern, trace width and thickness and adjacent components etc. It's suggested to verify the temperature rise of the component under the real operation application conditions.



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Shielded Power Inductor

www.laird.com TYS3010 Series Rev: A



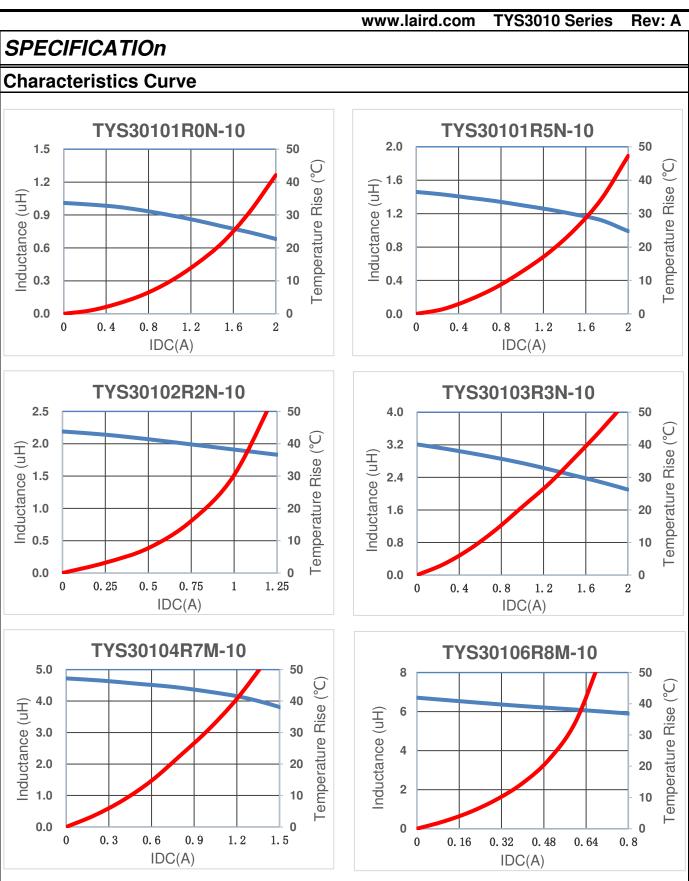


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SPECIFICA	TIOn					
PART NUMBER	INDUCTANCE (uH)	Irms(A) Typ.	Isat(A) Typ.	DCR(mΩ) TYP	DCR(mΩ) Max	SRF MHz
TYS30101R0N-10	1.00	1.45	1.40	65.00	85.0	180
TYS30101R5N-10	1.50	1.30	1.27	80.00	104.0	120
TYS30102R2N-10	2.20	1.09	1.15	110.00	143.0	100
TYS30103R3N-10	3.30	0.96	0.97	145.00	189.0	74
TYS30104R7M-10	4.70	0.77	0.75	225.00	293.0	59
TYS30106R8M-10	6.80	0.66	0.55	305.00	397.0	42
TYS3010100M-10	10.00	0.58	0.55	400.00	520.0	39
TYS3010150M-10	15.00	0.47	0.42	610.00	793.0	30
TYS3010220M-10	22.00	0.38	0.35	930.00	1209.0	28
TYS3010330M-10	33.00	0.30	0.29	1550.00	2015.0	18
TYS3010470M-10	47.00	0.26	0.22	1950.00	2535.0	18
GENERAL SPE	CIFICATION:					•
• Tolerance: M:	±20% or N: ±30%	0				
 Inductance tes 	ted at 100KHz, C).25Vrms				
			emperature rise	approximate 40°	С	
	perature 25±5°C)				•	
• •			nich the inductan	ice drops off app	rovimately 30%	
		(ambient tempe				
		•		ting temperature	riso)	
				and RH 70%(MA		
 storage tempe 	rature range (pa		115J 10 C - +40°C	. ατιά κπ 70%(IMA		

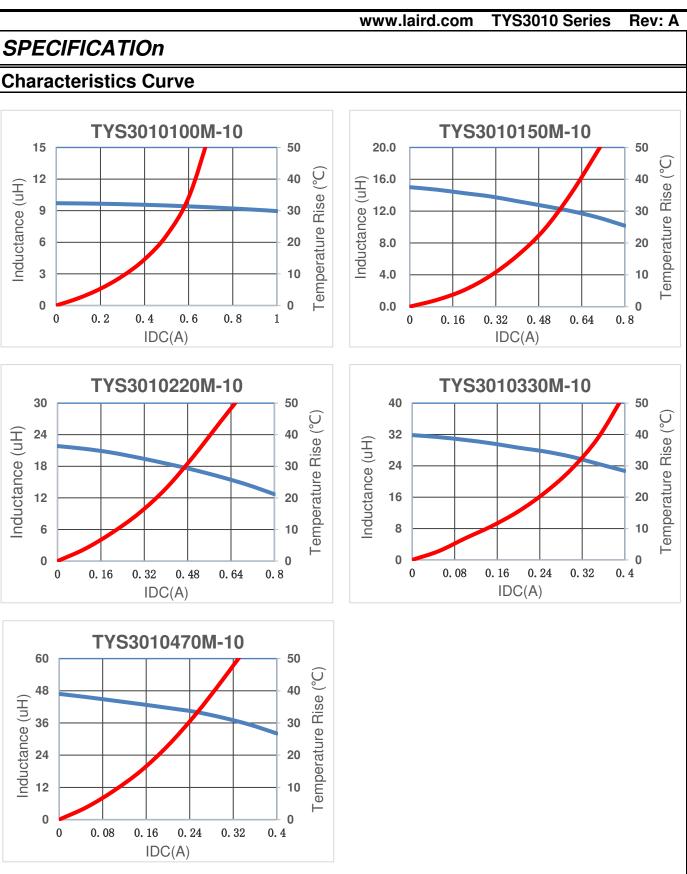
Laird Steward"

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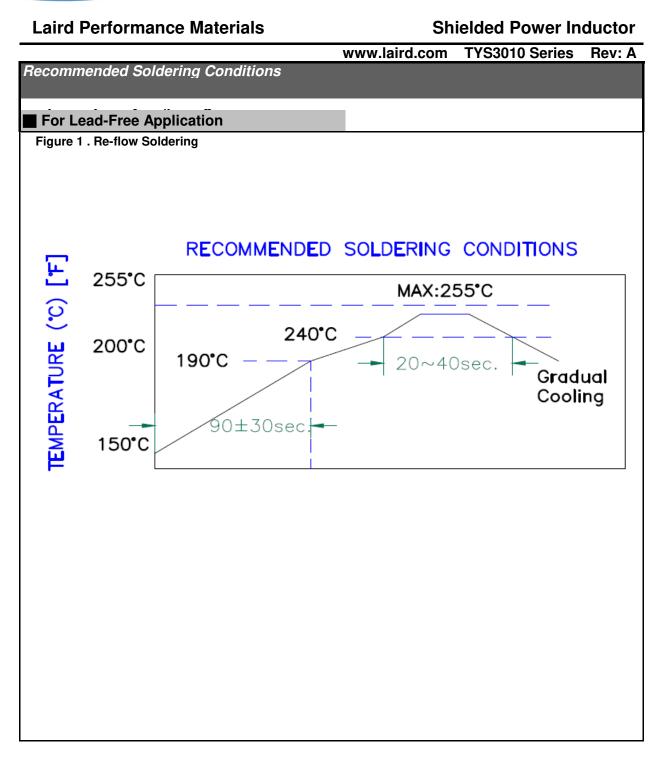


Laird Steward

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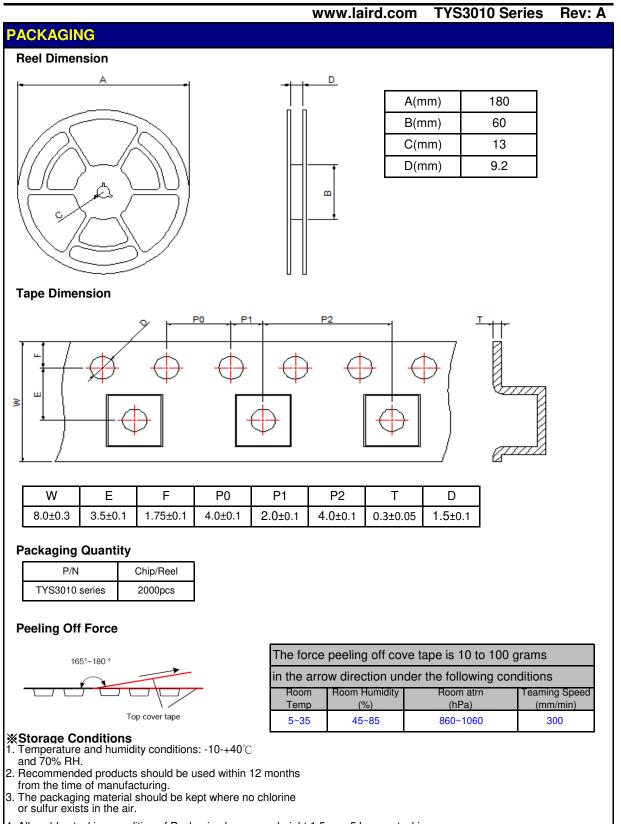
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Reliability and Te	sting Conditions / Pin Type Po						
SMD series(Consumer)							
Item	Reference	Additional Requirements					
Operating temperature range	-40° C ~ +125 $^{\circ}$ C (Including self-temperature rise)						
Storage temperature and humidity range	-10 $^\circ\!\mathrm{C}$ to +40 $^\circ\!\mathrm{C}$, 70% RH Max						
High Temperature Exposure (Storage)	MIL-STD-202 Method 108	85±2°C, 168+24hours					
Temperature Cycling	JESD22 Method JA-104	-40°C→+85, transforming interval:20s, 100cycles					
Operational Life	MIL-PRF-2	$85\pm^\circ\!\!\!^\circ C$, 168+24hours Apply maximum rated voltage and current according part drawing					
External Visual	MIL-STD-883 Method 2009	Inspect device construction, marking and workmanship. Electrical Test not required.					
Physical Dimension	JESD22 Method JB-100	Verify physical dimensions to the applicable device detail specification. Note: User(s) and Suppliers spec. Electrical Test not required					
Vibration	MIL-STD-202 Method 204	10~55Hz,1.5mm, 2 hours in each 3mutually perpendicular directions (total of 6 hours)					
Resistance to Soldering Heat	MIL-STD-202 Method 210	1. Max. 260±5°C,10±1s, 2 times 2.Solder Composition: Sn/3Ag/0.5Cu					
Solderability	J-STD-002	245±5℃, 5±1sec, Solder: Sn/3.0Ag/0.5Cu					
Electrical Characterization	Print Spec	Parametrically test per lot and sample size requirements, summary to show Min, Max, Mean and Standard deviation at room as well as Min and Max Operating temperatures					
Board Flex	AEC-Q200-005	2mm,30±1s					
Terminal Strength(SMD)	AEC-Q200-006	10N, 5S, X,Y direct					



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4. Allowable stacking condition of Packaging box: max height 1.5m or 5 boxes stacking