

40 V, 5 A low VF MEGA Schottky barrier rectifier 26 July 2016 Pro

Product data sheet

### 1. General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a CFP15 (SOT1289) power and flat lead Surface-Mounted Device (SMD) plastic package.

### 2. Features and benefits

- Average forward current: I<sub>F(AV)</sub> ≤ 5 A
- Reverse voltage:  $V_R \le 40 V$
- Extremely low forward voltage
- High power capability due to clip-bonding technology and heat sink
- Small and thin SMD power plastic package, typical height 0.78 mm
- AEC-Q101 qualified

### 3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Freewheeling application
- Reverse polarity protection
- Low power consumption application

### 4. Quick reference data

Table 1. Quick					_		
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I <sub>F(AV)</sub>	average forward current	square wave; $\delta$ = 0.5 ; f = 20 kHz; T <sub>sp</sub> ≤ 158 °C		-	-	5	A
V <sub>R</sub>	reverse voltage	T <sub>j</sub> = 25 °C		-	-	40	V
V <sub>F</sub>	forward voltage	$I_{\text{F}}$ = 5 A; $t_{\text{p}}$ $\leq $ 300 $\mu \text{s};$ $\delta \leq $ 0.02 $$ ; $T_{\text{j}}$ = 25 $^{\circ}\text{C}$		-	475	520	mV
I <sub>R</sub>	reverse current	$V_R$ = 10 V; $T_j$ = 25 °C; pulsed	[1]	-	6	52	μA
		$V_R$ = 40 V; $T_j$ = 25 °C; pulsed	[1]	-	32	120	μA

[1] Very short test pulse to prevent junction self heating

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# 5. Pinning information

Table 2. F	Pinning inf	formation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	A	anode		
2	A	anode		
3	К	cathode	(2) CFP15 (SOT1289)	

# 6. Ordering information

Table 3. Ordering infor	mation					
Type number	Package					
	Name	Description	Version			
PMEG040V050EPD	CFP15	plastic, thermal enhanced ultra thin SMD package; 3 leads; body: 5.8 x 4.3 x 0.78 mm	SOT1289			

### 7. Marking

Table 4. Marking codes	
Type number	Marking code
PMEG040V050EPD	040V U05E

40 V, 5 A low VF MEGA Schottky barrier rectifier

### 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>R</sub>	reverse voltage	T <sub>j</sub> = 25 °C		-	40	V
l <sub>F</sub>	forward current	T <sub>sp</sub> ≤ 154 °C; δ = 1		-	7	А
I <sub>F(AV)</sub>	average forward current	square wave; $\delta$ = 0.5 ; f = 20 kHz; T <sub>sp</sub> ≤ 158 °C		-	5	A
I <sub>FSM</sub>	non-repetitive peak forward current	square wave; $t_p$ = 8 ms; $T_{j(init)}$ = 25 °C		-	120	A
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	1.66	W
			[2]	-	2.15	W
Tj	junction temperature			-	175	°C
T <sub>amb</sub>	ambient temperature			-55	175	°C
T <sub>stg</sub>	storage temperature			-65	175	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

### 9. Thermal characteristics

#### Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	-	[1][2]	-	-	90	K/W
			[1][3]	-	-	70	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		[4]	-	-	3	K/W

[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P<sub>R</sub> are a significant part of the total power losses.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

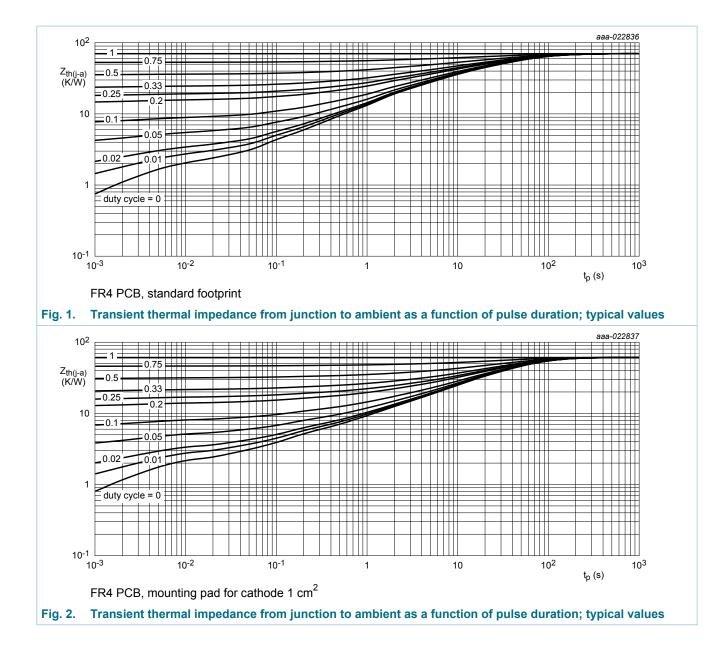
[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

[4] Soldering point of cathode tab.

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# PMEG040V050EPD

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### **10. Characteristics**

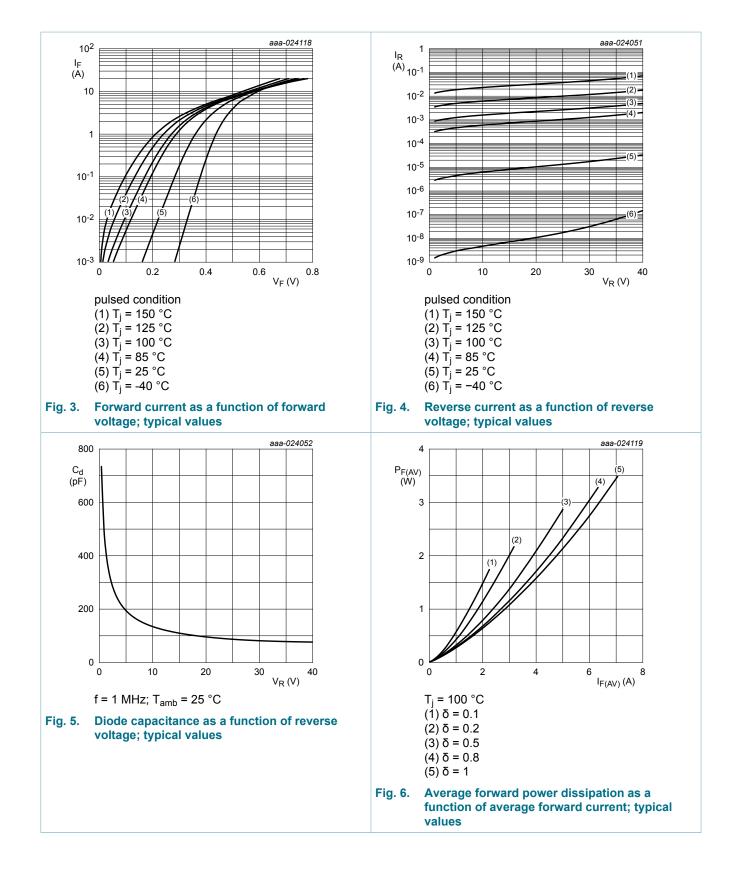
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>(BR)R</sub>	reverse breakdown voltage	$I_R$ = 3 mA; $T_j$ = 25 °C; pulsed	[1]	40	-	-	V
V <sub>F</sub> forward voltag	forward voltage	$\begin{array}{l} {\sf I}_{\sf F} = 0.1 \; {\sf A};  t_p \leq \; 300 \; \mu {\sf s};  \bar{\sf 0} \leq \; 0.02 \; \; ; \\ {\sf T}_j = 25 \; ^\circ {\sf C} \end{array}$		-	285	320	mV
		$I_{F}$ = 1 A; $t_{p}$ $\leq~$ 300 $\mu$ s; $\delta$ $\leq~$ 0.02 $~;$ $T_{j}$ = 25 $^{\circ}C$		-	360	420	mV
		$\begin{array}{l} I_{\text{F}} = 1.5 \; \text{A;} \; t_{\text{p}} \leq \; 300 \; \mu \text{s;} \; \bar{\delta} \leq \; 0.02 \; \; ; \\ T_{\text{j}} = 25 \; ^{\circ}\text{C} \end{array}$		-	380	435	mV
		$I_{\text{F}}$ = 2 A; $t_{p}$ $\leq~$ 300 $\mu\text{s};$ $\delta$ $\leq~$ 0.02 $;$ $T_{j}$ = 25 $^{\circ}\text{C}$		-	395	-	mV
		$I_{\text{F}}$ = 3 A; $t_{p}$ $\leq~$ 300 $\mu \text{s};$ $\delta$ $\leq~$ 0.02 $;$ $T_{j}$ = 25 $^{\circ}\text{C}$		-	425	490	mV
		$I_{\text{F}}$ = 5 A; $t_{p}$ $\leq~$ 300 $\mu\text{s};$ $\delta$ $\leq~$ 0.02 $$ ; $T_{j}$ = 25 $^{\circ}\text{C}$		-	475	520	mV
		$I_{\text{F}}$ = 5 A; $t_{p}$ $\leq~$ 300 $\mu\text{s};\delta\leq~$ 0.02 $\;;$ $T_{j}$ = -40 $^{\circ}\text{C}$		-	515	-	mV
		$I_{F}$ = 5 A; $t_{p}$ $\leq~$ 300 $\mu$ s; $\delta$ $\leq~$ 0.02 $$ ; $T_{j}$ = 125 $^{\circ}C$		-	415	-	mV
I <sub>R</sub>	reverse current	$V_R$ = 10 V; $T_j$ = 25 °C; pulsed	[1]	-	6	52	μA
		$V_R$ = 30 V; $T_j$ = 25 °C; pulsed	[1]	-	17	-	μA
		$V_R$ = 40 V; $T_j$ = 25 °C; pulsed	[1]	-	32	120	μA
		V <sub>R</sub> = 40 V; T <sub>j</sub> = 125 °C; pulsed	[1]	-	18	-	mA
C <sub>d</sub>	diode capacitance	V <sub>R</sub> = 1 V; f = 1 MHz; T <sub>j</sub> = 25 °C		-	395	-	pF
		V <sub>R</sub> = 4 V; f = 1 MHz; T <sub>j</sub> = 25 °C		-	210	-	pF
		V <sub>R</sub> = 10 V; f = 1 MHz; T <sub>j</sub> = 25 °C		-	130	-	pF
t <sub>rr</sub>	reverse recovery time step recovery	$I_{F}$ = 0.5 A; $I_{R}$ = 0.5 A; $I_{R(meas)}$ = 0.1 A; T <sub>j</sub> = 25 °C		-	13	-	ns
	reverse recovery time ramp recovery	dI <sub>F</sub> /dt = 200 A/µs; T <sub>j</sub> = 25 °C; I <sub>F</sub> = 6 A; V <sub>R</sub> = 26 V		-	11	-	ns

[1] Very short test pulse to prevent junction self heating

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# PMEG040V050EPD

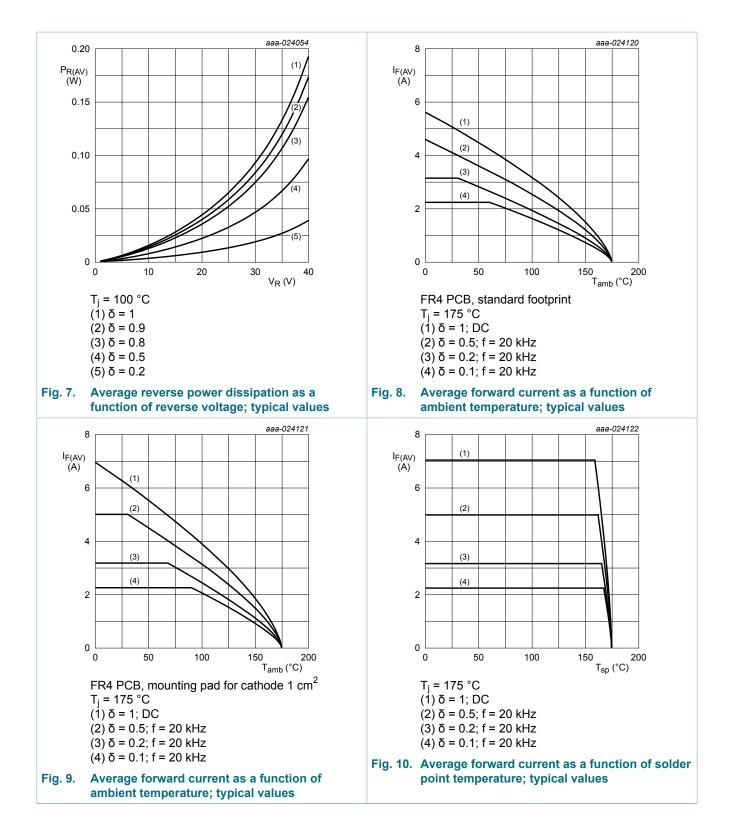
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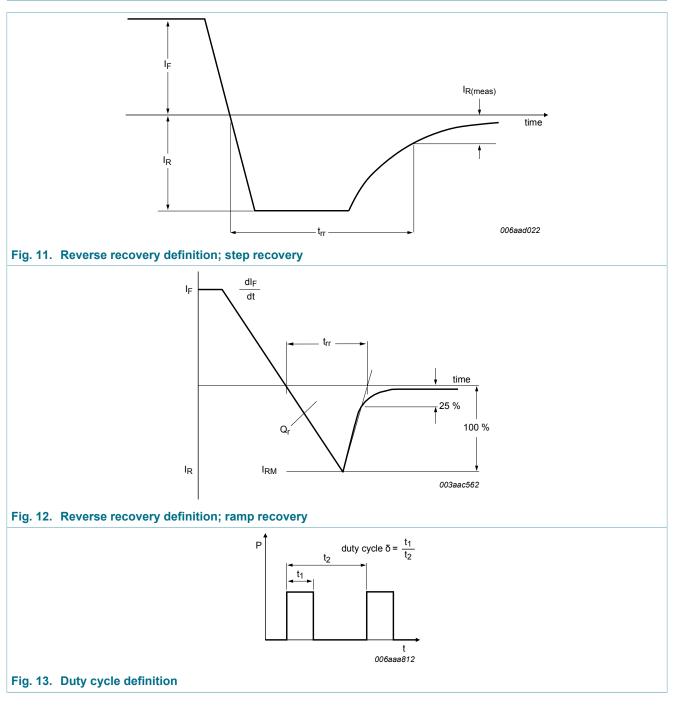
# PMEG040V050EPD

#### 40 V, 5 A low VF MEGA Schottky barrier rectifier



#### 40 V, 5 A low VF MEGA Schottky barrier rectifier

### 11. Test information



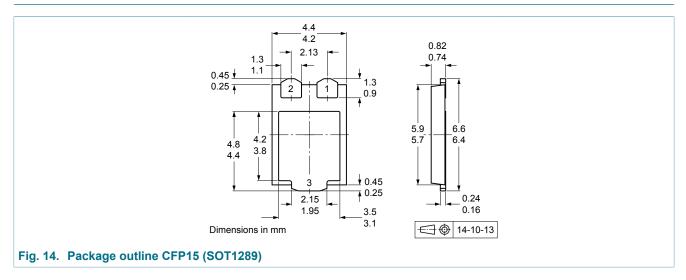
The current ratings for the typical waveforms are calculated according to the equations:  $I_{F(AV)} = I_M \times \delta$  with  $I_M$  defined as peak current,  $I_{RMS} = I_{F(AV)}$  at DC, and  $I_{RMS} = I_M \times \sqrt{\delta}$  with  $I_{RMS}$  defined as RMS current.

#### 40 V, 5 A low VF MEGA Schottky barrier rectifier

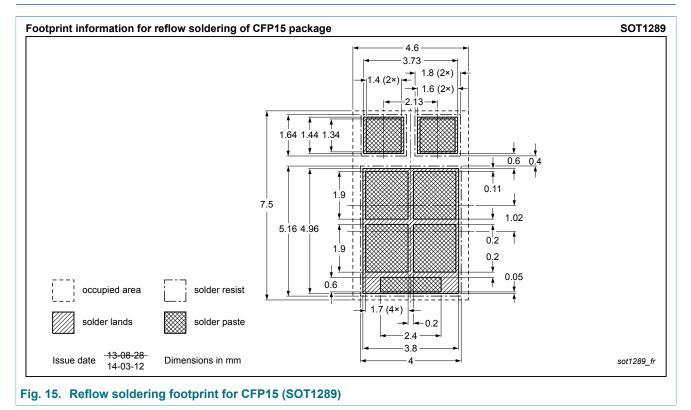
#### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

### 12. Package outline



### 13. Soldering





### 40 V, 5 A low VF MEGA Schottky barrier rectifier

# 14. Revision history

Table 8. Revision history				
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMEG040V050EPD v.1	20160726	Product data sheet	-	-

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### 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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