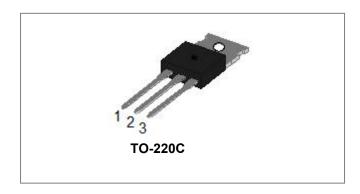


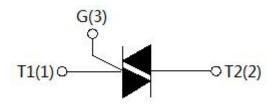




# SST138 Series 12A TRIACs







# **Description**

SST138 series triacs with low holding and latching current are especially recommended for use on middle and small resistance type power load.

# **Maximum Ratings:**

Characteristics	Symbol	Condition	Max.	Units
Storage junction temperature range	T <sub>stg</sub>	-	-40-150	$^{\circ}\!\mathbb{C}$
Operating junction temperature range	Tj	-	-40-125	$^{\circ}\mathbb{C}$
Repetitive peak off-state voltage( $T_j$ =25 $^{\circ}$ C)	$V_{DRM}$	-	600/800	V
Repetitive peak reverse voltage(T <sub>j</sub> =25°C)	$V_{RRM}$	-	600/800	V
Non repetitive surge peak Off-state voltage	V <sub>DSM</sub>	-	V <sub>DRM</sub> + 100	V
Non repetitive peak reverse voltage	V <sub>RSM</sub>	-	V <sub>RRM</sub> + 100	V
RMS on-state current	I <sub>(TRMS)</sub>	TO-220C(T <sub>C</sub> =110℃)	12	Α
Non repetitive surge peak on-state current (tp=20ms)	Ітѕм	-	95	Α
I <sup>2</sup> t value for fusing (tp=10ms)	l <sup>2</sup> t	-	45	A <sup>2</sup> s
Critical rate of rise of on-state current	dI/dt	I - II -III	50	- A/μs
$(I_G = 2 \times I_{GT})$		IV	10	
Peak gate current	I <sub>GM</sub>	-	2	Α
Average gate power dissipation	P <sub>GM</sub>	-	0.5	W
Peak gate power	$P_{G(AV)}$	-	5	W

<sup>•</sup> http://www.smc-diodes.com - sales@ smc-diodes.com •







# **Electrical Characteristics**(Tj=25℃ unless otherwise specified)

Symbol	Test Condition	Quadrant		Value	Unit	
I <sub>GT</sub>		I - II -III	MAX	10	A	
	V <sub>D</sub> =12V R <sub>L</sub> =33Ω	IV	IVIAA	25	mA	
V <sub>GT</sub>		ALL	MAX	1.5	V	
$V_{GD}$	$V_D=V_{DRM}T_j=125^{\circ}C$ R <sub>L</sub> =3.3K $\Omega$	ALL	MIN	0.2	V	
I <sub>L</sub> I <sub>G</sub> =1.2I <sub>GT</sub>		I - III	MAX	30	mA	
	I <sub>G</sub> =1.2I <sub>GT</sub>	II - IV	IVIAX	40		
lн	I <sub>T</sub> =100mA		MAX	25	mA	
dV/dt	V <sub>D</sub> =2/3V <sub>DRM</sub> Gate Open T <sub>j</sub> =125	${\mathbb C}$	MIN	50	V/µs	

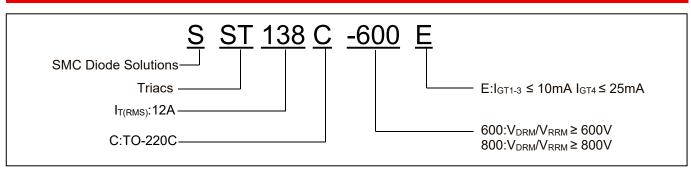
#### **Static Characteristics**

Symbol	Condition	Max.	Units
$V_{TM}$	I <sub>τм</sub> =15A tp=380μs,Tj=25℃	1.6	V
I <sub>DRM</sub>	$V_D=V_{DRM} V_R=V_{RRM}$ , $Tj=25$ °C	5	μA
I <sub>RRM</sub>	V <sub>D</sub> =V <sub>DRM</sub> V <sub>R</sub> =V <sub>RRM</sub> , Tj=125°C	1	mA

### **Thermal Resistances**

Symbol	Condition		Value	Units
Rth(j-c)	Junction to case(AC)	TO-220AC	1.4	°C/W

## **Ordering Information**



Device	Package	Shipping	
SST138C-600E SST138C-800E	TO-220C	50pcs/ Tube	

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  - http://www.smc-diodes.com sales@ smc-diodes.com •







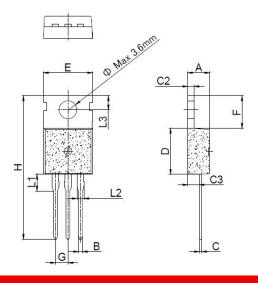
# **Marking Diagram**



Where XXXXX is YYWWL

SST138C-600E = Part name
YY = Year
WW = Week
L = Lot Number

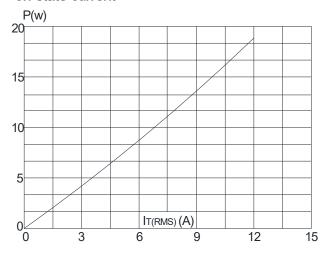
### **Mechanical Dimensions TO-220C**



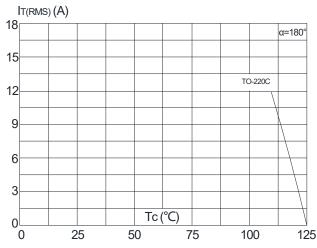
SYMBOL	Millimeters			Inches		
STMBOL	Min.	Тур.	Max.	Min.	Тур.	Max.
А	4.40		4.60	0.173		0.181
В	0.70		0.90	0.028		0.035
С	0.45		0.60	0.018		0.024
C2	1.23		1.32	0.048		0.052
C3	2.20		2.60	0.087		0.102
D	8.90		9.90	0.350		0.390
E	9.90		10.3	0.39		0.406
F	6.30		6.90	0.248		0.272
G		2.54			0.1	
Н	28.0		29.8	1.102		1.173
L1		3.39			0.133	
L2	1.14		1.70	0.045		0.067
L3	2.65		2.95	0.104		0.116
ф		3.6			0.142	

### **Ratings and Characteristics Curves**

**FIG.1:** Maximum power dissipation versus RMS on-state current



**FIG.2:** RMS on-state current versus case temperature



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FIG.3: Surge peak on-state current versus number of cycles

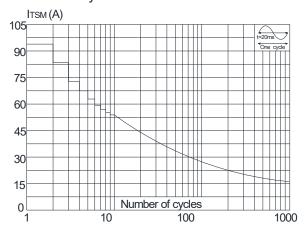


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp<20ms, and corresponding value of  $l^2t(I - II - III - III : dI/dt < 50A/\mu s;$ IV: $dI/dt < 10A/\mu s$ )

ITSM (A),  $I^2 t$  (A<sup>2</sup> s)

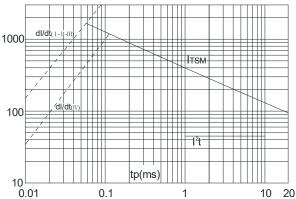


FIG.7: Relative variations of holding current versus junction temperature

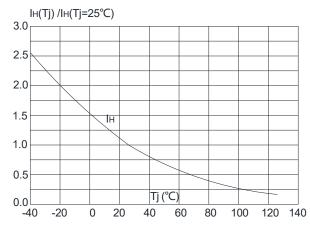


FIG.4: On-state characteristics (maximum values)

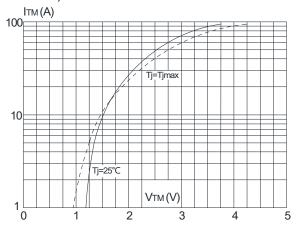


FIG.6: Relative variations of gate trigger current versus junction temperature

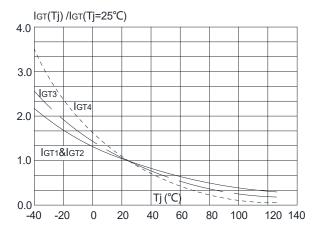
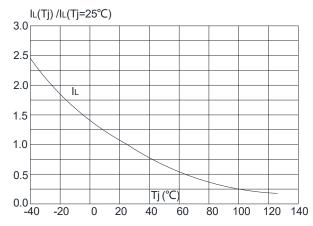


FIG.8: Relative variations of latching current versus junction temperature



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