



AS333

1.8V, MICROPOWER CMOS ZERO-DRIFT OPERATIONAL AMPLIFIERS

Description

The AS333 is a high precision operational amplifier designed with chopping stabilization technique which eliminates 1/f noise, and the cross over distortion presented in most RRIO amplifiers.

The AS333, RRIO amplifier (rail-to-rail input and output), provides not only maximum output voltage swing capability but also an extended 100mV common-mode voltage beyond the supply rail. The device is fully specified to operate from 1.8V to 5.5V single supply, or ±0.9V and ±2.5V dual supply applications.

The device features a good speed/power consumption ratio, offering 350kHz gain bandwidth while consuming only 17 μ A quiescent current. With the low input offset voltage 8 μ V and zero-drift offset voltage 0.02 μ V/°C, it is ideal for applications that require precision, especially for low power and high precision ones.

The AS333 is available in SOT25 and SOT353 packages, and is specified for operation from -40°C to +125°C among all supply voltages. The wide temperature ranges and high ESD tolerance facilitate their use in harsh applications.

Features

Low Input Offset Voltage: 8µV (typ)

Zero Drift: 0.02μV/°C (typ)

0.01Hz to 10Hz Noise: 1.1μV_{PP}

Low Quiescent Current: 17μA (typ)

Supply Voltage: 1.8V to 5.5V

Rail-to-Rail Input and Output

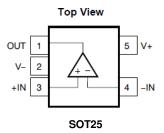
V_{CM}: 100mV Beyond Supply Rail @ V_{CC} = 1.8V to 5.5V

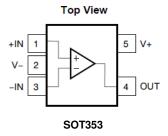
Unity Gain Stable up to 200pF C-Load

Gain Bandwidth: 350kHz

- Slew Rate 0.12V/µs (typ)
- Operation Ambient Temperature Range: -40°C to +125°C
- ESD Protection JESD 22, 4000V HBM (A114)
- SOT25 and SOT353 Packages
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

Pin Assignments





Applications

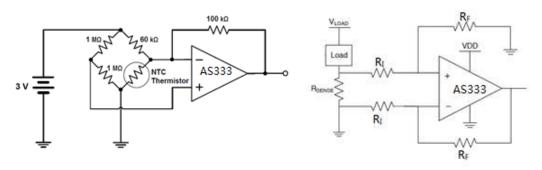
- Battery-powered instruments
- Handheld test equipment
- Medical instrumentation
- Low voltage current sensing
- Sensor signal conditioning
- Sensors interfaces
- CO detectors, pressure sensors, smoke alarms, pulse blood oximeters, glucose meters

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



Typical Application



Thermistor Measurement

Low-Side Current Monitor

Pin Descriptions

Pin Name	Package Name		I/O	Function
Pili Name	SOT25	SOT353	1/0	Function
+IN	3	1	I	Noninverting Input
-IN	4	3	I	Inverting Input
OUT	1	4	0	Output
V+	5	5	_	Positive Power Supply
V-	2	2	_	Negative Power Supply

Absolute Maximum Ratings (Note 4)

Symbol	Parameter	Rati	Rating	
V _S = V+ - V-	Supply Voltage Range	6.	6.5	
V_{-IN} / V_{+IN}	Signal Input Terminals (Note 5)	V 0.3V to	V+ + 0.3V	V
	Signal Input Terminals (Note 5)	-1 to	+1	mA
_	Output Short-Circuit (Note 6)	Contin	nuous	mA
T _{STG}	Storage Temperature	-65 to	+150	°C
TJ	Maximum Junction Temperature	+150		°C
T _{LEAD}	Lead Temperature (Soldering, 10 Seconds)	+260		°C
Б	Junction-to-Ambient Thermal Resistance	SOT25	207	°C/W
$R_{ heta JA}$	Junction-to-Ambient mermal nesistance	SOT353	298	°C/W
В	Junction-to-Case Thermal Resistance	SOT25	66	°C/W
$R_{ heta JC}$	Junction-to-Case Thermal Resistance	SOT353	76	°C/W
ESD HBM	Human Body Model ESD Protection	4		kV
ESD CDM	Charged-Device Model ESD Protection	1		kV

Notes:

- 4. Stresses greater than those listed under *Absolute Maximum Ratings* can cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Conditions* is not implied. Exposure to Absolute Maximum Ratings for extended periods can affect device reliability.

 5. Input terminals are diode-clamped to the power-supply rails. Input signals that can swing more than 0.3V beyond the supply rails should be current
- limited to 10mA or less.
- 6. Short-circuit to ground.



Recommended Operating Conditions (@ $T_A = +25$ °C, unless otherwise specified.)

Symbol	Parameter	Rating	Unit
V _S = V+ - V-	Supply Voltage Range	1.8 to 5.5	V
T _A	Operating Ambient Temperature Range	-40 to +125	°C

$\textbf{Electrical Characteristics} \ (@T_A = +25^{\circ}C,\ V_S = 5.0V,\ R_L = 10 \text{k}\Omega \ \text{connected to} \ V_{OUT} = V_S/2,\ V_{CM} = V_S/2,\ unless \ \text{otherwise specified.})$

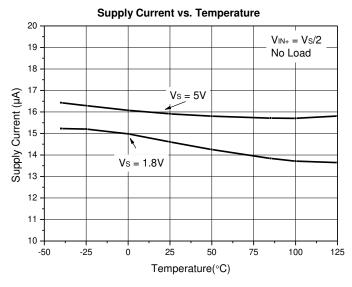
Symbol	Parameter	Con	ditions	Min	Тур	Max	Unit
Offset Voltag	le			I		1	
Vos	Input Offset Voltage	V _S = 5V		_	8	22	μV
	Leave Office Male as Data (Nate 7)	$T_A = -40$ °C to +85°C		_	0.02	0.1	μV/°C
$\Delta V_{OS}/\Delta T$	Input Offset Voltage Drift (Note 7)	$T_A = -40^{\circ}C \text{ to } +12^{\circ}$	25°C	_	_	0.2	μV/°C
PSRR	Power-Supply Rejection Ratio	V _S = 1.8V to 5.5V +125°C		_	1	10	μV/V
	Long-Term Stability	_			(Note 7)		μV
	Channel Separation, DC	_		_	0.1	_	μV/V
Input Bias Cu	urrent						
	Input Bias Current	$T_A = +25$ °C			±70	±200	
l _Β	Input bias Current	$T_A = -40^{\circ}C \text{ to } +12^{\circ}$	25°C	_	±400	_	pА
I _{OS}	Input Offset Current	_		_	±140	±400	
Noise							
V	Input Voltage Noise	f = 0.01Hz to 1Hz	<u>,</u>		0.3	_	\/
V_N	Input voltage Noise	f = 0.1Hz to 10Hz	!	_	1.1	_	μV_{PP}
I _N	Input Current Noise	f = 10Hz	f = 10Hz		100	_	fA/√ Hz
Input Voltage)						
V_{CM}	Common-Mode Voltage Range	_		(V-) - 0.1	_	(V+) + 0.1	V
CMRR	Common-Mode Rejection Ratio	$(V-) - 0.1V < V_{CM} < (V+) + 0.1V$ T _A = -40°C to +125°C		90	120	_	dB
Input Capacit	tance						
_	Differential	_		_	2	_	pF
_	Common-Mode	_		_	4	_	pF
Open-Loop G	Sain						
A _{OL}	Open-Loop Voltage Gain		$(V-) + 100 \text{mV} < V_O < (V+) - 100 \text{mV}$ $R_L = 10 \text{k}\Omega$, $T_A = -40 ^{\circ}\text{C}$ to $+125 ^{\circ}\text{C}$		130	_	dB
Frequency Re	esponse						
GBW	Gain-Bandwidth Product	C _L = 100pF		_	350	_	kHz
SR	Slew Rate	G = +1		_	0.12	_	V/µs
Output							
		Positive Rail	$T_A = +25^{\circ}C$		30	50	
	Voltage Output Swing from Bail	$R_L = 10k\Omega$	$T_A = -40^{\circ}C$ to $+125^{\circ}C$	_	_	70	mV
_	Voltage Output Swing from Rail	Negative Rail	$T_A = +25^{\circ}C$	_	10	50	111 V
		$R_L = 10k\Omega$	$T_A = -40$ °C to +125°C	_	_	70	
,	Short Circuit Current	Source Current		_	5	_	mA
I _{SC}	Short-Circuit Current	Sink Current		_	25	_	mA
_	Open-Loop Output Impedance	$f = 350kHz, I_O = 0$)A	_	2		kΩ
Power Supply	у						
Vs	Specified Voltage Range	_		1.8	_	5.5	٧
IQ	Quiescent Current		$I_O = 0A$, $T_A = +25^{\circ}C$ $I_O = 0A$, $T_A = -40^{\circ}C$ to $+125^{\circ}C$		17	28 30	μA
ton	Turn-On Time		0 10 + 120 U	<u> </u>	100	_	116
ton	Tain On Time	$V_S = 5V$			100		μs

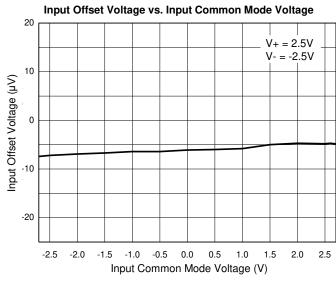
Note:

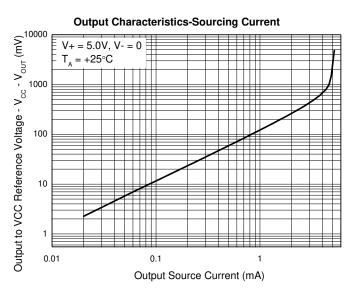
^{7. 300-}hour life test at +150°C demonstrated randomly distributed variation of approximately 1µV. This parameter is guaranteed by design and characterization, not by testing.

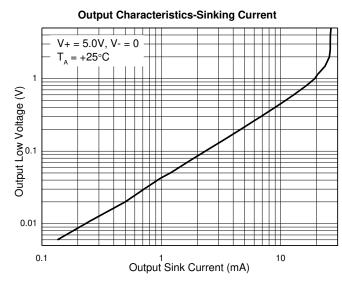


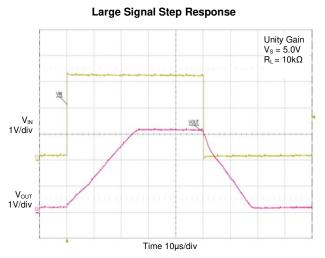
Typical Performance Characteristics

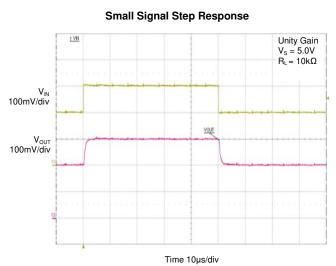








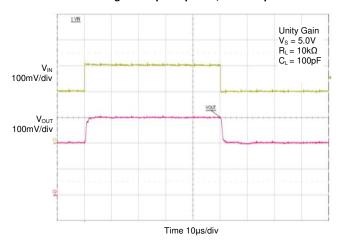




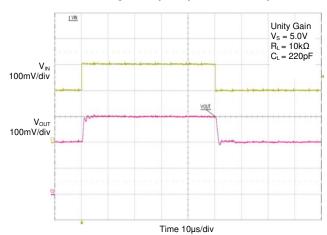


Typical Performance Characteristics (continued)

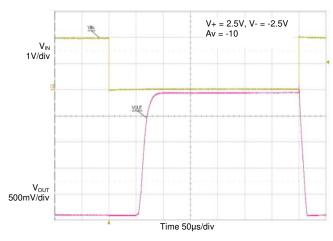
Small Signal Step Response, CL = 100pF



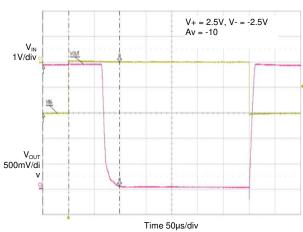
Small Signal Step Response, CL = 220pF



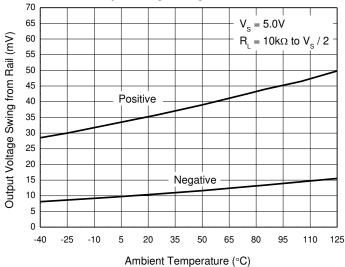
Negative Overvoltage Response



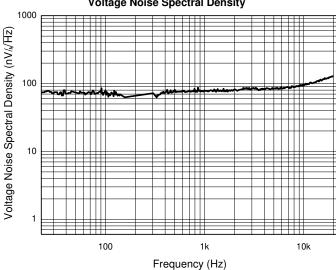
Positive Overvoltage Response







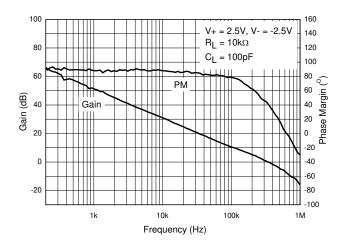
Voltage Noise Spectral Density



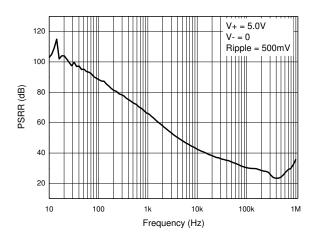


Typical Performance Characteristics (continued)

Frequency Response (Gain and Phase Margin)



PSRR (Power Supply Rejection Ration) vs. Frequency





Application Information

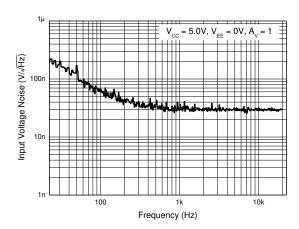
Overview

The AS333 is low power, zero-drift, high precision, rail-to-rail input and output operational amplifier, which adopts chopper-stabilized function circuits to provide the advantage of minimizing input offset voltage and offset voltage drift over time and temperature. Its input common-mode voltage range extends 0.1V beyond the supply rails to allow for sensing near ground or system V_{DD}. The device operates from a single-supply voltage as low as 1.8V, is unity-gain stable, has no 1/f noise, has good PSRR and CMRR performance. These features make the part suitable for a wide range of general-purpose applications, especially for low-power high precision ones.

Low Input Referred Noise

The device AS333 is chopper stabilized amplifier, the flicker noise is reduced greatly because of this technique. The zero-drift chopper-stabilized amplifiers are especially suited for accurate, high-gain amplification at lower frequencies. In general, they do not exhibit the higher bandwidth of linear operational amplifier, and the location of their clock frequency establishes a practical frequency limit on signal fidelity. This makes performance at low frequencies especially important, and the chopper-stabilized architecture further contributes to low frequency usefulness by eliminating the classic linear operational amplifier 1/f input voltage noise. Many high gain sensor applications are at low frequencies, making zero-drift amplifiers a natural choice for this function.

Below plots compared conventional amplifier voltage noise density behavior and zero-drift amplifier's, this 1/f noise elimination in zero-drift amplifier allows the AS333 to have much lower noise at DC and low frequency compared to conventional low noise amplifier.



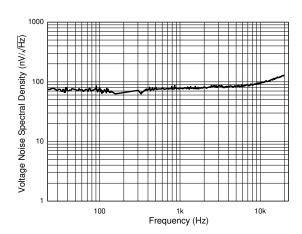


Figure 1. Input Voltage Noise in Conventional Amplifier

Figure 2. Input Voltage Noise in Zero-Drift Amplifier

Driving a Capacitive Load

The AS333 can directly drive 200pF in unity-gain without oscillation. The unity-gain follower is the most sensitive configuration to capacitive loading. Capacitive loading directly on the output terminal can decrease the device's phase margin leading to high frequency ringing or oscillation.

To drive a heavier capacitive load, the circuit in Figure 3 can be used. The resistor R_{NULL} and C_L form a pole to increase stability by adding more phase margin to the system. The bigger R_{NULL} resistor value the more stable V_{OUT} is. Figure 4 and Figure 5 are AS333 output pulse response waveforms with and without R_{NULL} 330 Ω for load conditions $C_L = 470pF$ and $R_L = 10k\Omega$.

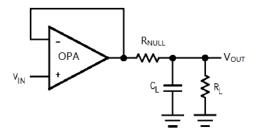
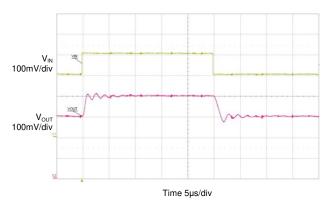


Figure 3. Capacitive Load with R_{NULL}



Application Information (continued)



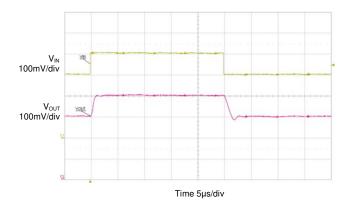
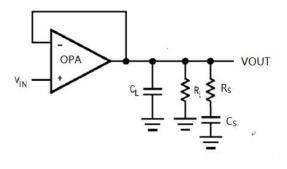


Figure 4. Test Result Without R_{NULL}

Figure 5. Test Result with R_{NULL} 330 Ω

To reduce output ringing and overshoot, another way is to use the RC snubber circuit, shown in Figure 6, it does allow the amplifier to drive larger values of capacitance while maintaining a minimum of overshoot and ringing. Figure 7 shows AS333 test result for capacitive load 470pF with snubber circuit.



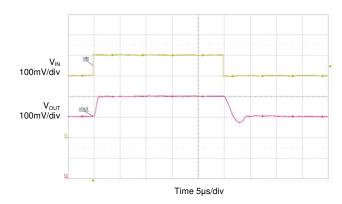


Figure 6. The RC Snubber Circuit

Figure 7. Test Result with Snubber Circuit

Differential Amplifier for Bridged Circuits

Sensors to measure strain, pressure, and temperature are often configured in a Wheatstone bridge circuit as shown in Figure 8. In the measurement, the voltage change that is produced is relatively small and needs to be amplified before going into an ADC. Precision amplifiers are recommended in these types of applications due to their high gain, low noise, and low offset voltage.

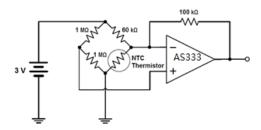
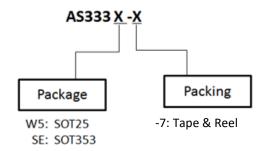


Figure 8. Bridge Circuit Amplification



Ordering Information

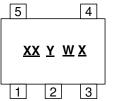


Orderable Part Number	Part Number Suffix	Package Code	Dookowa	Packing		
Orderable Part Number			Package	Quantity	Carrier	
AS333W5-7	-7	W5	SOT25	3,000	Tape & Reel	
AS333SE-7	-7	SE	SOT353	3,000	Tape & Reel	

Marking Information

(1) SOT25 and SOT353

(Top View)



XX : Identification Code

 \underline{Y} : Year 0 to 9 (ex: 3 = 2023) \underline{W} : Week: A to Z: week 1 to 26;

a to z : week 27 to 52; z represents

week 52 and 53 \underline{X} : Internal Code

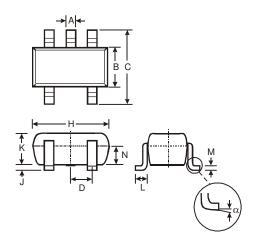
Part Number	Package	Identification Code
AS333SE-7	SOT353	PF
AS333W5-7	SOT25	PG



Package Outline Dimensions

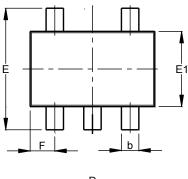
 $Please see \ http://www.diodes.com/package-outlines.html \ for \ the \ latest \ version.$

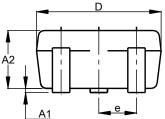
SOT25

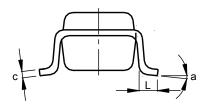


	SOT25				
Dim	Min	Max	Тур		
Α	0.35	0.50	0.38		
В	1.50	1.70	1.60		
С	2.70	3.00	2.80		
D	-	-	0.95		
Н	2.90	3.10	3.00		
J	0.013	0.10	0.05		
K	1.00	1.30	1.10		
L	0.35	0.55	0.40		
M	0.10	0.20	0.15		
N	0.70	0.80	0.75		
α	0°	8°	-		
All Dimensions in mm					

SOT353







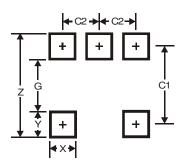
SOT353					
Dim	Min	Max	Тур		
A1	0.00	0.10	0.05		
A2	0.90	1.00	0.95		
b	0.10	0.30	0.25		
С	0.10	0.22	0.11		
D	1.80	2.20	2.15		
Е	2.00	2.20	2.10		
E1	1.15	1.35	1.30		
е	C).650 E	SC		
F	0.40	0.45	0.425		
L	0.25	0.40	0.30		
а	0°	8°			
All Dimensions in mm					



Suggested Pad Layout

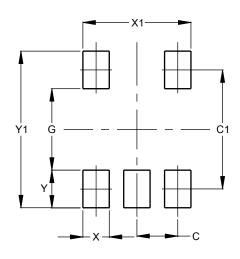
Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT25



Dimensions	Value
Z	3.20
G	1.60
Х	0.55
Υ	0.80
C1	2.40
C2	0.95

SOT353



Dimensions	Value (in mm)
С	0.650
C1	1.900
G	1.300
Х	0.420
X1	1.720
Υ	0.600
Y1	2.500

Mechanical Data

SOT25

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.015 grams (Approximate)

SOT353

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.006 grams (Approximate)



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