

Features

- Low On-Resistance
 - 60mΩ @ $V_{GS} = -4.5V$
 - 90mΩ @ $V_{GS} = -2.5V$
 - 113mΩ @ $V_{GS} = -1.8V$
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **The DMP2305UQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.**

<https://www.diodes.com/quality/product-definitions/>

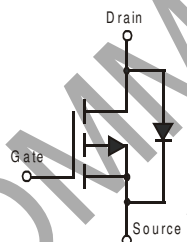
Mechanical Data

- Package: SOT23
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish — Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Terminals Connections: See Diagram Below
- Weight: 0.008 grams (Approximate)

SOT23 (Standard)



Top View



Internal Schematic



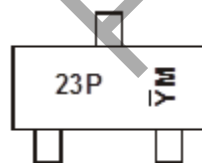
Top View

Ordering Information (Note 4)

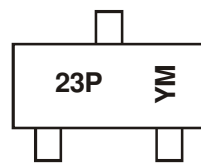
Part Number	Qualification	Package	Packing	
			Qty.	Carrier
DMP2305UQ-7	Automotive	SOT23 (Standard)	3000	Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



Chengdu A/T Site



Shanghai A/T Site

23P = Product Type Marking Code
 YM = Date Code Marking for SAT (Shanghai Assembly/ Test Site)
 YM = Date Code Marking for CAT (Chengdu Assembly/ Test Site)
 Y or \bar{Y} = Year (ex: J = 2022)
 M = Month (ex: 2 = February)

Date Code Key

Year	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033
Code	J	K	L	M	N	O	P	R	S	T	U	V

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	-20	V
Gate-Source Voltage			V _{GSS}	±8	V
Continuous Drain Current (Note 5)	Steady State	T _A = +25°C	I _D	-4.2	A
		T _A = +70°C		-3.4	
Pulsed Drain Current (Note 6)			I _{DM}	-10	A

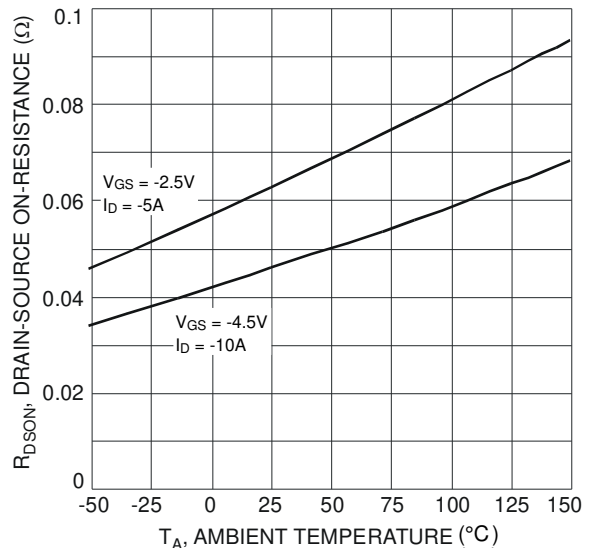
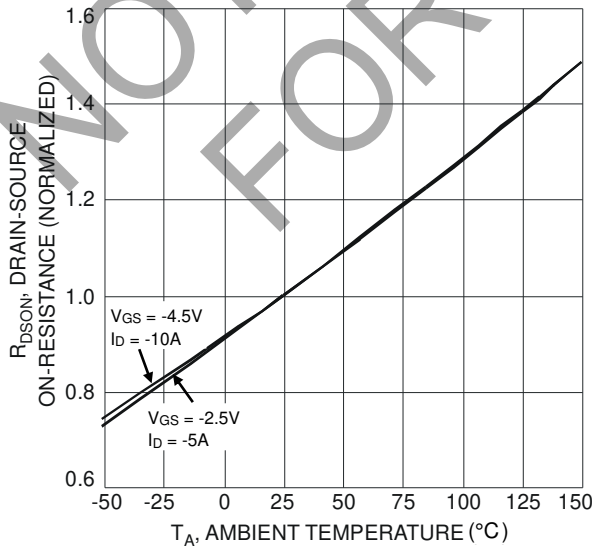
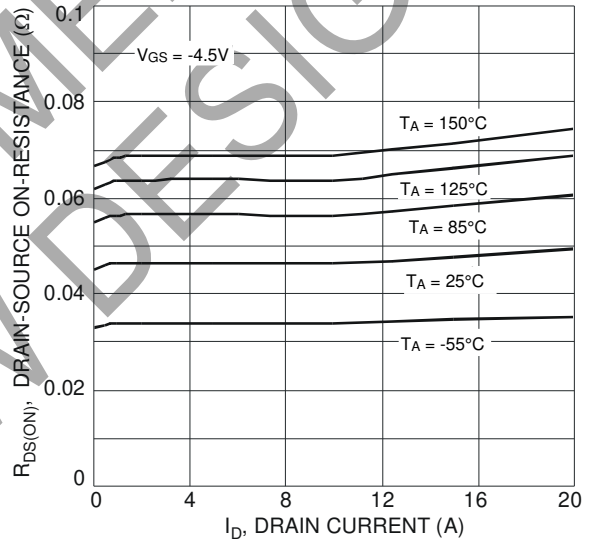
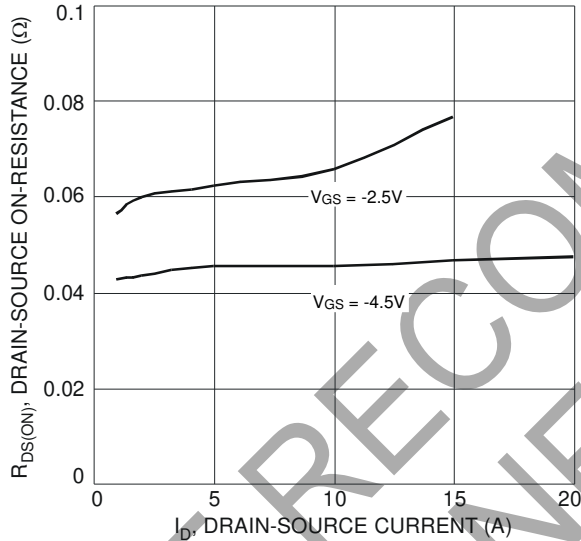
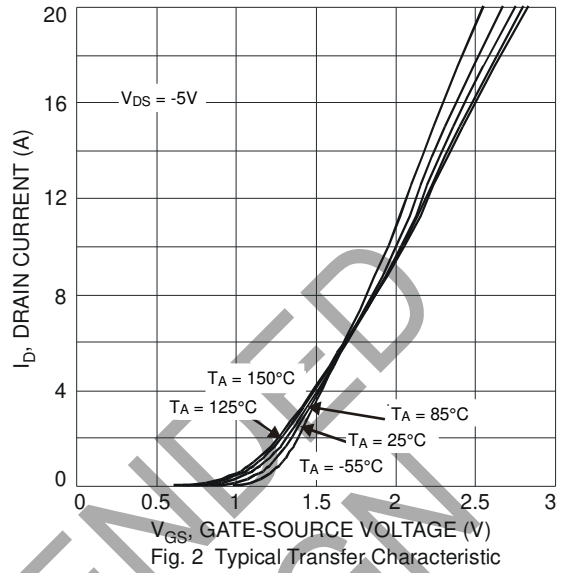
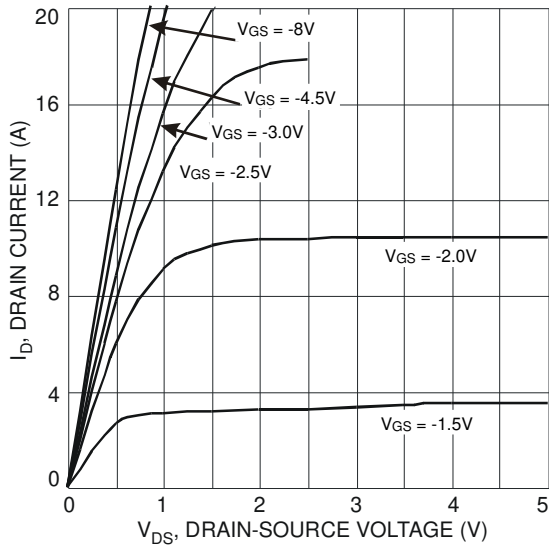
Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P _D	1.4	W
Thermal Resistance, Junction to Ambient @T _A = +25°C	R _{θJA}	90	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	-20	—	—	V	V _{GS} = 0V, I _D = -250μA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	-1.0	μA	V _{DS} = -20V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±8V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	-0.5	—	-0.9	V	V _{DS} = V _{GS} , I _D = -250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	45	60	mΩ	V _{GS} = -4.5V, I _D = -4.2A
		—	60	90		V _{GS} = -2.5V, I _D = -3.4A
		—	87	113		V _{GS} = -1.8V, I _D = -2.0A
Forward Transfer Admittance	Y _{FS}	—	9	—	S	V _{DS} = -5V, I _D = -4A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	—	727	—	pF	V _{DS} = -20V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{oss}	—	69	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	64	—	pF	
Gate Resistance	R _g	—	23	—	Ω	V _{GS} = 0V, V _{DS} = 0V, f = 1.0MHz
SWITCHING CHARACTERISTICS						
Total Gate Charge	Q _g	—	7.6	—	nC	V _{GS} = -4.5V, V _{DS} = -4V, I _D = -3.5A
Gate-Source Charge	Q _{gs}	—	1.4	—	nC	
Gate-Drain Charge	Q _{gd}	—	1.2	—	nC	
Turn-On Delay Time	t _{D(ON)}	—	14.0	—	ns	V _{DS} = -4V, V _{GS} = -4.5V, R _L = 4Ω, R _G = 6Ω, I _D = -1A
Turn-On Rise Time	t _R	—	13.0	—	ns	
Turn-Off Delay Time	t _{D(OFF)}	—	53.8	—	ns	
Turn-Off Fall Time	t _F	—	23.2	—	ns	

- Notes:
- Device mounted on FR-4 PCB with 2oz. copper and test pulse width t ≤ 10s.
 - Repetitive rating, pulse width limited by junction temperature.
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to production testing.



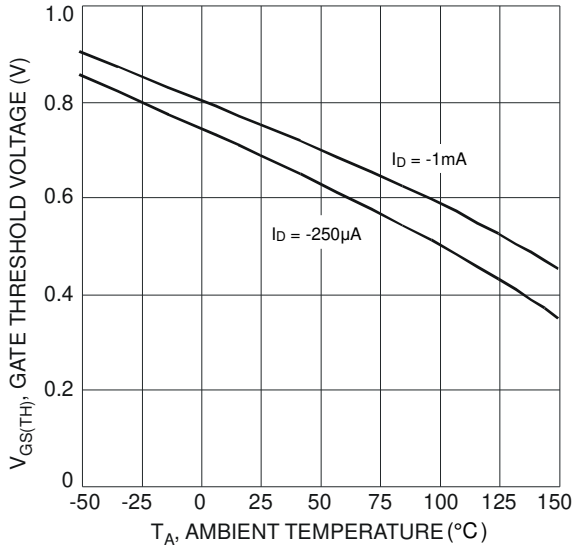


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

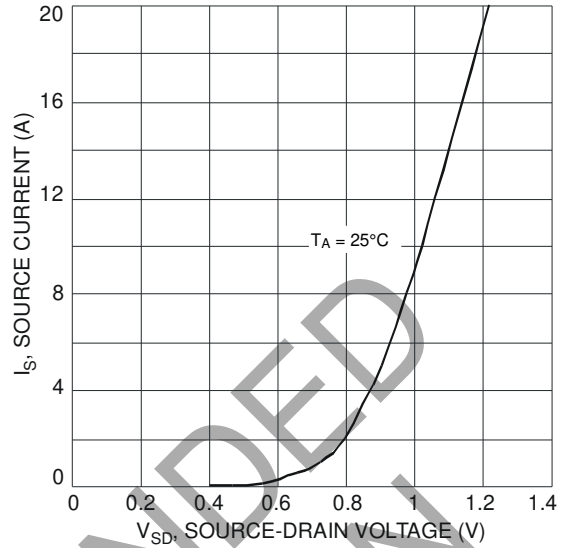


Fig. 8 Diode Forward Voltage vs. Current

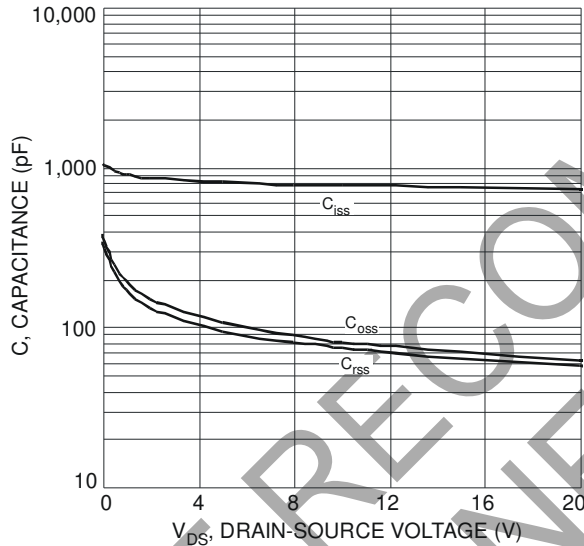


Fig. 9 Typical Total Capacitance

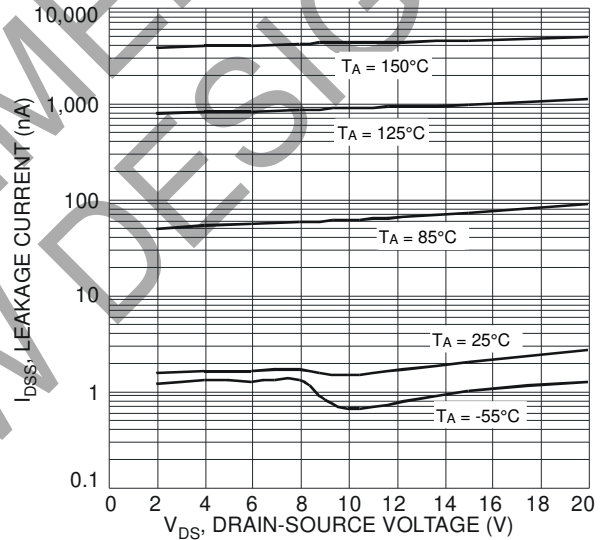


Fig. 10 Typical Leakage Current vs. Drain-Source Voltage

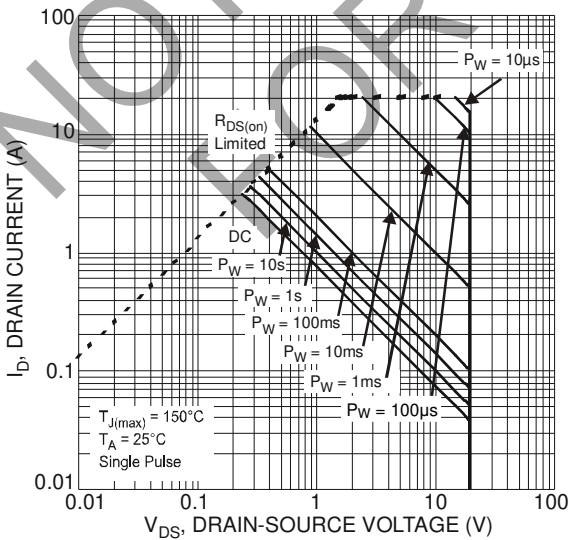


Fig. 11 SOA, Safe Operation Area

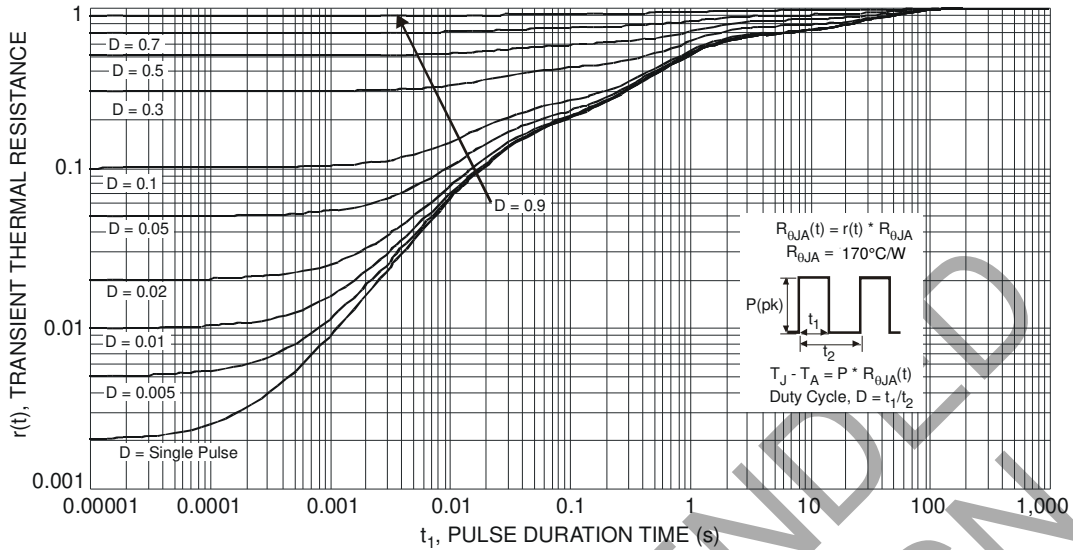


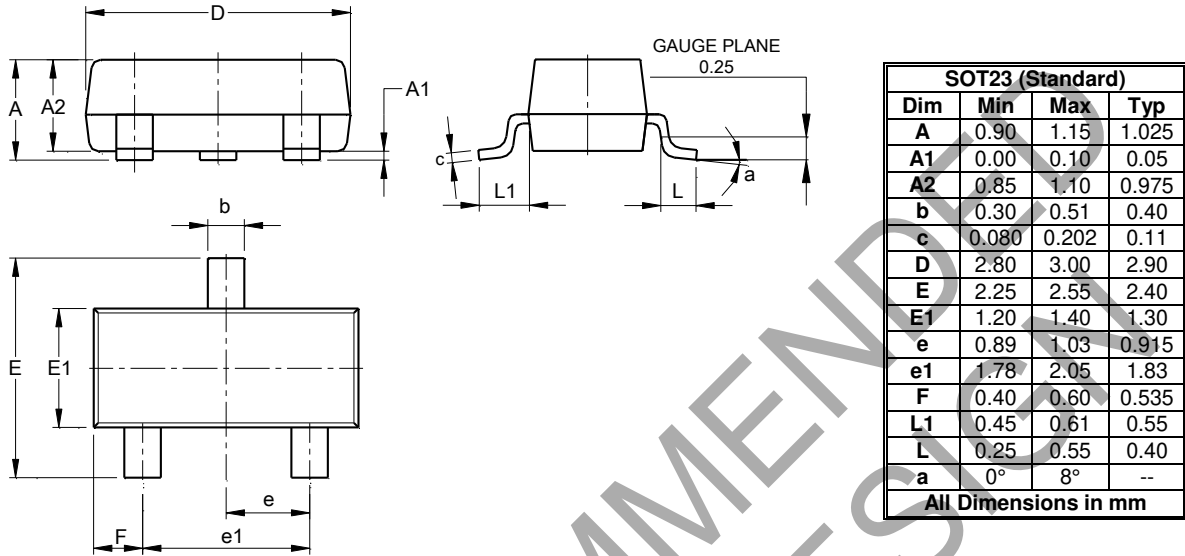
Fig. 12 Transient Thermal Response

NOT RECOMMENDED FOR NEW DESIGN

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

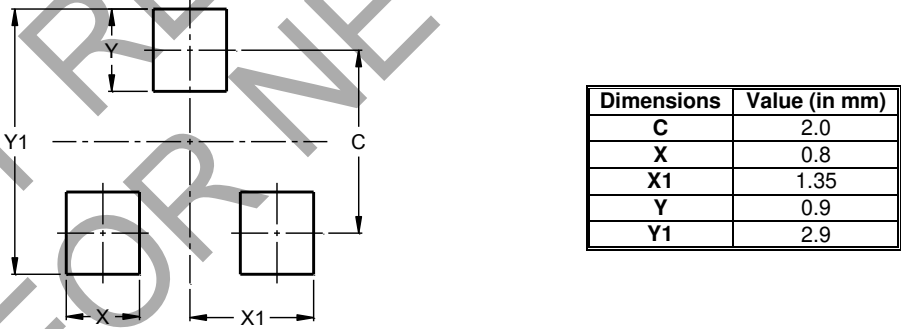
SOT23 (Standard)



Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT23 (Standard)



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