

GA01PNS150-220

Silicon Carbide PiN Diode

 V_{RRM} 15.0 kV 1 A I_{F (Tc=25°C)}

Features

- 15 kV blocking
- 175 °C operating temperature
- Fast turn off characteristics
- Soft reverse recovery characteristics
- Ultra-Fast high temperature switching

Advantages

- Highest voltage rectifier commercially available
- Reduced stacking
- · Reduced system complexity/Increased reliability

Package

• RoHS Compliant



Applications

- Voltage Multiplier
- Ignition/Trigger Circuits
- Oil/Downhole
- Lighting
- Defense

Maximum Ratings at T_j = 175 °C, unless otherwise specified

Parameter	Symbol	Conditions	Values	Unit
Repetitive peak reverse voltage	V_{RRM}		15	kV
Continuous forward current	l _F		1	Α
RMS forward current	I _{F(RMS)}		0.5	Α
Operating and storage temperature	T_j , T_stg		-55 to 175	°C

Electrical Characteristics at T_j = 175 °C, unless otherwise specified

Parameter	Symbol	Conditions —		Values		Unit	
Parameter	Syllibol			min.	typ.	max.	Unit
Diode forward voltage	V _F	$I_F = 1 A, T_j = 2$	5 °C		6.4		
Diode forward voltage	VF	$I_F = 1 A, T_j = 17$	I _F = 1 A, T _j = 175 °C		4.7		V
Reverse current	I_{R}	$V_R = 8 \text{ kV}, T_j =$	25 °C		1	20	^
Neverse current		$V_R = 8 \text{ kV}, T_j = 175 ^{\circ}\text{C}$			100	μΑ	
Total reverse recovery charge	Q_{rr}	I _F ≤ I _{F,MAX}	V _R = 1000 V I _F = 1.5 A		558		nC
Switching time	t _s	− dI _F /dt = 70 A/μs T _j = 175 °C	V _R = 1000 V I _F = 1.5 A		< 236		ns
		V _R = 1 V, f = 1 MHz,	T _j = 25 °C		22		
Total capacitance	С	V _R = 400 V, f = 1 MHz	z, T _j = 25 °C		4		pF
		V _R = 1000 V, f = 1 MH	z, T _j = 25 °C		3		
Total capacitive charge	$Q_{\mathbb{C}}$	V _R = 1000 V, f = 1 MH	z, T _j = 25 °C		4.5		nC



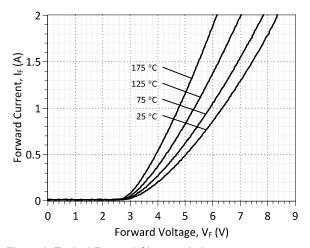


Figure 1: Typical Forward Characteristics

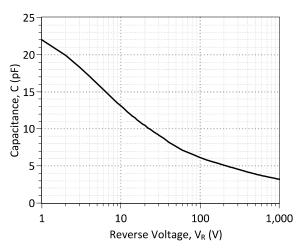


Figure 3: Typical Junction Capacitance vs Reverse Voltage Characteristics

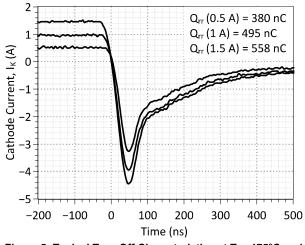


Figure 5: Typical Turn Off Characteristics at T_j = 175°C and V_R = 1000 V

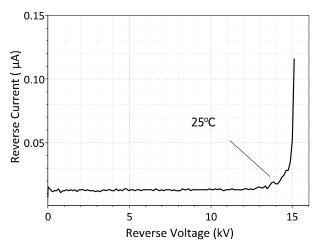


Figure 2: Typical Reverse Characteristics at 25°C

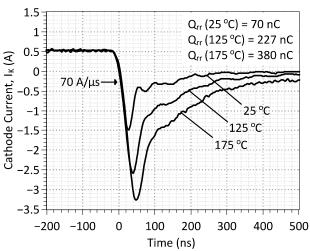


Figure 4: Typical Turn Off Characteristics at I_{k} = 0.5 A and V_{R} = 1000 V

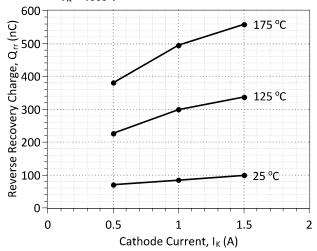


Figure 6: Reverse Recovery Charge vs Cathode Current



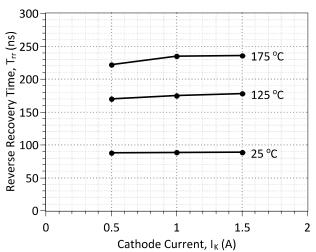
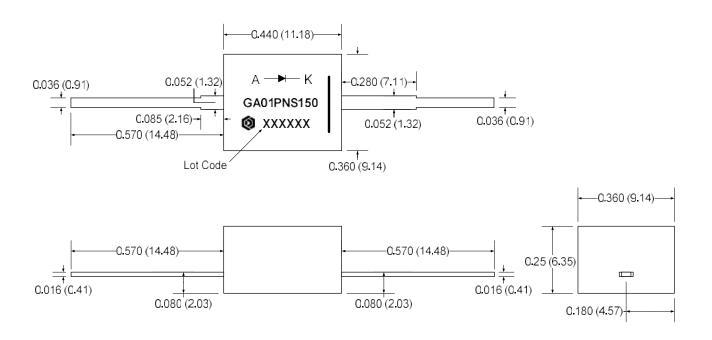


Figure 7: Reverse Recovery Time vs Cathode Current

Package Dimensions:

PACKAGE OUTLINE



- 1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
- 2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS



GA01PNS150-220

Revision History							
Date	Revision	Comments	Supersedes				
2015/04/30	1	Updated Electrical Characteristics					
2014/11/07	0	Initial release					

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SPICE Model Parameters

This is a secure document. Please copy this code from the SPICE model PDF file on our website (http://www.genesicsemi.com/sic_pin/GA01PNS150-220_SPICE.pdf) into LTSPICE (version 4) software for simulation of the GA01PNS150-220.

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MODEL OF GeneSiC Semiconductor Inc.
     $Revision: 1.1
                                $
     $Date: 30-APR-2015
     GeneSiC Semiconductor Inc.
     43670 Trade Center Place Ste. 155
     Dulles, VA 20166
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* These models are provided "AS IS, WHERE IS, AND WITH NO WARRANTY
* OF ANY KIND EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED
* TO ANY IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A
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* Models accurate up to 2 times rated drain current.
 Start of GA01PNS150-220 SPICE Model
.MODEL GA01PNS150 D
+ IS 9.2491e-015
         2.24770
+ RS
+ N
          3.3373
         0.00011784
+ IKF
          3.23
+ EG
+ XTI
         25
+ TRS1
         -0.0024
+ CJO
          2.28E-11
         2.304
+ VJ
+ M
         0.376
+ FC
         0.5
+ BV
         8000
         1.00E-03
+ IBV
         15000
+ VPK
+ IAVE
        SiC PiN
+ TYPE
+ MFG
         GeneSiC Semi
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* End of GA01PNS150-220 SPICE Model