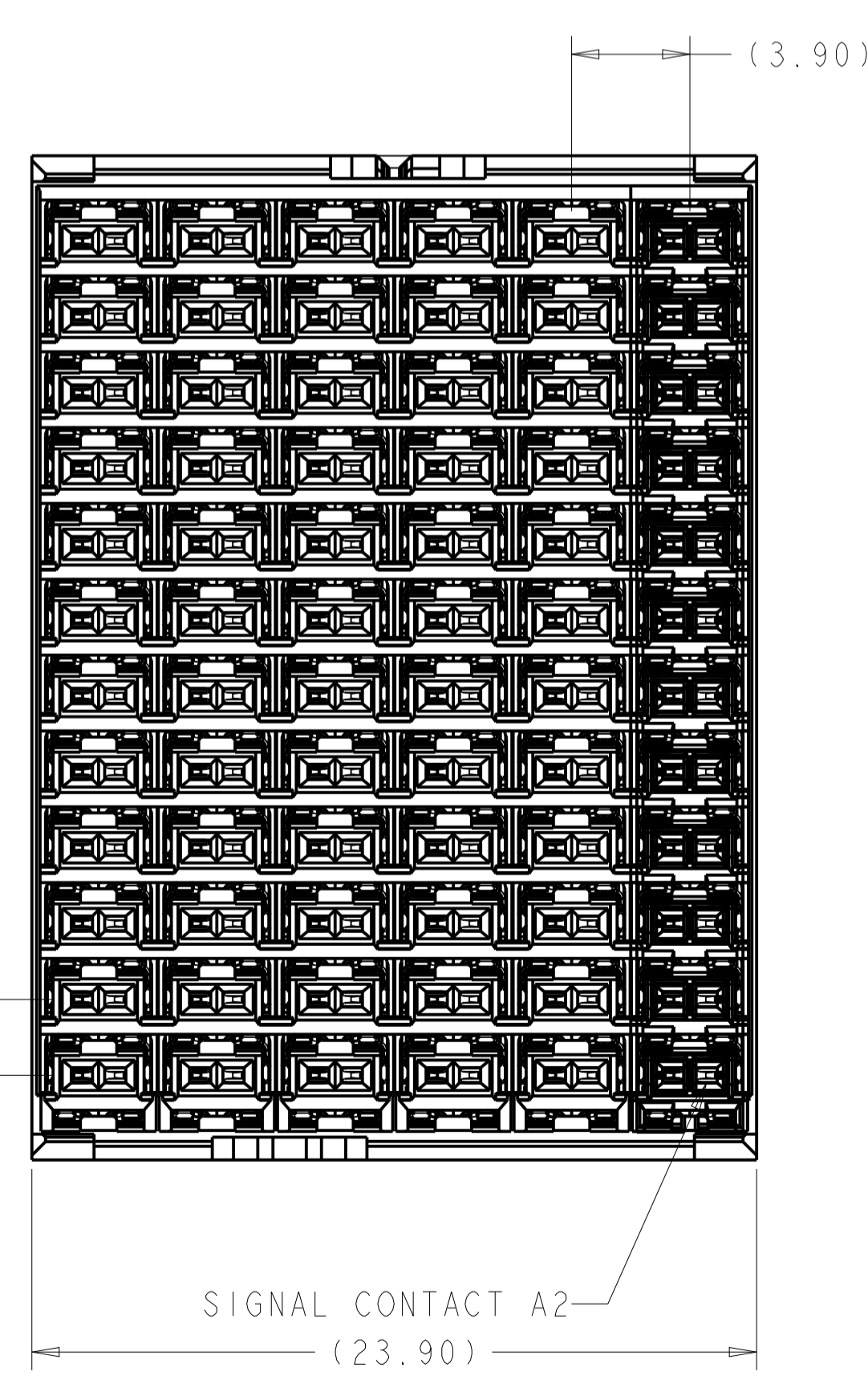
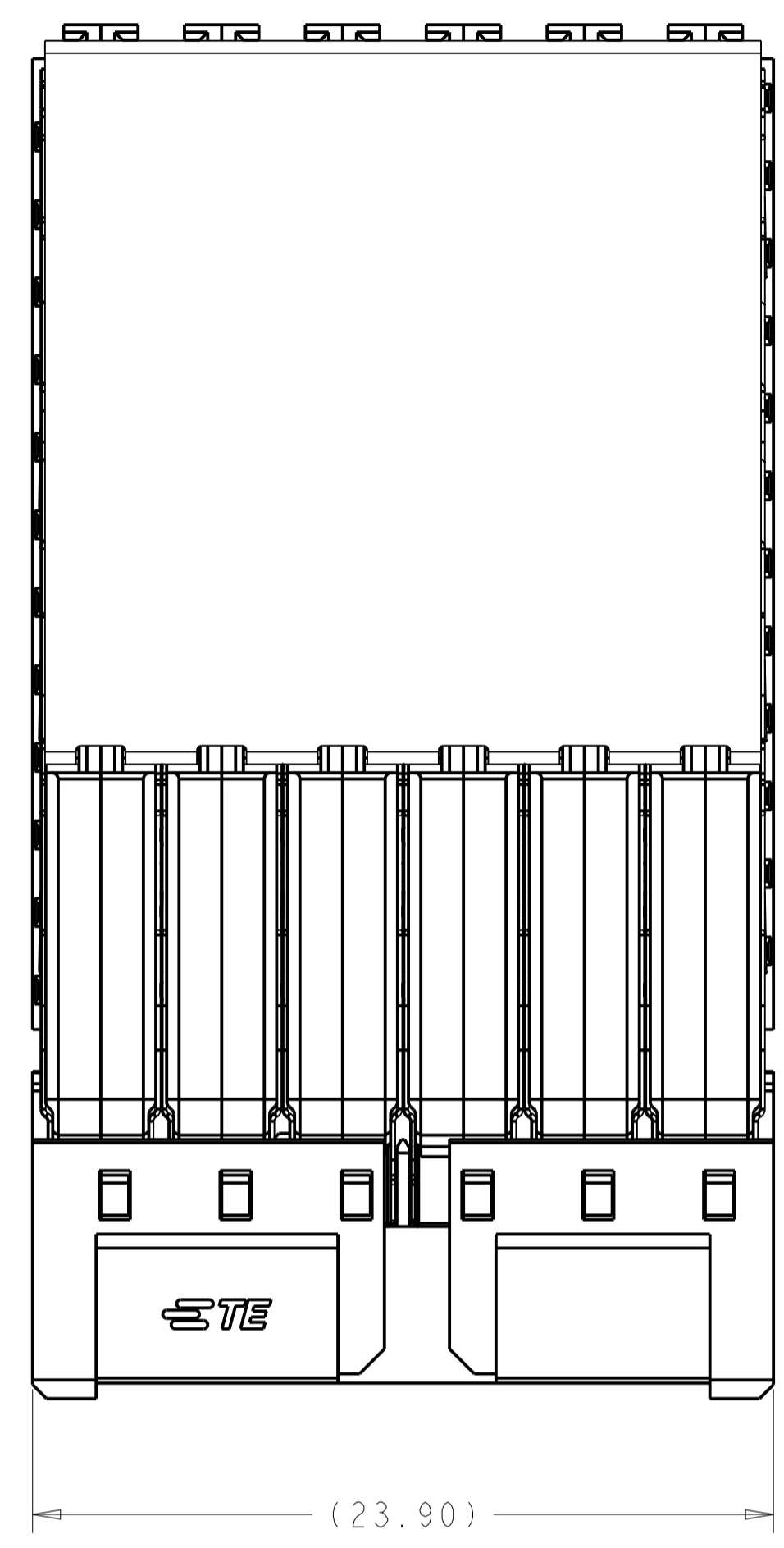
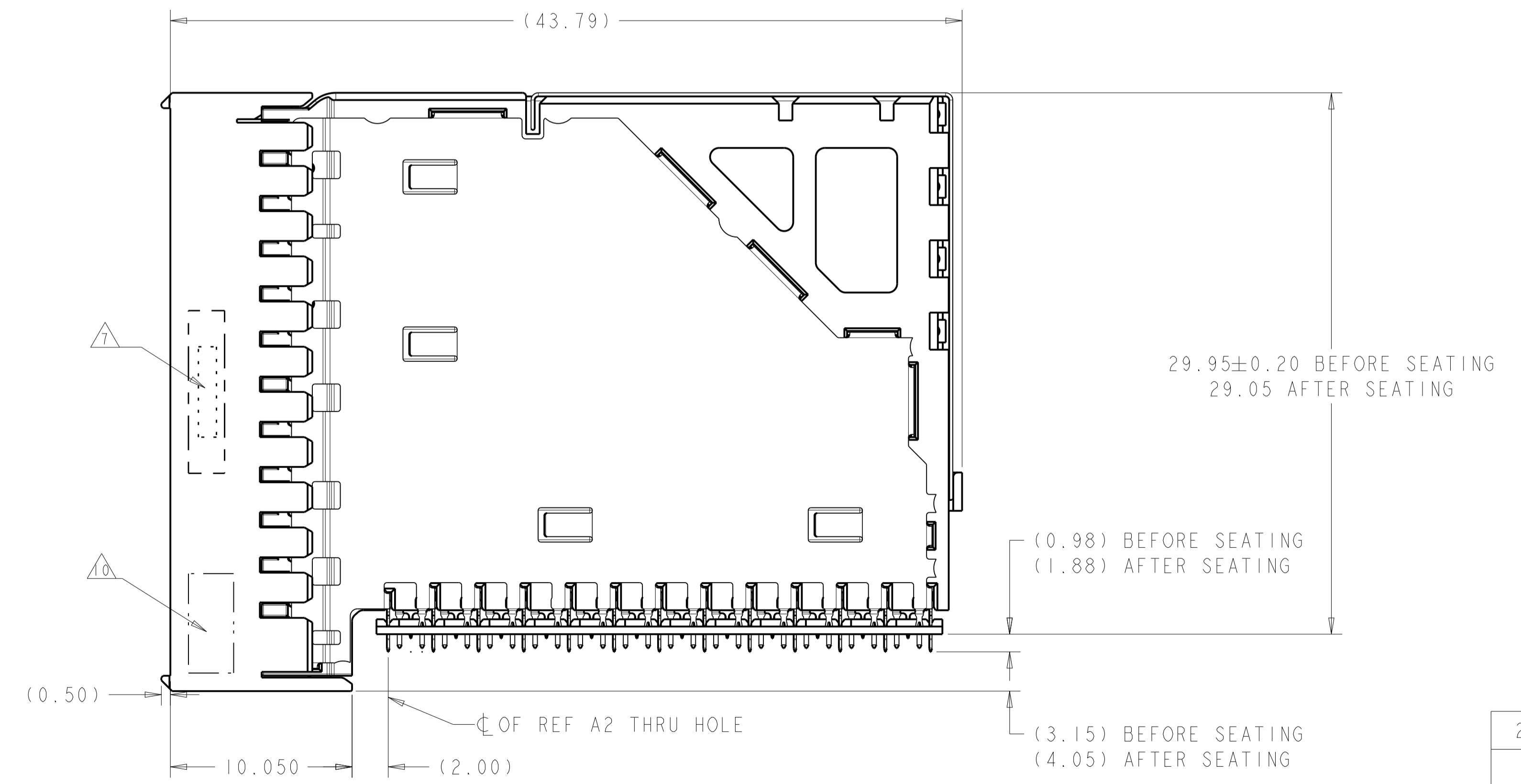


LOC		DIST		REVISIONS			
GP	00	P	LTN	DESCRIPTION	DATE	DWN	APVD
		D		REVISED PER ECO-19-006255	22APR2019	AP	RP
		E		REVISED PER ECO-19-006930	09MAY2019	AP	RP
		F		REVISED PER ECO-19-012865	20AUG2019	AP	RP
		G		REVISED PER ECO-21-003195	10MAR2021	AP	RP



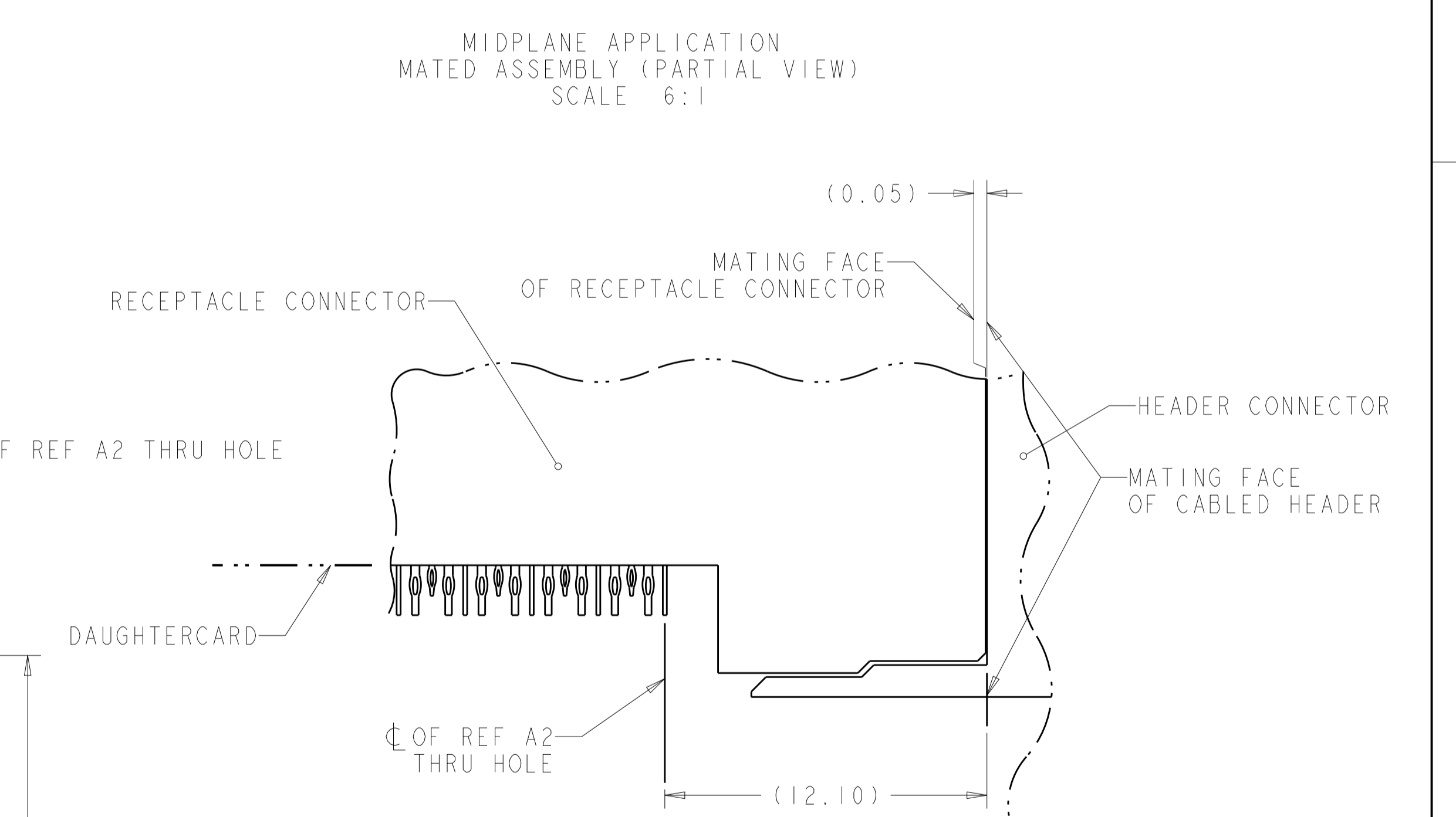
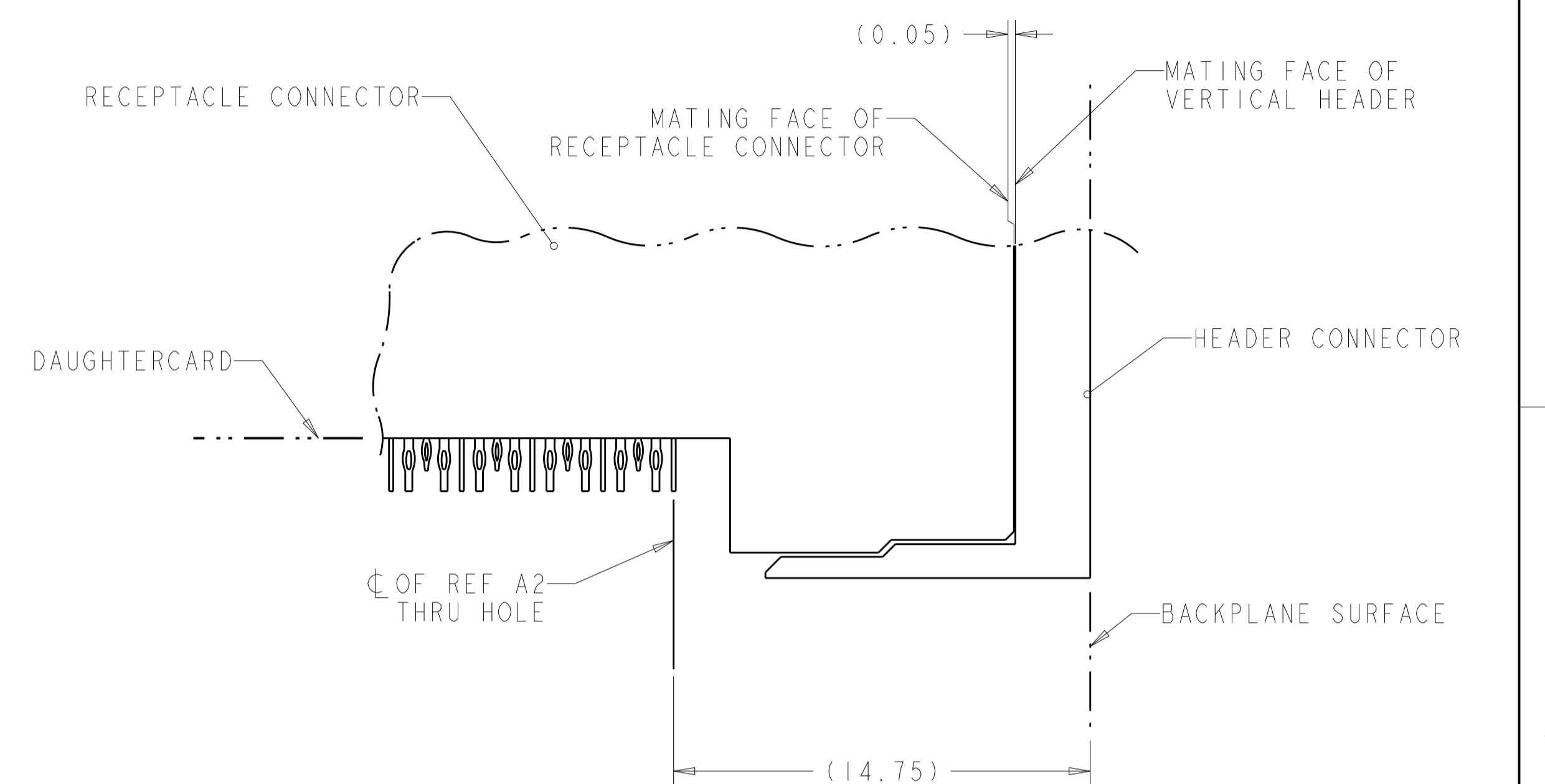
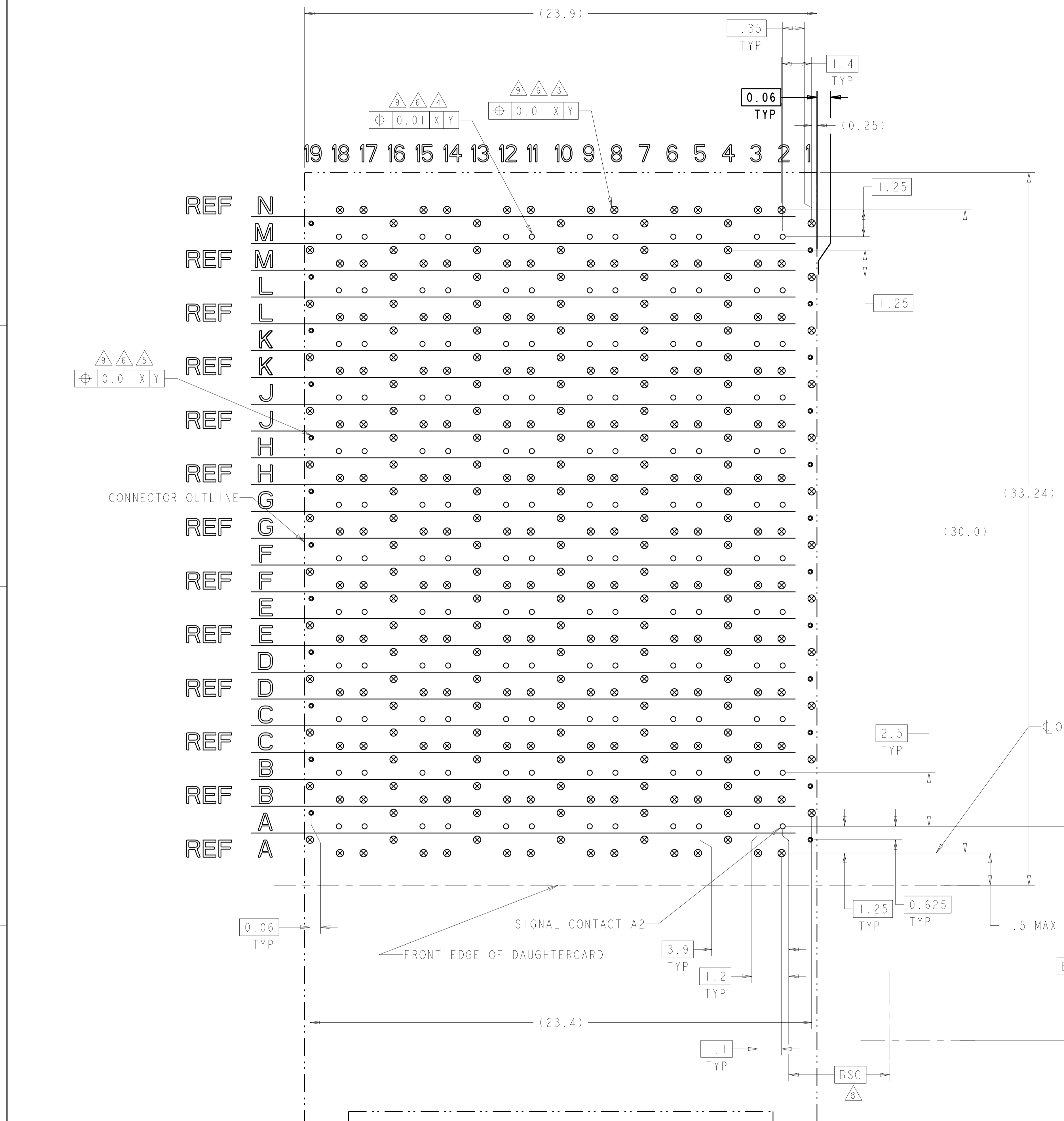
- 1 MATERIAL:  
 CONTACTS AND SHIELDS - COPPER ALLOY  
 ORGANIZER - STAINLESS STEEL  
 HOUSING, GROUND SHELLS, AND PIN SPACER - THERMOPLASTIC, UL 94V-0, BLACK
- 2 FINISH:  
 - CONFORMS TO THE REQUIREMENTS OF TE PRODUCT SPECIFICATION 108-32021  
 BASED ON TELCORDIA GR-1217-CORE FOR SYSTEM QUALITY LEVEL III,  
 APPLICATIONS IN CONTROLLED ENVIRONMENTS (CENTRAL OFFICE).  
 - COMPLIANT PIN AREA: 0.5µm MIN MATTE TIN.
- 3 GROUND PLATED THRU HOLE REQUIREMENTS:  
 FINISHED HOLE =  $\varnothing 0.34 \pm 0.05$   
 HOLE SIZE PRIOR TO PLATING =  $\varnothing 0.420 \pm 0.025$   
 COPPER THICKNESS = 0.025 MIN  
 IMMERSION TIN IF APPLICABLE = 0.00095 REF
- 4 SIGNAL PLATED THRU HOLE REQUIREMENTS:  
 FINISHED HOLE =  $\varnothing 0.24 \pm 0.05$   
 HOLE SIZE PRIOR TO PLATING =  $\varnothing 0.320 \pm 0.025$   
 COPPER THICKNESS = 0.025 MIN  
 IMMERSION TIN IF APPLICABLE = 0.00095 REF
- 5 SI VIA PLATED THRU HOLE REQUIREMENTS:  
 HOLE SIZE PRIOR TO PLATING =  $0.300 \pm 0.025$   
 COPPER THICKNESS = MIN REQUIRED TO MEET REQUIRED ELECTRICAL PERFORMANCE
- 6 SI VIAS & SIGNAL HOLES:  
 PTH DIMENSIONS APPLY TO THE TOP 1.00mm OF THE PCB THICKNESS FROM THE  
 CONNECTOR MOUNTING SIDE.  
 GROUND HOLES:  
 PTH DIMENSIONS APPLY TO THE TOP 1.25mm OF THE PCB THICKNESS FROM THE  
 CONNECTOR MOUNTING SIDE.
- 7 CONNECTOR MARKED WITH PART NUMBER, DATE CODE, AND "100 OHM" IN  
 APPROXIMATE AREA SHOWN.
- 8 DATUMS AND BASIC DIMENSIONS ESTABLISHED BY CUSTOMER
- 9 APPLIES TO FINISHED HOLE DIAMETER.
- 10 DATAMATRIX BARCODE MARKING IN THIS AREA.



2187462-1  
 PART NO

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN J. K. SHOBER 25NOV2013	TE Connectivity	
DIMENSIONS: mm		CHK E. WICKES 25NOV2013	R/A RECEPTACLE ASSEMBLY, 100 OHM 12 PAIR, 6 COLUMN, PAIR-IN-ROW, STRADA WHISPER	
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD	RESTRICTED TO	
0 PLC	±	PRODUCT SPEC	SIZE	
1 PLC	±0.13	108-32021	A1	
2 PLC	±	APPLICATION SPEC	CAGE CODE	
3 PLC	±	114-32029	DRAWING NO	
4 PLC	±	WEIGHT	2187462	
ANGLES	±	CUSTOMER DRAWING	SCALE 1:1 SHEET 1 OF 2 REV G	

LOC	DIST	REV	DATE	BY	APPV
		1			
		2			
		3			
		4			



○ = SIGNAL  
 ⊗ = GROUND  
 ⊙ = SIGNAL INTEGRITY VIA TO GROUND

RECOMMENDED PCB LAYOUT  
 SINGLE CONNECTOR (STACKABLE)  
 COMPONENT SIDE SHOWN  
 SCALE 10:1

THIS DRAWING IS A CONTROLLED DOCUMENT.		OWN J. K. SHOBER 25NOV2013	TE Connectivity
DIMENSIONS: mm		CHK E. WICKES 25NOV2013	
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD	NAME
0 PLC	±	PRODUCT SPEC	R/A RECEPTACLE ASSEMBLY, 100 OHM
1 PLC	±	108-32021	12 PAIR, 6 COLUMN,
2 PLC	±	APPLICATION SPEC	PAIR-IN-ROW, STRADA WHISPER
3 PLC	±	SIZE	114-32029
4 PLC	±	WEIGHT	A1 00779 ©=2187462
ANGLES	±	FINISH	Customer Drawing
MATERIAL		SCALE 1:1	SHEET 2 OF 2
FINISH		REV G	