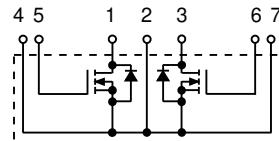


Dual Power MOSFET Module

VMK 90-02T2

Common-Source connected
N-Channel Enhancement Mode

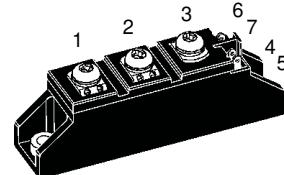
V_{DSS} = 200 V
I_{D25} = 83 A
R_{DS(on)} = 25 mΩ



Symbol	Test Conditions	Maximum Ratings		
V _{DSS}	T _J = 25°C to 150°C	200		V
V _{DGR}	T _J = 25°C to 150°C; R _{GS} = 6.8 kΩ	200		V
V _{GS}	Continuous	±20		V
V _{GSM}	Transient	±30		V
I _{D25}	T _C = 25°C	83		A
I _{D80}	T _C = 80°C	62		A
I _{DM}	T _C = 25°C, t _p = 10 μs, pulse width limited by T _{JM}	330		A
P _D	T _C = 25°C, T _J = 150°C,	380		W
T _J		-40 ... +150		°C
T _{JM}		150		°C
T _{stg}		-40 ... +125		°C
V _{ISOL}	50/60 Hz I _{ISOL} ≤ 1 mA	t = 1 min t = 1 s	2500 3000	V~ V~
M _d	Mounting torque(M5 or 10-32 UNF) Terminal connection torque (M5)	2.5-4.0/22-35 Nm/lb.in. 2.5-4.0/22-35 Nm/lb.in.		
Weight	Typical including screws	90		g

TO-240 AA

E 72873



1, 3 = Drain, 2 = Common Source
5, 6 = Gate, 4, 7 = Kelvin Source

Features

- Two MOSFET with common source
- International standard package JEDEC TO-240 AA
- Direct copper bonded Al₂O₃ ceramic base plate
- Isolation voltage 3000 V~
- Low R_{DS(on)} HDMOS™ process
- Low package inductance for high speed switching
- Kelvin source contact
- Keyed twin plugs

Applications

- Push-pull inverters
- Switched-mode and resonant-mode power supplies
- Uninterruptible power supplies (UPS)
- AC static switches

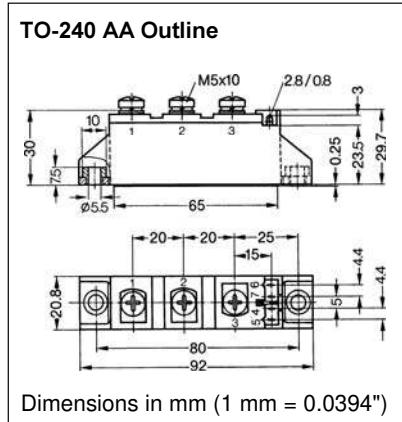
Advantages

- Easy to mount with two screws
- Space and weight savings
- High power density
- Low losses

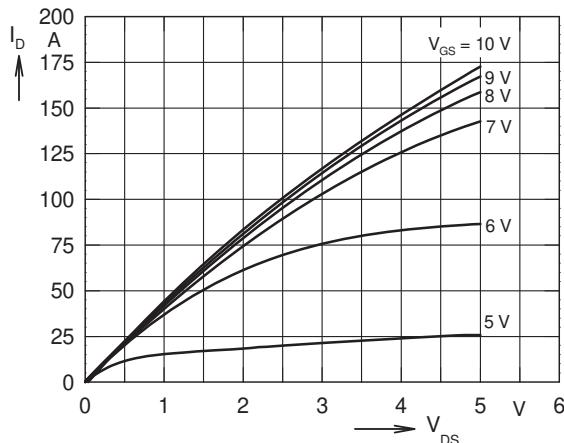
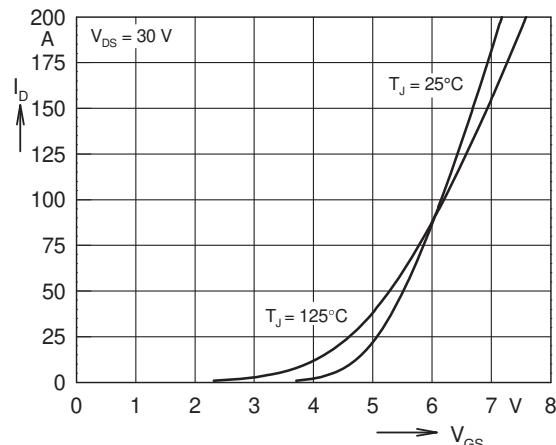
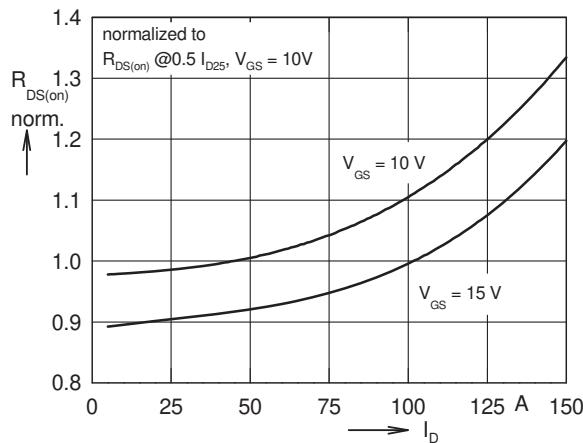
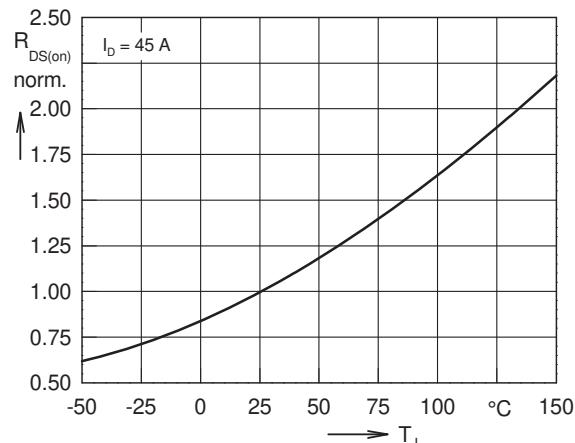
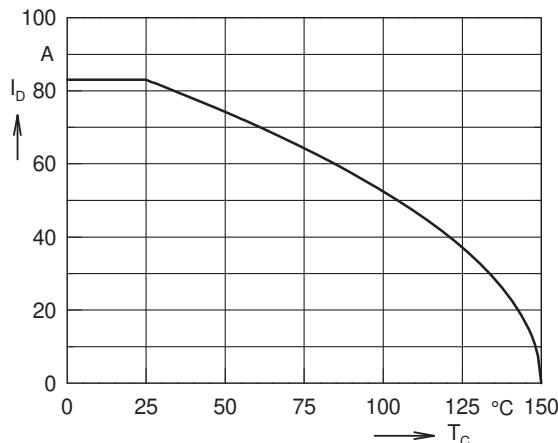
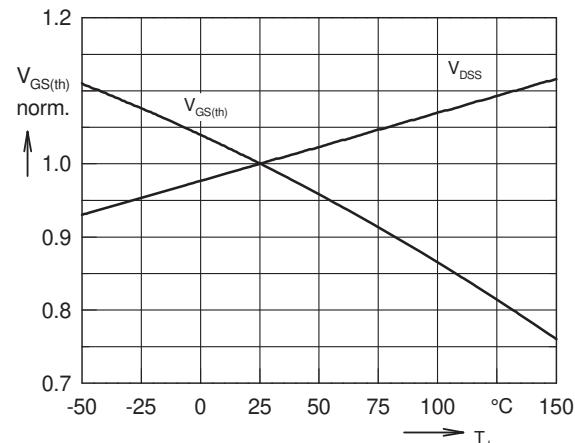
Symbol	Test Conditions	Characteristic Values			
		(T _J = 25°C, unless otherwise specified)	min.	typ.	max.
V _{DSS}	V _{GS} = 0 V, I _D = 1 mA	200			V
V _{GS(th)}	V _{DS} = V _{GS} , I _D = 3 mA	2		4	V
I _{ess}	V _{GS} = ±20 V DC, V _{DS} = 0			500	nA
I _{DSS}	V _{DS} = 0.8 • V _{DSS} , V _{GS} = 0 V, T _J = 25°C V _{GS} = 0 V, T _J = 125°C			400	μA 2 mA
R _{DS(on)}	V _{GS} = 10 V, I _D = 0.5 • I _{D25} Pulse test, t ≤ 300 μs, duty cycle d ≤ 2 %			25	mΩ

Data per MOSFET unless otherwise stated.
IXYS reserves the right to change limits, test conditions and dimensions

Symbol	Test Conditions	Characteristic Values ($T_J = 25^\circ\text{C}$, unless otherwise specified)		
		min.	typ.	max.
g_{fs}	$V_{DS} = 10 \text{ V}; I_D = 0.5 \cdot I_{D25}$ pulsed	60	S	
C_{iss} C_{oss} C_{rss}	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$	9000	15000	pF
		1600	4500	pF
		600	1500	pF
$t_{d(on)}$ t_r $t_{d(off)}$ t_f	$V_{GS} = 10 \text{ V}, V_{DS} = 0.5 \cdot V_{DSS}, I_D = 0.5 \cdot I_{D25}$ $R_G = 1 \Omega$ (External), resistive load		70	ns
			80	ns
			200	ns
			100	ns
Q_g Q_{gs} Q_{gd}	$V_{GS} = 10 \text{ V}, V_{DS} = 0.5 \cdot V_{DSS}, I_D = 0.5 \cdot I_{D25}$	380	450	nC
		70	110	nC
		190	230	nC
R_{thJC}			0.33	K/W
R_{thJK}	with heat transfer paste		0.53	K/W
d_s	Creepage distance on surface	12.7		mm
d_A	Strike distance through air	9.6		mm
a	Max. allowable acceleration	50		m/s ²



Symbol	Test Conditions	Characteristic Values ($T_J = 25^\circ\text{C}$, unless otherwise specified)		
		min.	typ.	max.
I_s	$V_{GS} = 0 \text{ V}$		83	A
I_{sm}	Repetitive; pulse width limited by T_{JM}		330	A
V_{SD}	$I_F = I_S; V_{GS} = 0 \text{ V}$, Pulse test, $t \leq 300 \mu\text{s}$, duty cycle $d \leq 2 \%$	1.0	1.2	V
t_{rr}	$I_F = I_S, -di/dt = 100 \text{ A}/\mu\text{s}, V_{DS} = 100 \text{ V}, V_{GS} = 0 \text{ V}$	400	750	ns

Fig. 1 Typical output characteristics $I_D = f (V_{DS})$ Fig. 2 Typical transfer characteristics $I_D = f (V_{GS})$ Fig. 3 Typical normalized $R_{DS(on)} = f (I_D)$ Fig. 4 Typical normalized $R_{DS(on)} = f (T_J)$ Fig. 5 Continuous drain current $I_D = f (T_c)$ Fig. 6 Typical normalized $V_{DSS} = f (T_J)$, $V_{GS(th)} = f (T_J)$

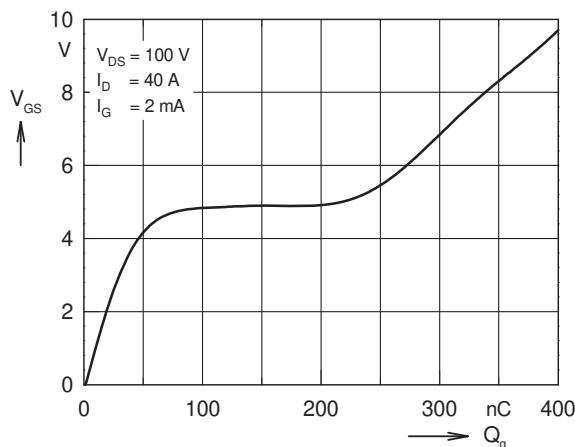


Fig. 7 Typical turn-on gate charge characteristics

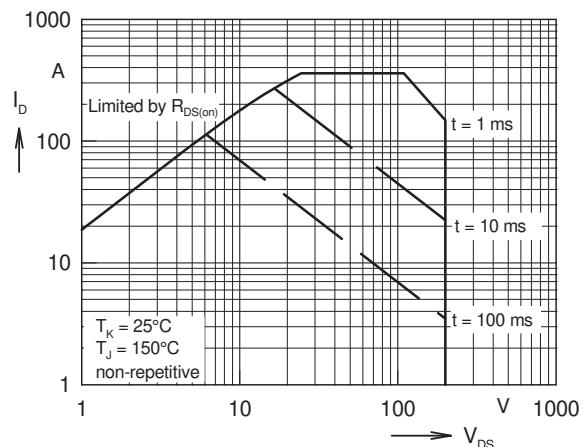


Fig. 8 Forward Safe Operating Area, $I_D = f(V_{DS})$

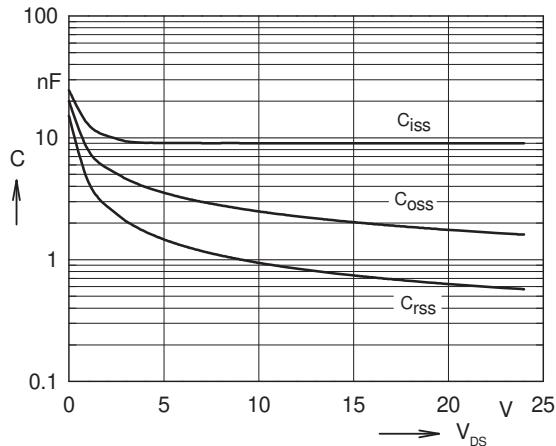


Fig. 9 Typical capacitances $C = f(V_{DS})$, $f = 1$ MHz

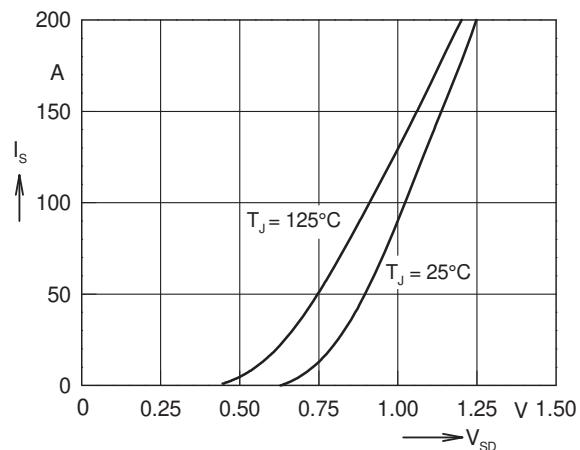


Fig. 10 Typical forward characteristics of reverse diode, $I_S = f(V_{SD})$

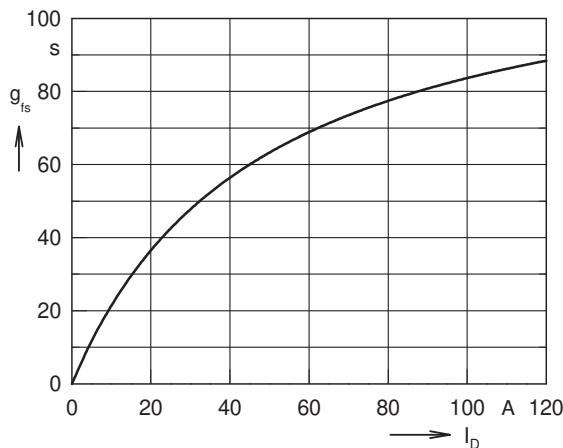


Fig. 11 Typical transconductance $g_{fs} = f(I_D)$

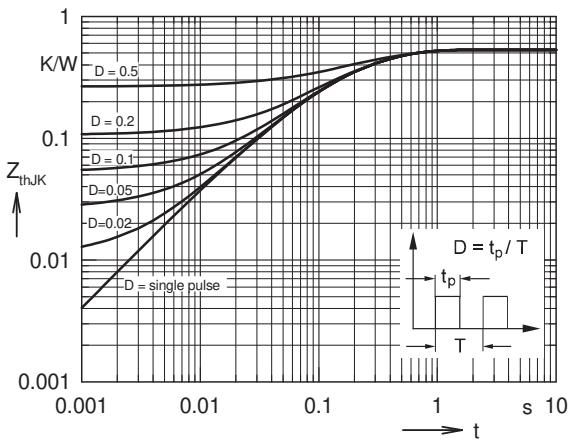


Fig. 12 Transient thermal resistance $Z_{thJK} = f(t_p)$