

-100V P-Channel Power MOSFET



TO-220

ITO-220



Pin Definition:

- 1. Gate
- 2. Drain
- 3. Source

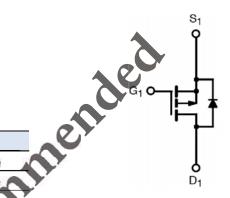
Key Parameter Performance

Parameter		Value	Unit	
V_{DS}		-100	V	
R _{DS(on)} (max)	V _{GS} = -10V	140	mΩ	
	V _{GS} = -4.5V	170		
Q_g		42	nC	

Application

- Networking
- Load Switch
- LED applications

Block Diagram



P-Channel MOSFET

Ordering Information

Pa	rt No.	Package	Packing
TSM22P1	0CZ C0G	TO-220	50pcs / Tube
TSM22P1	OCI COG	ITO-220	50pcs / Tube

Note: "G" denotes for Halogen- and Antimony-free as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds

Absolute Maximum Ratings (Tc = 25°C unless otherwise noted)

Parameter		Ck-al	Limit		1114
		Symbol	TO-220	ITO-220	Unit
Drain-Source Voltage		V _{DS}	-100		V
Gate-Source Voltage		V_{GS}	±	25	V
Operation of Decision Community (Note 1)	Tc = 25°C		-22		Α
Continuous Drain Current (Note 1)	Tc = 100°C	- I _D	-14		Α
Pulsed Drain Current (Note 2)		I _{DM}	-88		Α
Power Dissipation @ T _C = 25°C		P _D	125	48	W
Operating Junction Temperature		T _J	150		°C
Storage Temperature Range		T _{STG}	-55 to +150		°C

Thermal Performance

D	Complete I	Limit		1 lm !4
Parameter	Symbol	TO-220	ITO-220	Unit
Thermal Resistance - Junction to Case	R _{eJC}	1.0	2.6	0000
Thermal Resistance - Junction to Ambient	R _{eJA}	62		°C/W



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Electrical Specifications (T_C = 25°C unless otherwise noted)

Parameter	Conditions	Symbol	Min	Тур	Max	Unit
Static						
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = -250\mu A$	BV _{DSS}	-100			V
Drain-Source On-State Resistance	$V_{GS} = -10V, I_D = -20A$	R _{DS(ON)}		115	140	mΩ
	V _{GS} = -4.5V, I _D = -10A			130	170	
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	$V_{GS(TH)}$	-1		-3	V
Zero Gate Voltage Drain Current	V _{DS} = -100V, V _{GS} = 0V	_			-1	μА
	V _{DS} = -80V, T _J = 125°C	I _{DSS}			-10	
Gate Body Leakage	$V_{GS} = \pm 25V, V_{DS} = 0V$	I _{GSS}	A		±100	nA
Dynamic		A	7			
Total Gate Charge (Note 3,4)		Q_g		42		
Gate-Source Charge (Note 3,4)	$V_{DS} = -50V, I_{D} = -20A,$	Qgs		8		nC
Gate-Drain Charge (Note 3,4)	V _{GS} = -10V	\mathbf{Q}_{gd}		5.6		
Input Capacitance		C _{iss}		2250		
Output Capacitance	$V_{DS} = -30V, V_{GS} = 0V,$ f = 1.0MHz	C_{oss}		130		pF
Reverse Transfer Capacitance	1 - 1.0WIPZ	C _{rss}		90		
Switching						
Turn-On Delay Time (Note 3,4)	0	t _{d(on)}				
Turn-On Rise Time (Note 3,4)	$V_{DD} = -30V$, $I_{D} = -1A$,	t _r				
Turn-Off Delay Time (Note 3,4)	$V_{GS} = -10V, R_G = 6\Omega$	$t_{d(off)}$				ns
Turn-Off Fall Time (Note 3,4)		t _f				
Source-Drain Diode Ratings and Ch	aracteristic		•			
Maximum Continuous Drain Source		I.			-22	А
Diode Forward Current	Integral reverse diode in	Is		-	-22	^
Maximum Pulse Drain-Source Diode	the MOSFET	I _{SM}			-88	Α
Forward Current	V 0V 1 4A				ļ	
Diode-Source Forward Voltage	$V_{GS} = 0V$, $I_S = -1A$	V_{SD}			-1.1	V

Note:

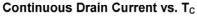
- 1. Limited by maximum junction temperature
- 2. Pulse width limited by safe operating area
- 3. Pulse test: pulse width \leq 300 μ s, duty cycle \leq 2%
- 4. Switching time is essentially independent of operating temperature.

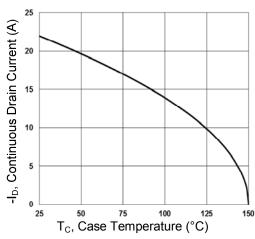


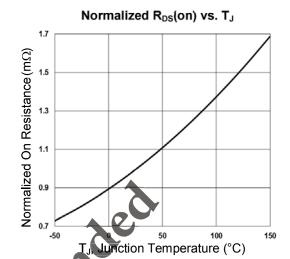
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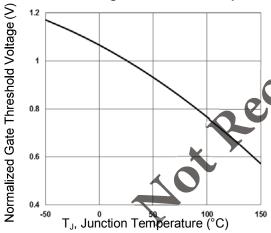
Electrical Characteristics Curve



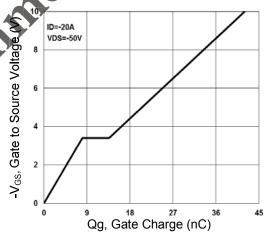




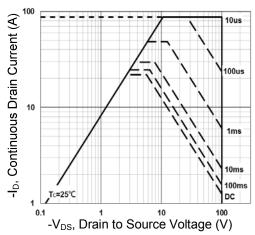
Threshold Voltage vs. Junction Temperature



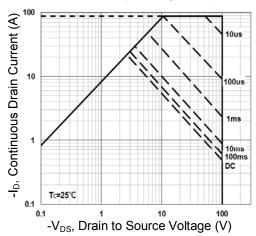
Gate Charge Waveform



Maximum Safe Operating Area (TO-220)



Maximum Safe Operating Area (ITO-220)





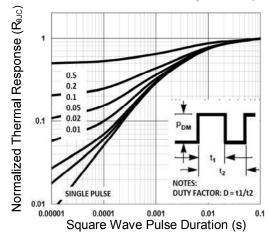




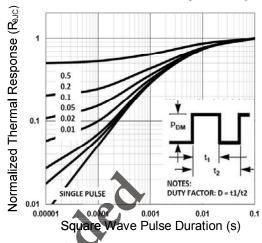
-100V P-Channel Power MOSFET

Electrical Characteristics Curve

Normalized Thermal Transient Impedance (TO-220)



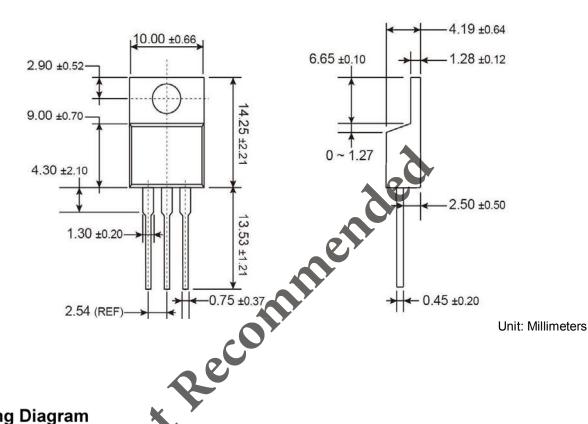
Normalized Thermal Transient Impedance (ITO-220)



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TO-220 Mechanical Drawing



Marking Diagram



lalogen Free Product

Year Code

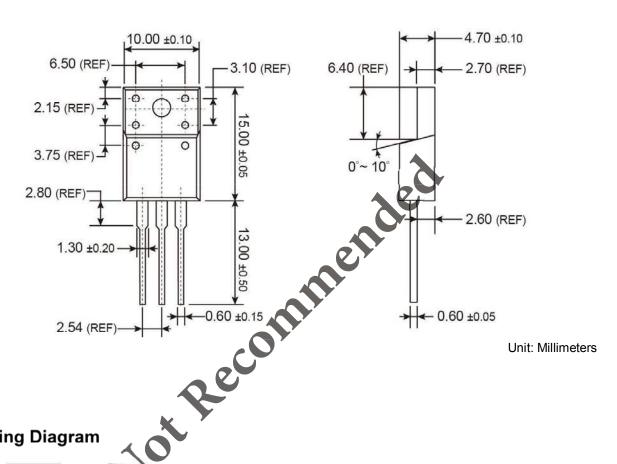
= Week Code (01~52)

= Factory Code

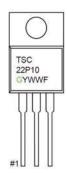


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ITO-220 Mechanical Drawing



Marking Diagram



= Halogen Free Product

= Year Code

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