



12V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
40)/	6.0mΩ @ V _{GS} = 4.5V	14.7A
12V	9.0mΩ @ V _{GS} = 2.5V	12A

Description

This new generation MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- General Purpose Interfacing Switch
- Power Management Functions

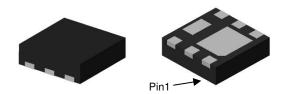
Features

- 0.6mm Profile Ideal for Low-Profile Applications
- PCB Footprint of 4mm²
- Low Gate Threshold Voltage
- Low On-Resistance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

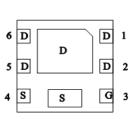
Mechanical Data

- Case: U-DFN2020-6
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Weight: 0.007 grams (Approximate)

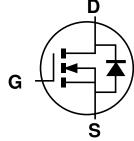
U-DFN2020-6 (Type F)



Top View Bottom View



Pin Out Bottom View



Equivalent Circuit

Ordering Information (Note 4)

Part Number	Case	Packaging
DMN14M8UFDF-7	U-DFN2020-6 (Type F)	3,000/Tape & Reel
DMN14M8UFDF-13	U-DFN2020-6 (Type F)	10,000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



O7 = Product Type Marking Code YWX = Date Code Marking Y = Year (ex: 1 = 2021)

W = Week (ex: a = Week 27; z Represents Week 52 and 53)

X = Internal Code (ex: U = Monday)

Date Code Key

Year	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	9	0	1	2	3	4	5	6	7	8	9	0

Week	1-26	27-52	53
Code	A-Z	a-z	Z

Internal Code	Sun	Mon	Tue	Wed	Thu	Fri	Sat
Code	T	U	V	W	X	Υ	Z



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage	V _{DSS}	12	V		
Gate-Source Voltage	Vgss	±8	V		
Continuous Drain Current (Note 6) V _{GS} = 4.5V	Steady State	$T_A = +25$ °C $T_A = +70$ °C	I _D	14.7 11.8	Α
Pulsed Drain Current (380µs Pulse, Duty Cycle = 1%)			I _{DM}	100	Α
Maximum Body Diode Continuous Current (Note 6)			Is	1.7	Α
Avalanche Current (Note 7) L = 0.1mH			las	5.6	Α
Avalanche Energy (Note 7) L = 0.1mH			Eas	1.6	mJ

Thermal Characteristics

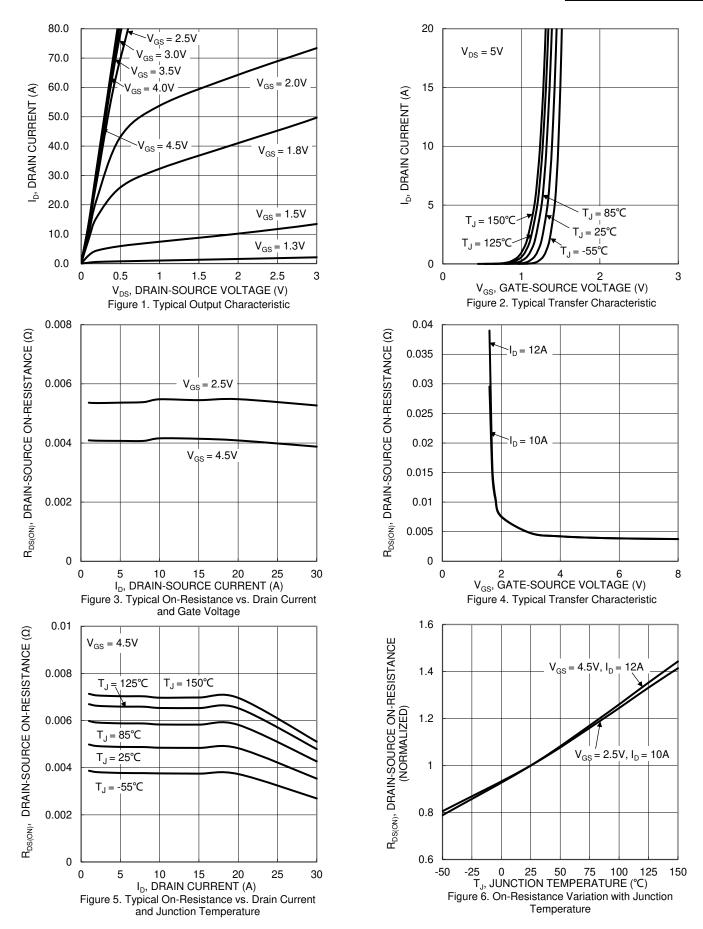
Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T _A = +25°C	PD	1.1	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	109.5	°C/W
Total Power Dissipation (Note 6)	T _A = +25°C	PD	1.9	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Reja	63.8	°C/W
Thermal Resistance, Junction to Case (Note 6)		Rejc	9	- C/VV
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

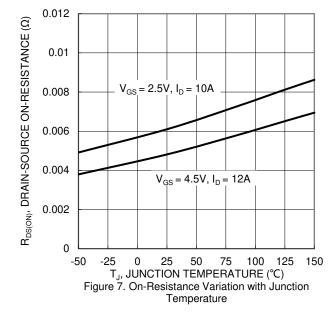
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Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	12	_	_	V	$V_{GS} = 0V, I_{D} = 250\mu A$
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}		_	1	μΑ	$V_{DS} = 9.6V, V_{GS} = 0V$
Gate-Source Leakage	Igss	_	_	±100	nA	$V_{GS} = \pm 8V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS} (TH)	0.45	_	1.2	٧	$V_{DS} = V_{GS}$, $I_D = 250\mu A$
Static Drain-Source On-Resistance	Decrees	_	4.8	6.0	mΩ	$V_{GS} = 4.5V, I_{D} = 12A$
Static Drain-Source On-Nesistance	RDS(ON)		6.1	9.0	11122	$V_{GS} = 2.5V, I_D = 10A$
Diode Forward Voltage	V _{SD}	_	0.69	1.2	V	$V_{GS} = 0V, I_{S} = 3.2A$
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	Ciss		1246	_	рF	V 0V V 0V
Output Capacitance	Coss	_	454	_	рF	$V_{DS} = 6V, V_{GS} = 0V,$ - f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	352	_	рF	T = 1.0IVII IZ
Gate Resistance	Rg	_	5	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	14.6	_	nC	
Total Gate Charge (V _{GS} = 10V)	Qg	_	29.5	_	nC	\/ C\/
Gate-Source Charge	Qgs	_	1.2	_	nC	$V_{DS} = 6V$, $I_D = 10A$
Gate-Drain Charge	Qgd	_	4.9	_	nC	
Turn-On Delay Time	td(ON)	_	4.7	_	ns	
Turn-On Rise Time	t _R	_	5	_	ns	$V_{DS} = 6V, I_{D} = 5.0A$
Turn-Off Delay Time		_	36.6	_	ns	$V_{GS} = 4.5V, R_{G} = 1.0\Omega$
Turn-Off Fall Time	tF	_	19	_	ns	
Reverse Recovery Time	t _{RR}	_	24.5	_	ns	1 000 11/-14 1000/
Reverse Recovery Charge	Qrr		7	_	nC	$I_F = 2.0A$, $di/dt = 100A/\mu s$

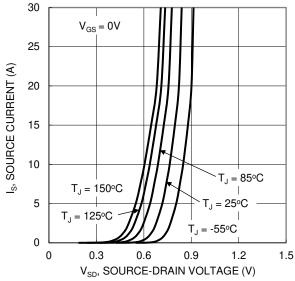
- 5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.
- 7. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep $T_J = +25$ °C.
- Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.

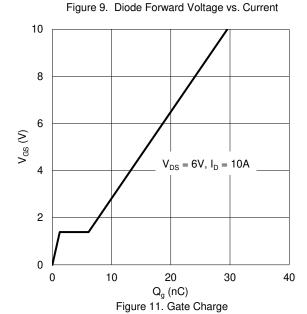


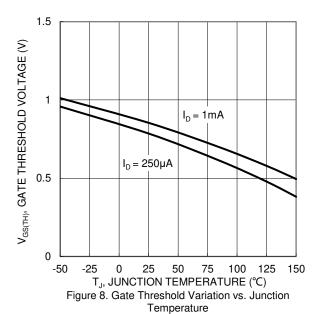


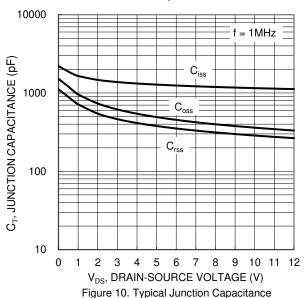


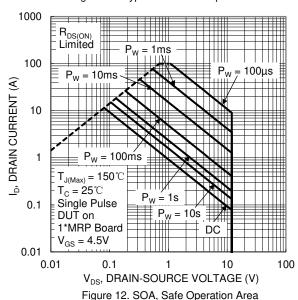














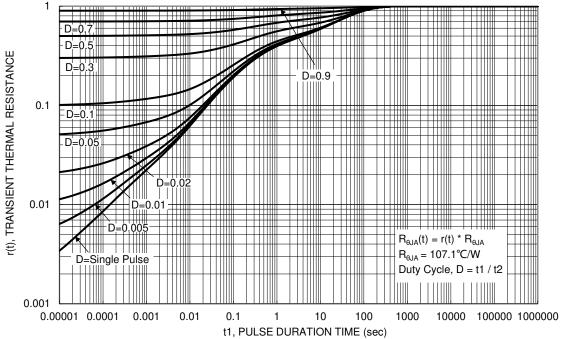


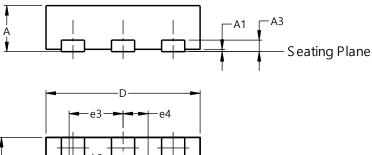
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type F)



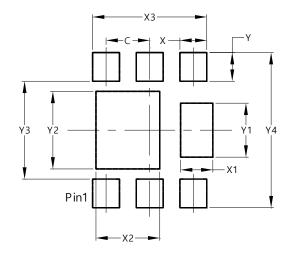
	D
	e3
1	- - - - - - - - - -
D2a	D2-
E E2a	
z1—	k l l l l l l l l l l l l l l l l l l l
z(4x)—	e — b — b

	U-DFN	2020-6						
		pe F)						
Dim	Min	Max	Тур					
Α	0.57	0.63	0.60					
A1	0.00	0.05	0.03					
A3	-	0.15						
b	0.25	0.25 0.35 0.30						
D	1.95	1.95 2.05 2.00						
D2	0.85 1.05 0.95							
D2a	0.33							
Е	1.95	2.05	2.00					
E2	1.05	1.25	1.15					
E2a	0.65 0.75 0.70							
е		0.65 BS	С					
e2	C).863 BS	SC					
e3		0.70 BS	С					
e4	C).325 BS	SC					
k		0.37 BS						
k1		0.15 BS	C					
k2		0.36 BS	С					
L	0.225 0.325 0.275							
Z		0.20 BS	С					
z 1	C).110 BS	SC					
z2		0.20 BS	С					
All C	imens	ions in	mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type F)



Value
(in mm)
0.650
0.400
0.480
0.950
1.700
0.425
0.800
1.150
1.450
2.300



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