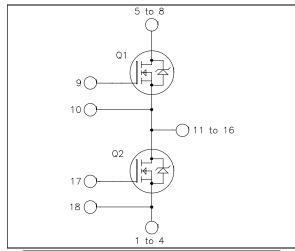
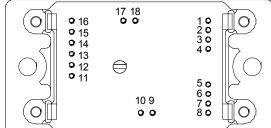


Phase leg Super Junction MOSFET Power Module

$$\begin{split} V_{DSS} &= 600 V \\ R_{DSon} &= 42 m \Omega \ max \ @\ Tj = 25^{\circ} C \\ I_D &= 66 A \ @\ Tc = 25^{\circ} C \end{split}$$





Pins 1/2/3/4 ; 5/6/7/8 ; 11/12/13/14/15/16 must be shorted together

Application

- Welding converters
- Switched Mode Power Supplies
- Uninterruptible Power Supplies
- Motor control

Features

- CoolMOSTM
 - Ultra low R_{DSon}
 - Low Miller capacitance
 - Ultra low gate charge
 - Avalanche energy rated
 - Very rugged
 - Fast intrinsic diode
- Very low stray inductance
- Kelvin source for easy drive
- High level of integration

Benefits

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Low profile
- RoHS Compliant

All ratings (a) $T_i = 25^{\circ}C$ unless otherwise specified

Absolute maximum ratings

Absolute maximum ratings							
Symbol	Parameter		Max ratings	Unit			
$V_{ m DSS}$	Drain - Source Breakdown Voltage		600	V			
I_D	Continuous Drain Current	$T_c = 25^{\circ}C$	66				
		$T_c = 80^{\circ}C$	49	Α			
I_{DM}	Pulsed Drain current		200				
V_{GS}	Gate - Source Voltage		±20	V			
R_{DSon}	Drain - Source ON Resistance		42	mΩ			
P_{D}	Maximum Power Dissipation $T_c = 25^{\circ}C$		416	W			
I_{AR}	Avalanche current (repetitive and non repetitive)		20	A			
E _{AR}	Repetitive Avalanche Energy		1	mJ			
E_{AS}	Single Pulse Avalanche Energy		1800	1117			

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com



Electrical Characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
I_{DSS}	Zero Gate Voltage Drain Current	$V_{GS} = 0V, V_{DS} = 600V$			100	μA
R _{DS(on)}	Drain – Source on Resistance	$V_{GS} = 10V, I_D = 33A$			42	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 6mA$	3	4	5	V
I_{GSS}	Gate – Source Leakage Current	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$			±200	nA

Dynamic Characteristics

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit	
C_{iss}	Input Capacitance	$V_{GS} = 0V$	$ V_{GS} = 0V $ $V_{DS} = 25V $		14.6		nF	
C_{oss}	Output Capacitance				3.47			
C_{rss}	Reverse Transfer Capacitance	f = 1MHz			0.082			
Q_{g}	Total gate Charge	$V_{GS} = 10V$	$V_{GS} = 10V$ $V_{Bus} = 300V$		510		nC	
Q_{gs}	Gate – Source Charge	$V_{\text{Bus}} = 300 \text{V}$			86			
Q_{gd}	Gate – Drain Charge	$I_{D} = 66A$			270			
$T_{d(on)}$	Turn-on Delay Time	Inductive Switching	Inductive Switching @ 125°C V _{GS} = 15V		21			
T_{r}	Rise Time				30			
$T_{d(off)}$	Turn-off Delay Time	$I_{\rm Bus} = 400 \text{ V}$ $I_{\rm D} = 66 \text{ A}$	$V_{\text{Bus}} = 400V$ $I_{\text{D}} = 66A$		240		ns	
T_{f}	Fall Time	$R_G = 2.5\Omega$	b		52			
Е	Turn-off Switching Energy	Inductive switching	$T_j = 25$ °C		1.18		1	
E _{off}		$V_{GS} = 15V ; I_D = 66A$ $V_{Bus} = 400V ; R_G = 2.5\Omega$	$T_j = 125$ °C		1.45		- mJ	
R_{thJC}	Junction to Case Thermal Resistan	ice				0.3	°C/W	

Source - Drain diode ratings and characteristics

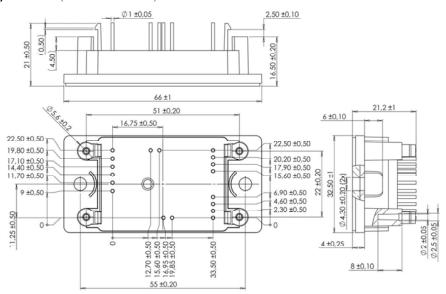
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Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit	
I_S	Continuous Source current		Tc = 25°C		66		A	
	(Body diode)		Tc = 80°C		49		A	
V_{SD}	Diode Forward Voltage	$V_{GS} = 0V, I_S = -66A$	$V_{GS} = 0V, I_S = -66A$			1.2	V	
dv/dt	Peak Diode Recovery					40	V/ns	
t_{rr}	Reverse Recovery Time	$I_S = -66A$	$T_j = 125$ °C		350		ns	
Qrr	Reverse Recovery Charge	$V_{R} = 400V$ $di_{S}/dt = 200A/\mu s$	T _j =125°C		5.4		μС	

Thermal and package characteristics

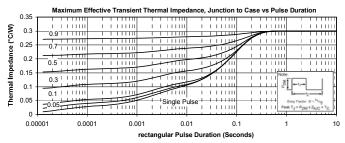
Symbol	Characteristic		Min	Typ	Max	Unit	
V_{ISOL}	RMS Isolation Voltage, any terminal to case t = 1 min, 50/60Hz			4000			V
T_{J}	Operating junction temperature range			-40		150	
T_{STG}	Storage Temperature Range			-40		125	°C
$T_{\rm C}$	Operating Case Temperature			-40		100	
Torque	Mounting torque	To heatsink	M4	2		3	N.m
Wt	Package Weight					75	g

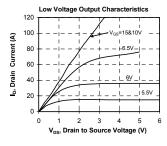


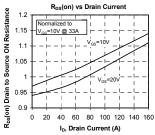
SP2 Package outline (dimensions in mm)

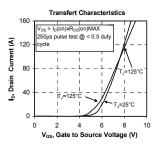


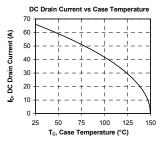
Typical Performance Curve



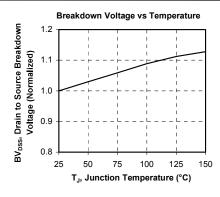


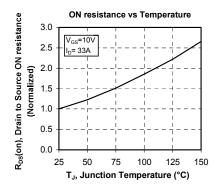


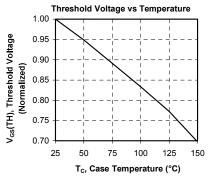


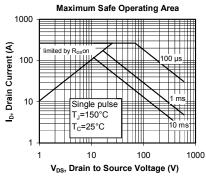


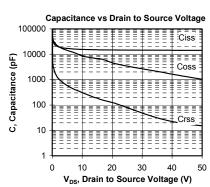


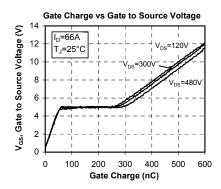




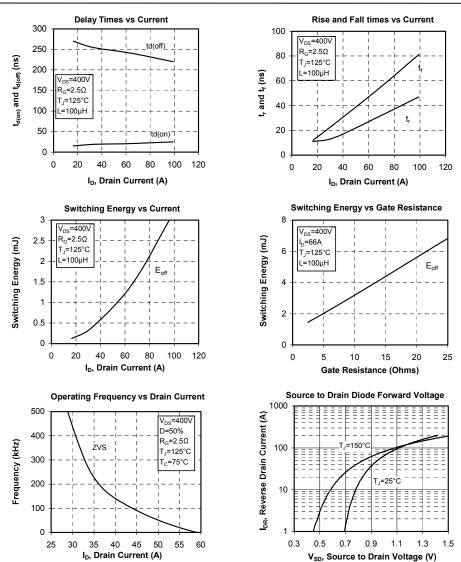












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