

DUAL P-CHANNEL ENHANCEMENT MODE MOSFET

Features

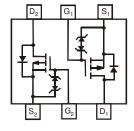
- Dual P-Channel MOSFET
- Low On-Resistance
- Very Low Gate Threshold Voltage V_{GS(TH)} < 1V
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SOT26
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.015 grams (Approximate)







Top View Internal Schematic

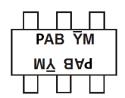
Ordering Information (Note 4)

Part Number	Case	Packaging
DMP2004DMK-7	SOT26	3000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



 $\begin{array}{l} \underline{P}AB = Marking \ Code \\ \overline{Y}M = Date \ Code \ Marking \\ Y \ or \ \overline{Y} = Year \ (ex: D = 2016) \\ M = Month \ (ex: 9 = September) \end{array}$

Date Code Key

Year	2007	~	2016	2017	7 201	18 20	19	2020	2021	2022	2023	2024
Code	U	~	D	Е	F		G	Н	I	J	K	L
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Au	g Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



$\hline \textbf{Maximum Ratings} \ (@T_A = +25^{\circ}C, \ \text{unless otherwise specified.})$

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	-20	V
Gate-Source Voltage	V_{GSS}	±8	V
Drain Current (Note 5)	I _D	-550	mA
Pulsed Drain Current	I _{DM}	-1.9	A

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P_{D}	500	mW
Thermal Resistance, Junction to Ambient (Note 5)	$R_{ hetaJA}$	250	°C/W
Operating and Storage Temperature Range	$T_{J_1}T_{STG}$	-55 to +150	°C

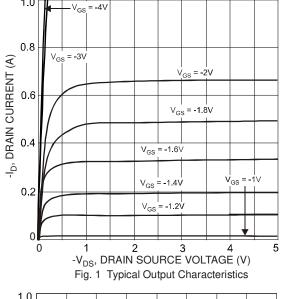
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

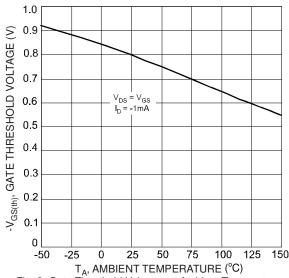
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)							
Drain-Source Breakdown Voltage	BV _{DSS}	-20	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	-1.0	μΑ	$V_{DS} = -20V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±1.0	μΑ	$V_{GS} = \pm 4.5V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage	V _{GS(TH)}	-0.5		-1.0	V	$V_{DS} = V_{GS}, I_D = -250 \mu A$	
			0.7	0.9		$V_{GS} = -4.5V$, $I_D = -430mA$	
Static Drain-Source On-Resistance	R _{DS (ON)}	_	1.1	1.4	Ω	$V_{GS} = -2.5V, I_D = -300mA$	
			1.7	2.0		$V_{GS} = -1.8V, I_D = -150mA$	
Forward Transfer Admittance	Y _{fs}	200	_	_	mS	$V_{DS} = -10V, I_{D} = -0.2A$	
Diode Forward Voltage (Note 6)	V _{SD}	-0.5	_	-1.2	V	$V_{GS} = 0V, I_{S} = 115mA$	
DYNAMIC CHARACTERISTICS							
Input Capacitance	C _{iss}	_	_	175	pF	101/1/	
Output Capacitance	Coss	_	_	30	pF	$V_{DS} = -16V, V_{GS} = 0V$ - f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}		_	20	pF	71 = 1.0IVIDZ	

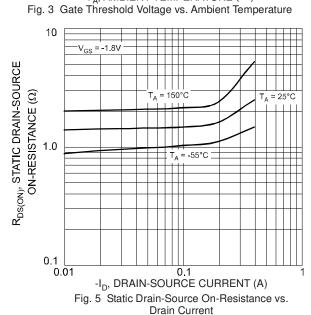
Notes:

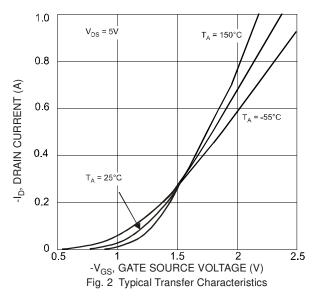
- 5. Device mounted on FR-4 PCB.6. Short duration pulse test used to minimize self-heating effect.

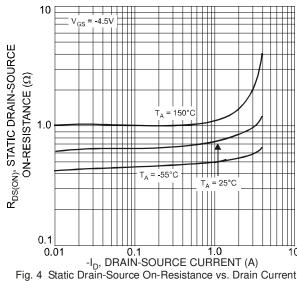












10 T_A = 25°C T_A

Drain-Source Current vs. Gate Source Voltage



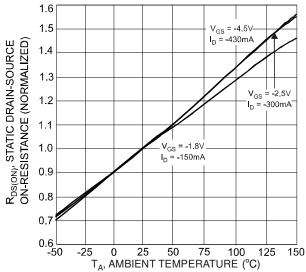


Fig. 7 Static Drain-Source On-State Resistance vs. Ambient Temperature

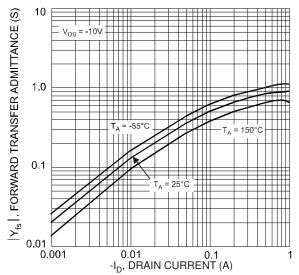


Fig. 9 Forward Transfer Admittance vs. Drain Current

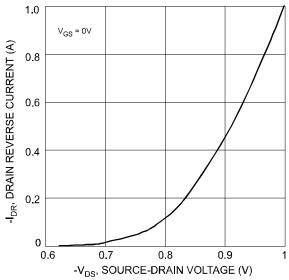
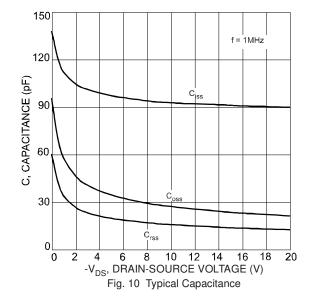


Fig. 8 Drain Reverse Current vs. Source-Drain Voltage

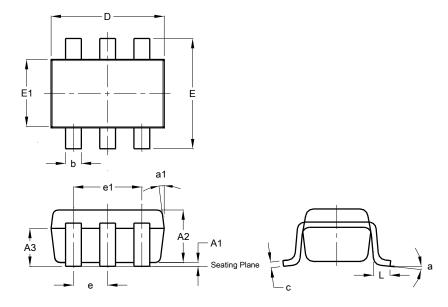




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT26

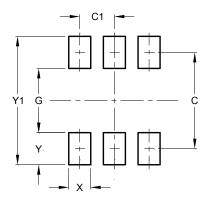


SOT26						
Dim	Min	Max	Тур			
A1	0.013	0.10	0.05			
A2	1.00	1.30	1.10			
A3	0.70	0.80	0.75			
b	0.35	0.50	0.38			
С	0.10	0.20	0.15			
D	2.90	3.10	3.00			
е	-	-	.95			
e1	-	-	1.90			
Е	2.70	3.00	2.80			
E1	1.50	1.70	1.60			
L	0.35	0.55	0.40			
а	-	-	8°			
a1	-	-	7°			
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT26



Dimensions	Value (in mm)
С	2.40
C1	0.95
G	1.60
Х	0.55
Υ	0.80
Y1	3.20



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