



DMP3056LDM

Product Summary

BV _{DSS}	RDS(ON) Max	ID TA = +25°C
-30V	45mΩ @ V _{GS} = -10V	-4.3A
	65mΩ @ V _{GS} = -4.5V	-3.3A

Description

This new generation MOSFET has been designed to minimize the onstate resistance ($R_{DS(ON)}$) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- General purpose interfacing switches
- Power management functions
- Analog switches

P-CHANNEL ENHANCEMENT MODE MOSFET

Features

- Low Gate Threshold Voltage
- Low On-Resistance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

https://www.diodes.com/products/automotive/automotiveproducts/.

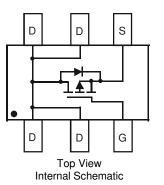
This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.
<u>https://www.diodes.com/quality/product-definitions/</u>

Mechanical Data

- Package: SOT26
- Package Material Molded Plastic, "Green" Molding Compound. UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Terminal Connections: See Diagram
- Weight: 0.016 grams (Approximate)







Ordering Information (Note 4)

Part Number	Paakaga	Packing		
Fait Nulliber	Package	Qty.	Carrier	
DMP3056LDM-7	SOT26	3000	Tape & Reel	

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

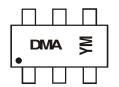
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information



DMA = Product Type Marking Code YM = Date Code Marking Y = Year (ex: J = 2022) M = Month (ex: 8 = August)

Date Code Key

Year	2008		2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Code	V		J	K	L	М	Ν	0	Р	R	S	Т
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code		0	0	4	~	0	7	0	0	<u> </u>	Ν	

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage		VDSS	-30	V	
Gate-Source Voltage			Vgss	±20	V
	Steady State	TA = +25°C	ID	-4.3	А
Continuous Drain Current (Note 5) V _{GS} = -10V	t < 10s	$T_A = +25^{\circ}C$	ID	-5.8	А
Maximum Continuous Body Diode Forward Curr	ent (Note 5)		ls	-2.3	А
Pulsed Drain Current (10µs Pulse, Duty Cycle =	1%)		ldм	-13	А

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 6)	T _A = +25°C	PD	1.25	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Reja	100	°C/W
Total Power Dissipation (Note 5)	TA = +25°C	PD	1.5	W
Thermal Resistance, Junction to Ambient (Note 5) Steady State		Reja	86	0 0 AN
Thermal Resistance, Junction to Case	Rejc	15.6	°C/W	
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	٥°

Notes:

Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad.
Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

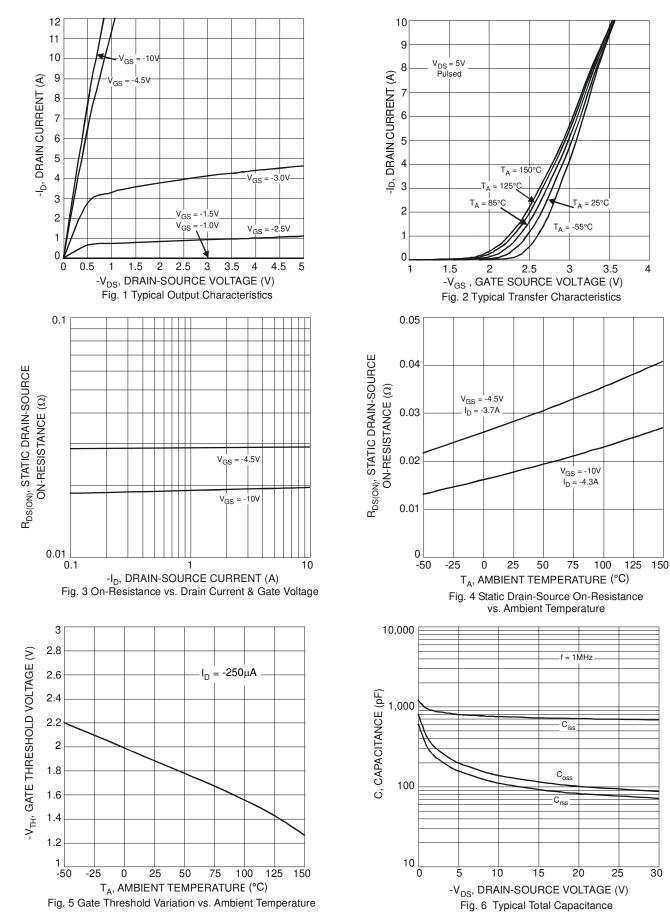


Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

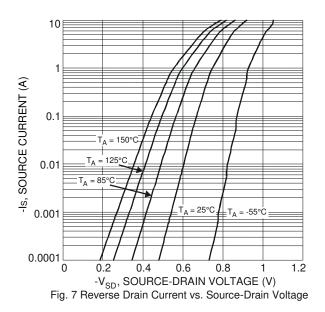
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
STATIC PARAMETERS (Note 7)							
Drain-Source Breakdown Voltage	BVDSS	-30	_		V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current $T_J = +25^{\circ}C$	IDSS	_	—	-1	μΑ	$V_{GS} = 0V, V_{DS} = -30V$	
Gate-Body Leakage Current		_		±100 ±800	nA		
Gate Threshold Voltage	VGS(TH)	-1.0	_	-2.1	V	$V_{GS} = V_{DS}$, $I_D = -250 \mu A$	
Static Drain-Source On-Resistance		_	-	45 65	mΩ	$V_{GS} = -10V, I_D = -5A$ $V_{GS} = -4.5V, I_D = -4.2A$	
Forward Transconductance	g FS	—	8	—	S	$V_{DS} = -10V, I_{D} = -4.3A$	
Diode Forward Voltage	V _{SD}	—	_	-1.2	V	$V_{GS} = 0V, I_{S} = -1.7A$	
DYNAMIC PARAMETERS (Note 8)							
Input Capacitance Output Capacitance Reverse Transfer Capacitance		—	948	—	pF		
		—	105	—	pF	└──VGS = 0V, VDS = -25V - f = 1.0MHz	
		_	100	—	pF	1 - 1.00012	
SWITCHING CHARACTERISTICS (Note 8)							
Total Gate Charge		_	10.1	_	nC	V _{DS} = -15V, V _{GS} = -4.5V I _D = -6A	
	Qg	_	21.1	—			
Gate-Source Charge	Qgs	_	2.8	—	nC	V _{DS} = -15V, V _{GS} = -10V I _D = -6A	
Gate-Drain Charge	Qgd	—	3.2	—			
Gate Resistance	Rg	_	13.15	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Turn-On Delay Time	t _{D(ON)}	—	10.2	—			
Rise Time	tR	_	6.6	_		V _{DS} = -15V, V _{GS} = -10V	
Turn-Off Delay Time	tD(OFF)	_	50.1	_	ns	$I_D=-1A,\ R_g=6.0\Omega$	
Fall Time	tF	_	22.3	_			

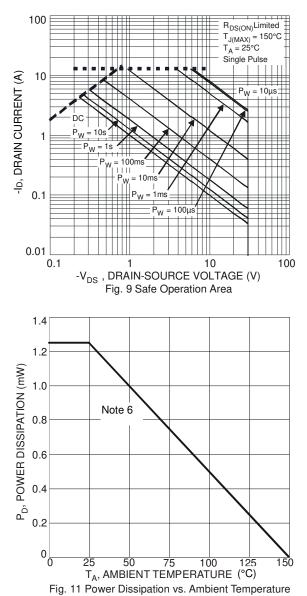
 Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to product testing. Notes:

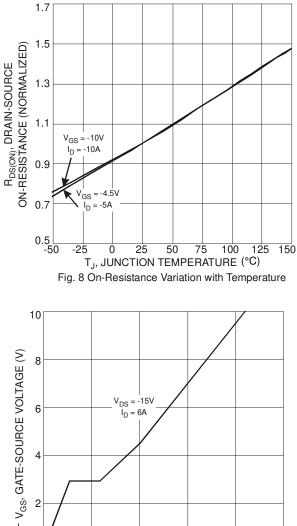


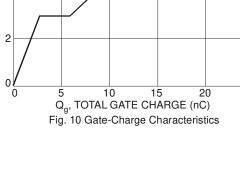






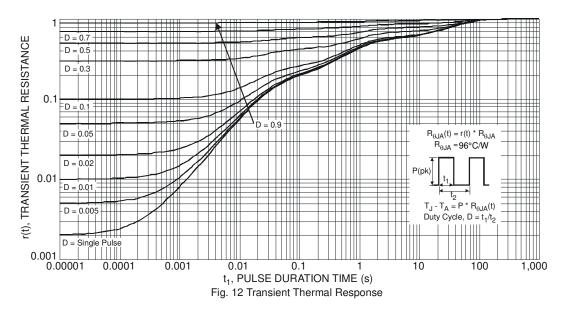






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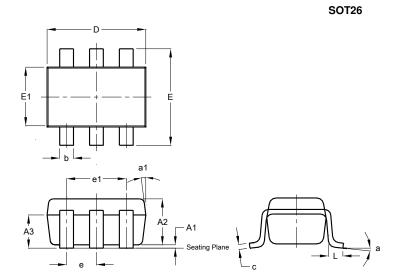






Package Outline Dimensions

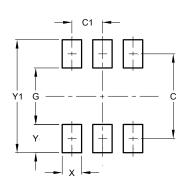
Please see http://www.diodes.com/package-outlines.html for the latest version.



	SOT26							
Dim	Min	Max	Тур					
A1	0.013	0.10	0.05					
A2	1.00	1.30	1.10					
A3	0.70	0.80	0.75					
b	0.35	0.50	0.38					
С	0.10	0.20	0.15					
D	2.90	3.10	3.00					
е	-	-	0.95					
e1	-	-	1.90					
Е	2.70	3.00	2.80					
E1	1.50	1.70	1.60					
L	0.35	0.55	0.40					
а	-	-	8°					
a1	-	-	7°					
All	Dimen	sions	in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	2.40
C1	0.95
G	1.60
Х	0.55
Y	0.80
Y1	3.20

SOT26



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