

[J-Link Debug Probes](#) [Adapters](#) [Xilinx Adapter](#)

## J-Link Xilinx Adapter

The J-Link Xilinx Adapter connects to the 14-pin 2 mm Xilinx JTAG connector providing debug access to FPGA based MCU cores like the ARM Cortex-A9 core in the Zynq devices.



NC	1 •• 2	VREF
GND	3 •• 4	TMS
GND	5 •• 6	TCK
GND	7 •• 8	TDO
GND	9 •• 10	TDI
GND	11 •• 12	NC
GND	13 •• 14	HALT