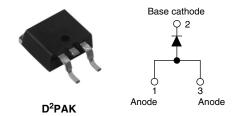


Vishay High Power Products

Input Rectifier Diode, 20 A



PRODUCT SUMMARY					
V _F at 10 A	1 V				
I _{FSM}	300 A				
V _{RRM}	800/1200 V				

DESCRIPTION/FEATURES

The 20ETS...S rectifier High Voltage Series has been optimized for very low forward voltage drop, with moderate leakage. The glass passivation technology used has reliable operation up to 150 °C junction temperature.

Typical applications are in input rectification and these products are designed to be used with Vishay HPP switches and output rectifiers which are available in identical package outlines.

This product series has been designed and qualified for industrial level.

OUTPUT CURRENT IN TYPICAL APPLICATIONS						
APPLICATIONS SINGLE-PHASE BRIDGE THREE-PHASE BRIDGE UNITS						
Capacitive input filter $T_A = 55 \text{ °C}$, $T_J = 125 \text{ °C}$ common heatsink of 1 °C/W	16.3	21	А			

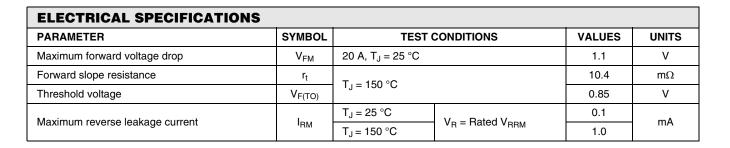
MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I _{F(AV)}	Sinusoidal waveform	20	A			
V _{RRM}		800/1200	V			
I _{FSM}		300	A			
V _F	20 A, T _J = 25 °C	1.1	V			
TJ		- 40 to 150	°C			

VOLTAGE RATINGS								
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} AT 150 °C mA					
20ETS08S	800	900	1					
20ETS12S	1200	1300						

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum average forward current	I _{F(AV)}	T_C = 105 °C, 180° conduction half sine wave	20			
Maximum peak one cycle		10 ms sine pulse, rated V_{RRM} applied	250	А		
non-repetitive surge current	IFSM	10 ms sine pulse, no voltage reapplied	300]		
Maximum I ² t for fusing	l ² t	10 ms sine pulse, rated V_{RRM} applied	316	— A ² s		
	1-1	10 ms sine pulse, no voltage reapplied 442		A-5		
Maximum I ² \sqrt{t} for fusing	l²√t	t = 0.1 to 10 ms, no voltage reapplied	4420	A²√s		

20ETS...S High Voltage Series

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THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		BOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature	e range T _J , T	Stg		- 40 to 150	°C	
Maximum thermal resistance, junction to case		JC	DC operation	1.3		
Maximum thermal resistance, junction to ambient		A ⁽¹⁾	For D ² PAK version	62	°C/W	
Typical thermal resistance, case to heatsink		cs	Mounting surface, smooth and greased	0.5		
Approximate weight				2	g	
				0.07	oz.	
	inimum			6.0 (5.0)	kgf ⋅ cm (lbf ⋅ in)	
Mounting torque ma	aximum			12 (10)		
Marking device			$P_{\rm resc}$ at the $P_{\rm r}^{2}PAI((2MP, 222))$	20ET	S08S	
			Case style D ² PAK (SMD-220)	20ET	20ETS12S	

Note

(1) When mounted on 1" square (650 mm²) PCB of FR-4 or G-10 material 4 oz. (140 μm) copper 40 °C/W

For recommended footprint and soldering techniques refer to application note #AN-994





20ETS...S High Voltage Series

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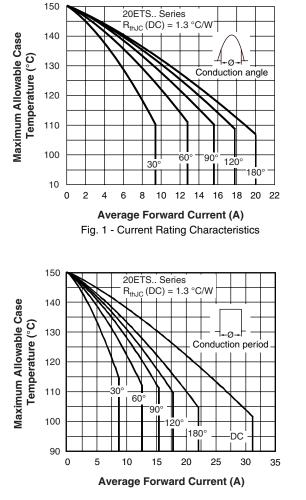
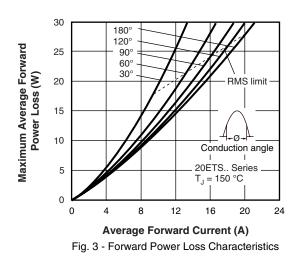


Fig. 2 - Current Rating Characteristics



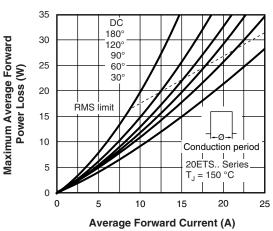
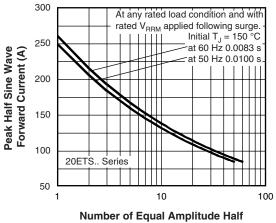
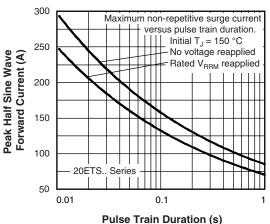
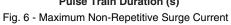


Fig. 4 - Forward Power Loss Characteristics



Cycle Current Pulse (N) Fig. 5 - Maximum Non-Repetitive Surge Current





20ETS...S High Voltage Series

Vishay High Power Products Input Rectifier Diode, 20 A



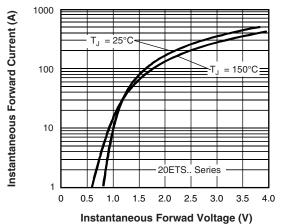


Fig. 7 - Forward Voltage Drop Characteristics

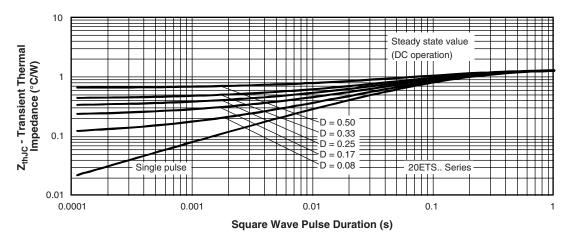


Fig. 8 - Thermal Impedance Z_{thJC} Characteristics



Input Rectifier Diode, 20 A Vishay High Power Products

ORDERING INFORMATION TABLE

Device code	20	Е	т	S	12	S	TRL	-	
		2	3	4	5	6	7	8	
	1 - 2 -	Circ	uit confi	ng (20 = guratior	-				
	3 -	Pac	E = Single diode Package: T = TO-220AC						
	4 -		e of silic Standar	on: d recove	ery recti	fier			
	5 - 6 -		-	le x 100 D ² PAK			sion	08 = 8 12 = 12	
	7 -	• No	one = Tu						
	8 -	• TF • No	RR = Ta one = St	pe and r andard ad (Pb)-f	eel (righ producti	nt orient			

LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95046				
Part marking information	http://www.vishay.com/doc?95054			
Packaging information	http://www.vishay.com/doc?95032			



Vishay

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