



#### **50V NPN SMALL SIGNAL TRANSISTOR IN DFN1006**

#### **Features**

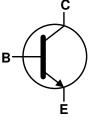
- BV<sub>CEO</sub> > 50V
- I<sub>C</sub> = 100mA High Collector Current
- P<sub>D</sub> = 1000mW Power Dissipation
- 0.60mm<sup>2</sup> Package Footprint, 13 times Smaller than SOT23
- 0.5mm Height Package Minimizing Off-Board Profile
- Complementary PNP Type 2DA1774QLP
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free, "Green" Device (Note 3)
- Qualified to AEC-Q101Standards for High Reliability

### **Mechanical Data**

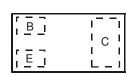
- Case: X1-DFN1006-3
- Case Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu. Solderable per MIL-STD-202, Method 208 @
- Weight: 0.0009 grams (Approximate)



Bottom View



Device Symbol



Top View Device Schematic

#### Ordering Information (Note 4)

Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
2DC4617QLP-7	8D	7	8	3,000
2DC4617QLP-7B	8D	7	8	10,000

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http"//www.diodes.com/products/packages.html.

# **Marking Information**

2DC4617QLP-7	Top View       Dot Denotes Collector Side       Image: Collector Side	From date code 1527 (YYWW), this changes to: Top View Bar Denotes Base and Emitter Side	
2DC4617QLP-7B	Top View Bar Denotes Base and Emitter Side	8D = Product Type Marking Code	



# Absolute Maximum Ratings (@T<sub>A</sub> = +25°C unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	50	V
Collector-Emitter Voltage	V <sub>CEO</sub>	50	V
Emitter-Base Voltage	V <sub>EBO</sub>	5.0	V
Collector Current	lc	100	mA
Peak Collector Current	I <sub>CM</sub>	200	mA

#### Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Power Dissignation	(Note 5)	D	400	mW	
Power Dissipation	(Note 6)		1000	111VV	
Thermal Desistance, Junction to Ambient	(Note 5)		310	°C/W	
Thermal Resistance, Junction to Ambient	(Note 6)	R <sub>0JA</sub>	120		
Thermal Resistance, Junction to Lead	(Note 7)	R <sub>0JL</sub>	120	°C/W	
Operating and Storage and Temperature Rang	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C		

### ESD Ratings (Note 8)

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	ЗA
Electrostatic Discharge - Machine Model	ESD MM	200	V	В

#### Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

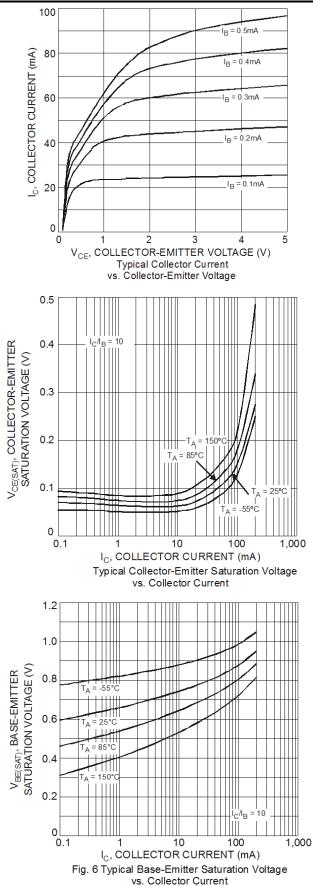
Characteristic	Symbol	Min	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 9)					
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	50		V	$I_{C} = 50 \mu A, I_{E} = 0$
Collector-Emitter Breakdown Voltage	BV <sub>CEO</sub>	50		V	$I_{\rm C} = 1.0 {\rm mA}, I_{\rm B} = 0$
Emitter-Base Breakdown Voltage	BV <sub>EBO</sub>	5.0		V	$I_E = 50 \mu A, I_C = 0$
Collector Cutoff Current	Dilector Cutoff Current	_	100	nA	$V_{CB} = 30V$
Collector Guton Current			5	μA	$V_{CB} = 30V, T_A = +150^{\circ}C$
Emitter Cutoff Current	I <sub>EBO</sub>	_	100	nA	$V_{EB} = 4.0V$
ON CHARACTERISTICS (Note 9)				-	
DC Current Gain	h <sub>FE</sub>	120	270	—	$V_{CE} = 6.0V, I_{C} = 1.0mA$
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>		0.2	V	$I_{\rm C} = 50 {\rm mA}, I_{\rm B} = 5.0 {\rm mA}$
SMALL SIGNAL CHARACTERISTICS					
Output Capacitance	C <sub>obo</sub>		3.5	pF	V <sub>CB</sub> = 12V, f = 1.0MHz, I <sub>E</sub> = 0
Current Gain-Bandwidth Product	f <sub>T</sub>	100		MHz	$V_{CE} = 12V, I_C = 2.0mA, f = 100MHz$

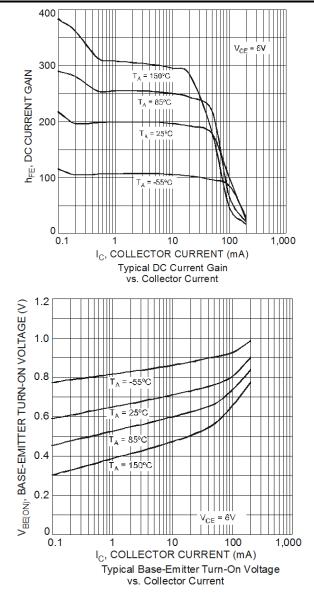
5. For the device mounted on minimum recommended pad layout 1oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured under still air conditions whilst operating in steady state condition. The entire exposed collector pad is attached to the heatsink.
6. Same as Note 5, except the exposed collector pad is mounted on 25mm x 25mm 2oz copper.
7. Thermal resistance from junction to solder-point (on the exposed collector pad).
8. Refer to JEDEC specification JESD22-A114 and JESD22-A115.
9. Measured under pulsed conditions. Pulse width ≤ 300µs. Duty cycle ≤ 2%. Notes:



# 2DC4617QLP

### Typical Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

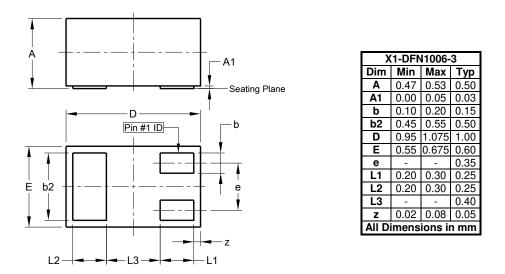






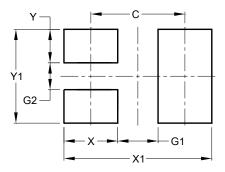
#### **Package Outline Dimensions**

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



### Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)		
С	0.70		
G1	0.30		
G2	0.20		
Х	0.40		
X1	1.10		
Ŷ	0.25		
Y1	0.70		



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