



Product data sheet

1. Product profile

1.1 General description

Silicon Monolithic Microwave Integrated Circuit (MMIC) wideband amplifier with internal matching circuit in a 6-pin SOT363 SMD plastic package.

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Therefore care should be taken during transport and handling.

1.2 Features and benefits

- Internally matched to 50 Ω
- Wide frequency range (3.2 GHz at 3 dB bandwidth)
- Flat 23 dB gain (±1 dB up to 2.7 GHz)
- 9 dBm output power at 1 dB compression point
- Good linearity for low current (IP3_{out} = 22 dBm)
- Low second harmonic; -38 dBc at P_L = -5 dBm
- Unconditionally stable ($K \ge 1.2$).

1.3 Applications

- LNB IF amplifiers
- Cable systems
- ISM
- General purpose.

1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _S	DC supply voltage		-	5	6	V
ls	supply current		-	15.9	-	mA
s ₂₁ ²	insertion power gain	f = 1 GHz	-	22.9	-	dB
NF	noise figure	f = 1 GHz	-	5.3	-	dB
P _{L(sat)}	saturated load power	f = 1 GHz	-	11.6	-	dBm



MMIC wideband amplifier

2. Pinning information

Table 2.	Pinning		
Pin	Description	Simplified outline	Symbol
1	V _S	□6 □5 □4	
2, 5	GND2		\sim
3	RF_OUT		6
4	GND1		
6	RF_IN		4 2, 5 777 777 sym052

3. Ordering information

Table 3. Orde	Table 3. Ordering information						
Type number	Package						
	Name	Description	Version				
BGA2716	-	plastic surface mounted package; 6 leads	SOT363				

4. Marking

Table 4.	Marking	
Type num	per	Marking code
BGA2716		B7-

5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V _S	DC supply voltage	RF input AC coupled	-	6	V
l _S	supply current		-	30	mA
P _{tot}	total power dissipation	$T_{sp} \le 90$ °C	-	200	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		-	150	°C
PD	maximum drive power		-	-10	dBm

6. Thermal characteristics

Table 6.	Thermal characteristics				
Symbol	Parameter	Conditions	Тур	Unit	
$R_{th(j-sp)}$	thermal resistance from junction to solder point	P_{tot} = 200 mW; $T_{sp} \le 90 \ ^{\circ}C$	300	K/W	

7. Characteristics

Table 7.Characteristics $V_S = 5 V$; $I_S = 15.9 \text{ mA}$; $T_j = 25 \degree$ C; measured on demo board; unless otherwise specified.							
Symbol	Parameter	Conditions	Min	Тур	Max	Unit	
I _S	supply current		13	15.9	21	mA	
s ₂₁ ²	insertion power	f = 100 MHz	21	22.1	23	dB	
	gain	f = 1 GHz	22	22.9	24	dB	
		f = 1.8 GHz	22	23.1	25	dB	
		f = 2.2 GHz	21	22.8	24	dB	
		f = 2.6 GHz	20	22.1	24	dB	
		f = 3 GHz	19	20.8	22	dB	
s ₁₁ ²	s ₁₁ ² input return losses	f = 1 GHz	15	17	-	dB	
		f = 2.2 GHz	10	12	-	dB	
$ s_{22} ^2$	s ₂₂ ² output return	f = 1 GHz	10	12	-	dB	
	losses	f = 2.2 GHz	9	11	-	dB	
s ₁₂ ²	isolation	f = 1.6 GHz	30	31	-	dB	
		f = 2.2 GHz	33	35	-	dB	
NF noise	noise figure	f = 1 GHz	-	5.3	5.4	dB	
		f = 2.2 GHz	-	5.5	5.6	dB	
В	bandwidth	at s ₂₁ ² –3 dB below flat gain at 1 GHz	3	3.2	-	GHz	
К	stability factor	f = 1 GHz	-	1.4	-		
		f = 2.2 GHz	-	1.9	-		
P _{L(sat)}	saturated load	f = 1 GHz	10	11.6	-	dBm	
	power	f = 2.2 GHz	6	7.5	-	dBm	
$P_{L(1dB)}$	load power	at 1 dB gain compression; f = 1 GHz	8	8.9	-	dBm	
		at 1 dB gain compression; f = 2.2 GHz	5	6.1	-	dBm	
IM2	second order intermodulation product	at $P_L = -5 \text{ dBm}$; f ₀ = 1 GHz	36	38	-	dBc	
IP3 _{in}	input, third	f = 1 GHz	-2	-0.7	-	dBm	
	order intercept point	f = 2.2 GHz	-8	-6.9	-	dBm	
IP3 _{out}	output, third	f = 1 GHz	21	22.2	-	dBm	
	order intercept point	f = 2.2 GHz	15	15.9	-	dBm	
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8. Application information

<u>Figure 1</u> shows a typical application circuit for the BGA2716 MMIC. The device is internally matched to 50 Ω , and therefore does not need any external matching. The value of the input and output DC blocking capacitors C2 and C3 should not be more than 100 pF for applications above 100 MHz. However, when the device is operated below 100 MHz, the capacitor value should be increased.

The nominal value of the RF choke L1 is 100 nH. At the frequencies below 100 MHz this value should be increased. At frequencies above 1 GHz, a lower value can be used to tune the output return loss. For optimal results, a good quality chip inductor or a wire-wound SMD type should be chosen.

Both the RF choke and the 22 nF supply decoupling capacitor C1 should be located as close as possible to the MMIC.

The printed-circuit board (PCB) top ground plane, connected to pins 2, 4 and 5 must be as close as possible to the MMIC, and ideally directly beneath it. When using via holes, use multiple via holes, located as close as possible to the MMIC.



Figure 2 shows the PCB layout, used for the standard demonstration board.

MMIC wideband amplifier



8.1 Application examples

The excellent wideband characteristics of the MMIC make it an ideal building block in IF amplifier such as LNBs (see Figure 3).

As second amplifier after an LNA, the MMIC offers an easy matching, low noise solution (see Figure 4).

As driver amplifier in the TX path, the good linear performance and matched input/output offer quick design solutions (see Figure 5).



MMIC wideband amplifier



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MMIC wideband amplifier

f (MHz)	s ₁₁		S ₂₁		S ₁₂	s ₁₂		S ₂₂	
	Magnitude (ratio)	Angle (deg)	Magnitude (ratio)	Angle (deg)	Magnitude (ratio)	Angle (deg)	Magnitude (ratio)	Angle (deg)	
100	0.182562	102.7794	12.69581	13.48682	0.029472	28.74955	0.39239	91.48628	1.3
200	0.123465	87.55274	13.13419	-5.272917	0.035438	-2.202361	0.267851	62.37296	1.2
400	0.107855	58.58513	13.47149	-31.7377	0.035299	-22.54301	0.227252	24.6455	1.2
600	0.114731	40.14071	13.57901	-53.09631	0.033167	-43.06353	0.227993	-3.493572	1.3
800	0.130176	24.28555	13.67457	-73.60665	0.033194	-59.63503	0.234967	-31.11084	1.3
1000	0.144984	9.657616	13.91705	-94.01973	0.029047	-76.09972	0.239818	-60.54722	1.4
1200	0.160922	-7.518892	14.10949	-114.55	0.028188	-88.34045	0.242141	-91.56898	1.4
1400	0.179351	-23.35989	14.2808	-135.3117	0.025188	-101.2729	0.243087	-124.5484	1.4
1600	0.20199	-41.01349	14.3825	-156.7041	0.022257	-110.3342	0.24499	-158.6224	1.5
1800	0.218268	-60.71294	14.26935	-178.3843	0.019611	-121.0192	0.255598	167.5983	1.7
2000	0.233965	-81.48254	14.0667	160.1504	0.018087	-127.6765	0.269829	136.117	1.8
2200	0.242904	-103.1109	13.83968	138.2379	0.017203	-137.8213	0.283613	106.0987	1.9
2400	0.246576	-125.52	13.46447	115.7594	0.016318	-138.8717	0.29058	77.95189	2.0
2600	0.249069	-148.8707	12.74638	93.38644	0.015514	-147.6622	0.281505	50.68612	2.2
2800	0.243665	-172.646	11.87558	71.02792	0.014954	-152.1988	0.25135	24.40624	2.5
3000	0.233266	163.9035	10.94049	50.42722	0.015522	-163.8718	0.211425	-0.674037	2.7
3200	0.222055	140.7754	10.05626	30.75908	0.016261	-170.5637	0.165534	-23.9944	2.9
3400	0.207486	117.0531	9.576357	11.98315	0.016664	-176.5407	0.118726	-46.28101	3.0
3600	0.191654	94.64431	9.199166	-7.677643	0.016982	176.9385	0.083354	-72.36691	3.2
3800	0.175783	71.9551	8.912598	-27.73098	0.017094	165.8227	0.058549	-109.9804	3.3
4000	0.163768	49.89436	8.618058	-48.90874	0.017414	157.6095	0.055225	-163.7132	3.3

Table 8. Scattering parameters

 $V_S = 5 V; I_S = 15.9 mA; P_D = -35 dBm; Z_o = 50 \Omega; T_{amb} = 25$ °C.

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9. Package outline



Fig 14. Package outline; SOT363 (SC-88).

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10. Revision history

ory			
Release date	Data sheet status	Change notice	Supersedes
20110908	Product data sheet	-	BGA2716 v.2
 The format of guidelines of Legal texts hat Package outline 	this data sheet has been rede NXP Semiconductors. ave been adapted to the new c ne drawings have been update	signed to comply wi ompany name wher ed to the latest versio	th the new identity e appropriate. on.
20040924	Product data sheet	-	BGA2716_N v.1
20040202	Preliminary data sheet	-	-
	Release date 20110908 • The format of guidelines of • Legal texts ha • Package outli 20040924 20040202	Release date Data sheet status 20110908 Product data sheet • The format of this data sheet has been rede guidelines of NXP Semiconductors. • Legal texts have been adapted to the new c • Package outline drawings have been update 20040924 Product data sheet 20040202 Preliminary data sheet	Release date Data sheet status Change notice 20110908 Product data sheet - • The format of this data sheet has been redesigned to comply wirguidelines of NXP Semiconductors. - • Legal texts have been adapted to the new company name where • Package outline drawings have been updated to the latest version 20040924 Product data sheet - 20040202 Preliminary data sheet -

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Development	This document contains data from the objective specification for product development.
Qualification	This document contains data from the preliminary specification.
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