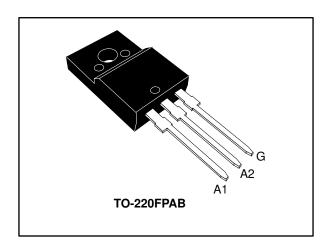
T405T-6FP



4 A logic level Triac

Datasheet - production data



Features

- Three triggering quadrants Triac
- V_{DRM} / V_{RRM} = 600 V
- UL certified device rated 2000 V_{RMS} (ref. file E81734)
- ECOPACK®2 compliant component
- Halogen-free molding, lead-free plating

Applications

- General purpose AC inductive loads
- Induction motor control circuits
- Small home appliances

Benefits

- Low gate consumption
- Direct drive from microcontroller
- · Direct mounting on heat sink

Description

Based on ST's logic level technology providing high commutation performance, this device is suitable for use on AC low current loads. It is recommended for motor driving, electro valves, kitchen appliances, power tools and dishwashers. Available in a fully insulated package, it complies with standard UL1557.

Table 1: Device summary

Symbol	Value	Unit	
I _{T(RMS)}	4	Α	
V_{DRM}/V_{RRM}	600	V	
lgт	5	mA	
T _j max.	125	°C	
Package	TO-220FPAB		
Ordering code	T405T-6FP		

Characteristics T405T-6FP

1 Characteristics

Table 2: Absolute maximum ratings (limiting values)

Symbol Parameter Value Unit					
Symbol		Value	Unit		
I _{T(RMS)}	RMS on-state current (full sine wave) T _c = 104 °C			4	Α
	Non repetitive surge	$t_p = 16.7 \text{ ms}$		31	Α
I _{TSM}	peak on-state current (full sine cycle)	t _p = 20 ms	T_j initial = 25 °C	30	
l²t	I ² t value for fusing	$t_p = 10 \text{ ms}$	T_j initial = 25 °C	5.1	A ² s
dl/dt	Critical rate of rise of on- state current	$l_G = 2 \times l_{GT}$, $t_r \le 100 \text{ ns}$	f = 120 Hz	50	A/μs
I _{GM}	Peak gate current			4	Α
P _{GM}	Maximum gate power dissipation	$t_p = 20 \ \mu s$ $T_j = 125 \ ^{\circ}C$		1	W
T _{stg}	Storage junction temperature range				°C
Tj	Operating junction temperature range			-40 to +125	°C
T∟	Maximum lead temperature for soldering during 10 s			260	°C
Vins	Insulation RMS voltage (60 seconds)			2000	V

Table 3: Static electrical characteristics

Symbol	Test conditions T _j		Value	Unit	
V _{TM}	$I_{TM} = 5.5 \text{ A}, t_p = 380 \ \mu s$	25 °C	Max.	1.56	V
V _{TO}	threshold on-state voltage	125 °C	Max.	0.9	V
R_D	Dynamic resistance	125 °C	Max.	100	mΩ
I _{DRM}	DRM V V V	25 °C	Max.	5	μΑ
I _{RRM}	I _{RRM} V _D = V _{DRM} , V _R = V _{RRM}		Max.	1	mA

Table 4: Dynamic characteristics

Symbol	Parameter	Quadrant	Tj		Value	Unit
I _{GT} ⁽¹⁾	V 10 V D = 20 O		25 °C	Max.	5	mA
V _{GT}	$V_D = 12 \text{ V}, R_L = 30 \Omega$			Max.	1.3	V
V_{GD}	$V_D = V_{DRM}, R_L = 3.3 \text{ k}\Omega$	1 - 11 - 111	125 °C	Min.	0.2	V
lL	Ig = 1.2 x IgT		0E °C	Max.	15	m A
Ін	I _{TM} = 100 mA	25 °C		Max.	10	mA
dV/dt ⁽²⁾	V _D = V _R = 402 V, gate open		125 °C	Min.	20	V/µs
(dl/dt)c ⁽²⁾	$(dV/dt)c = 0.1 V/\mu s$		125 °C	Min.	1.8	A/ms

Notes:

 $^{^{(1)}\!}Minimum$ IgT is guaranteed at 5 % of IgT max.

⁽²⁾For both polarities of A2 referenced to A1

T405T-6FP Characteristics

Table 5: Thermal resistance

Symbol	Parameter		Unit
R _{th(j-c)}	Max. junction to case thermal resistance (AC)	4.3	0 C/M
R _{th(j-a)}	Typical junction to ambient thermal resistance 60		°C/W

Characteristics T405T-6FP

1.1 Characteristics (curves)

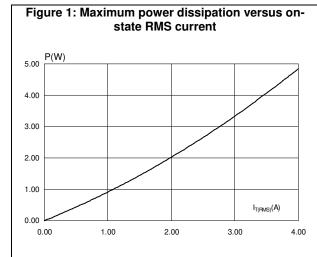


Figure 2: RMS on-state current versus temperature under tab. (full cycle)

5.00

1T(RMS)(A)

4.00

2.00

1.00

0.00

2 5 50 75 100 125

Figure 3: RMS on-state current versus ambient temperature (free air convection)

2.50

1.50

1.00

0.50

0.00

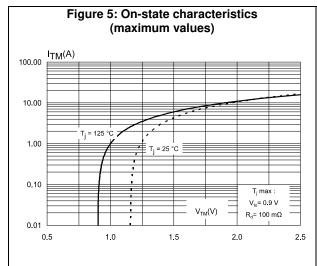
25

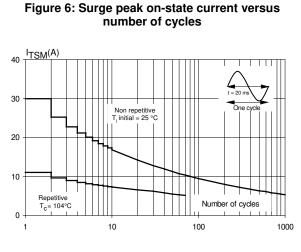
50

75

100

125





T405T-6FP Characteristics

Figure 7: Non repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 10 \text{ ms}$ 1.E+04

1.E+03

1.E+01

1.E+00

1.E+01

1.E+00

1.O0

1.O0

1.O0

Figure 8: Relative variation of gate trigger current and gate trigger voltage versus junction temperature (typical values)

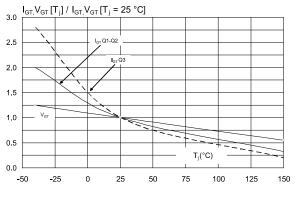


Figure 9: Relative variation of holding current and latching current versus junction temperature (typical values)

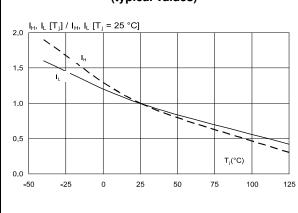


Figure 10: Relative variation of critical rate of decrease of main current (dl/dt)c versus reapplied (dV/dt)c

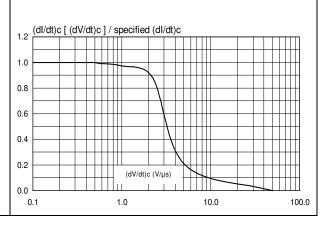


Figure 11: Relative variation of critical rate of decrease of main current versus junction temperature (typical values)

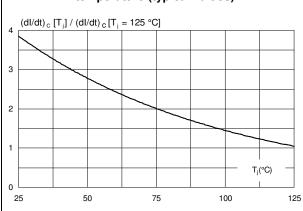
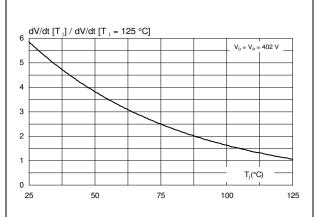


Figure 12: Relative variation of static dV/dt immunity versus junction temperature



Package information T405T-6FP

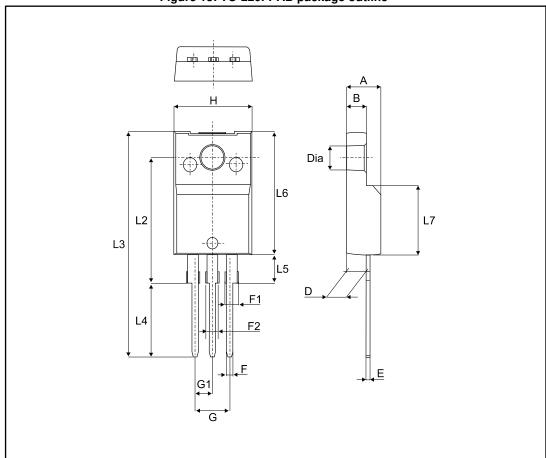
2 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: **www.st.com**. ECOPACK® is an ST trademark.

- ECOPACK®2 compliant
- Lead-free package leads finishing
- Molding compound resin is halogen-free and meets UL94 level V0
- Recommended torque (for through-hole package): 0.4 to 0.6 N·m

2.1 TO-220FPAB package information

Figure 13: TO-220FPAB package outline



T405T-6FP Package information

Table 6: TO-220FPAB package mechanical data

	Dimensions			
Ref.	Millimeters		Inc	hes
	Min.	Max.	Min.	Max.
Α	4.40	4.60	0.1739	0.1818
В	2.5	2.7	0.0988	0.1067
D	2.50	2.75	0.0988	0.1087
E	0.45	0.70	0.0178	0.0277
F	0.75	1.0	0.0296	0.0395
F1	1.15	1.70	0.0455	0.0672
F2	1.15	1.70	0.0455	0.0672
G	4.95	5.20	0.1957	0.2055
G1	2.40	2.70	0.0949	0.1067
Н	10.00	10.40	0.3953	0.4111
L2	16.00 typ.		0.632	4 typ.
L3	28.60	30.60	1.1304	1.2095
L4	9.80	10.6	0.3874	0.4190
L5	2.90	3.60	0.1146	0.1423
L6	15.90	16.40	0.6285	0.6482
L7	9.00	9.30	0.3557	0.3676
Dia	3.0	3.20	0.1186	0.1265

Ordering information T405T-6FP

3 Ordering information

Figure 14: Ordering information scheme

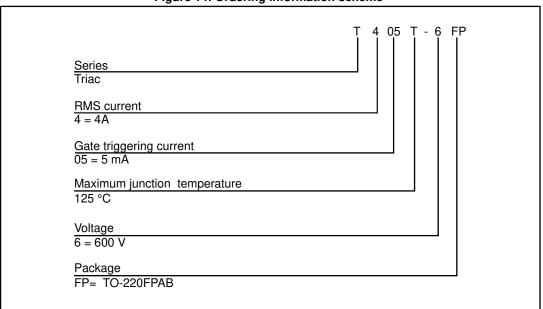


Table 7: Ordering information

Order code	Marking	Package	Weight	Base qty.	Delivery mode
T405T-6FP	T405T-6FP	TO-220FPAB	2.0 g	50	Tube

4 Revision history

Table 8: Document revision history

Date	Revision	Changes
04-Nov-2016	1	Inital release.

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