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April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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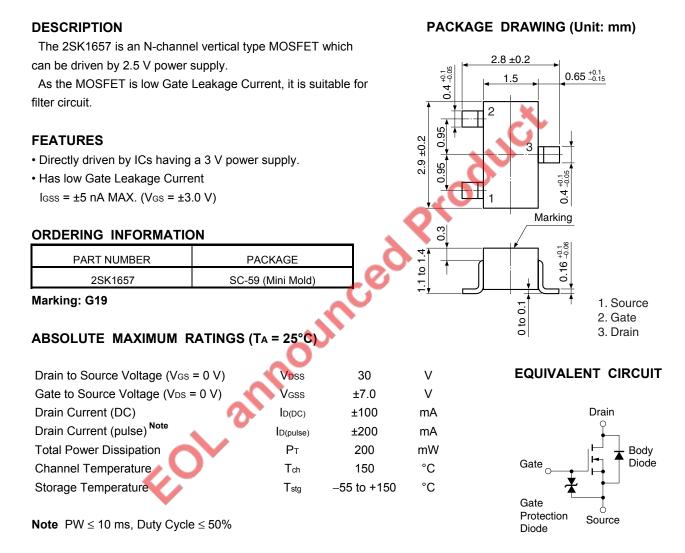
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RENESAS

MOS FIELD EFFECT TRANSISTOR **2SK1657**

N-CHANNEL MOSFET FOR SWITCHING



Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

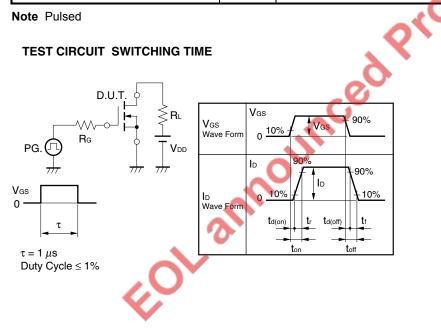
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ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$)

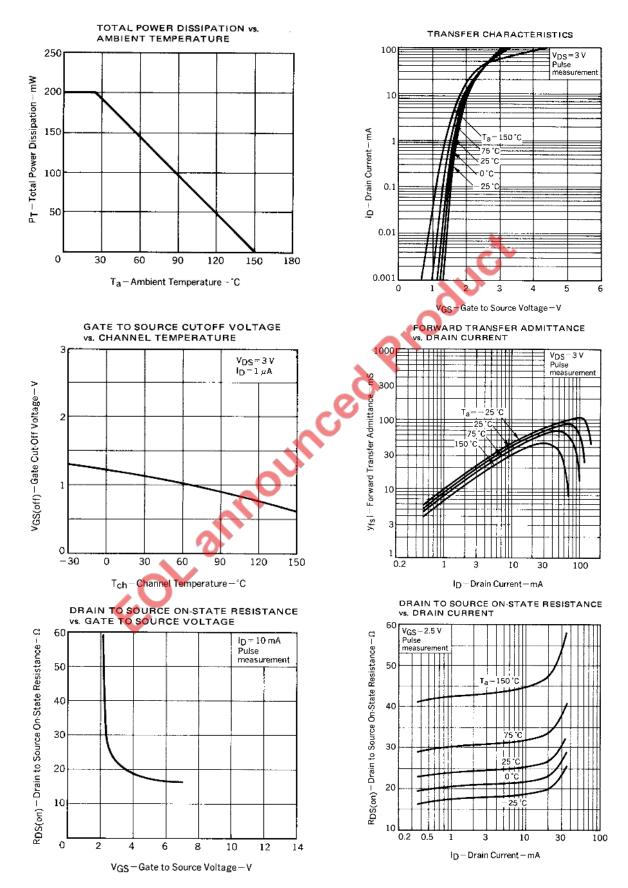
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 30 V, V _{GS} = 0 V			10	μA
Gate Leakage Current	lgss	V _{GS} = ±3.0 V, V _{DS} = 0 V			±5.0	nA
Gate Cut-off Voltage	V _{GS(off)}	V _{DS} = 3.0 V, I _D = 1.0 μA	0.9	1.2	1.5	V
Forward Transfer Admittance Note	y _{fs}	V _{DS} = 3.0 V, I _D = 10 mA	20	40		mS
Drain to Source On-state Resistance Note	RDS(on)1	V _{GS} = 2.5 V, I _D = 10 mA		25	45	Ω
	RDS(on)2	V _{GS} = 4.0 V, I _D = 10 mA		18	25	Ω
Input Capacitance	Ciss	V _{DS} = 3.0 V		15		pF
Output Capacitance	Coss	V _{GS} = 0 V		10		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		1.5		pF
Turn-on Delay Time	td(on)	V _{DD} = 3.0 V, I _D = 10 mA		50		ns
Rise Time	tr	V _{GS} = 3 V		23		ns
Turn-off Delay Time	td(off)	Rg = 10 Ω		34		ns
Fall Time	tr)	43		ns

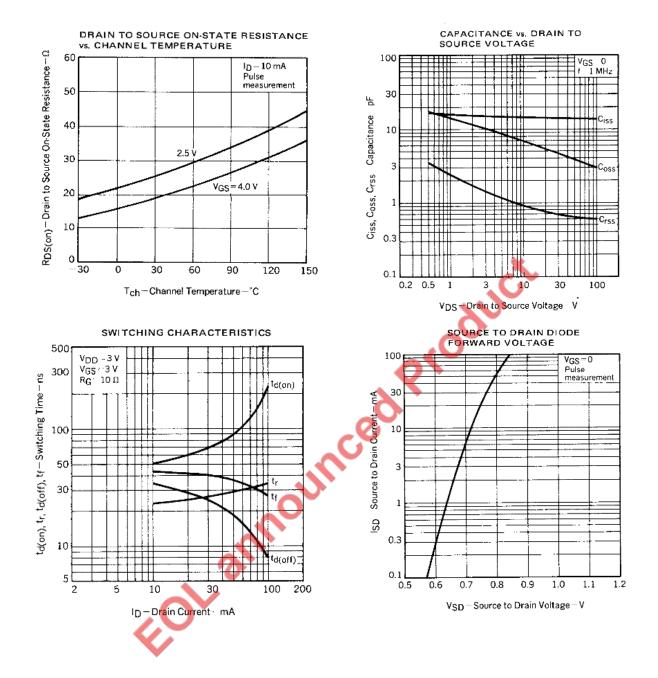
Note Pulsed

TEST CIRCUIT SWITCHING TIME



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NEC

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