RECOMMENDED PCB LAYOUT FOR QSH-XXX-01-X-D-EM2

3/19/2018 ECN-322102 .0050 [0.127] SOLDER MASK DEFINED DIMENSION $.0430^{+.003}_{-.001}$ 1.092 $^{+0.08}_{-0.03}$.030[0.76] .075[1.91] .1680±.0030 [4.267±0.076] .050[1.27] REF .7875[20.003] ---'A' ******** .125[3.18] .1850 4.699 "B"◄¬ HIDDEN LINES REFER .012[0.30] "B"◀– .0100[0.254] REF WIDTH OF PLATED EDGE <u>Detail 'a'</u> Scale 5 : 1 # OF BANKS X .7875 [20.003] +.0225 [.5715] (SEE NOTE 3) -EM2: .064±.004 [1.63±0.10] PCB .0100[0.254] 01 (SEE NOTE 4) BROKEN EDGE MUST BE LOWER THAN PLATED EDGE OR .5713 [14.511] 29 EQUAL SPACES @ .0197 [.500] MILLED FOR HOUSING CLEARANCE. (SEE NOTE 1) DETAIL "B"-"B" SCALE 10 : 1

NOTES:

REVISION L

1. 1 BANK = 30 POSITIONS/SIDE FOR STANDARD VERSION.

2. NOTE DELETED.

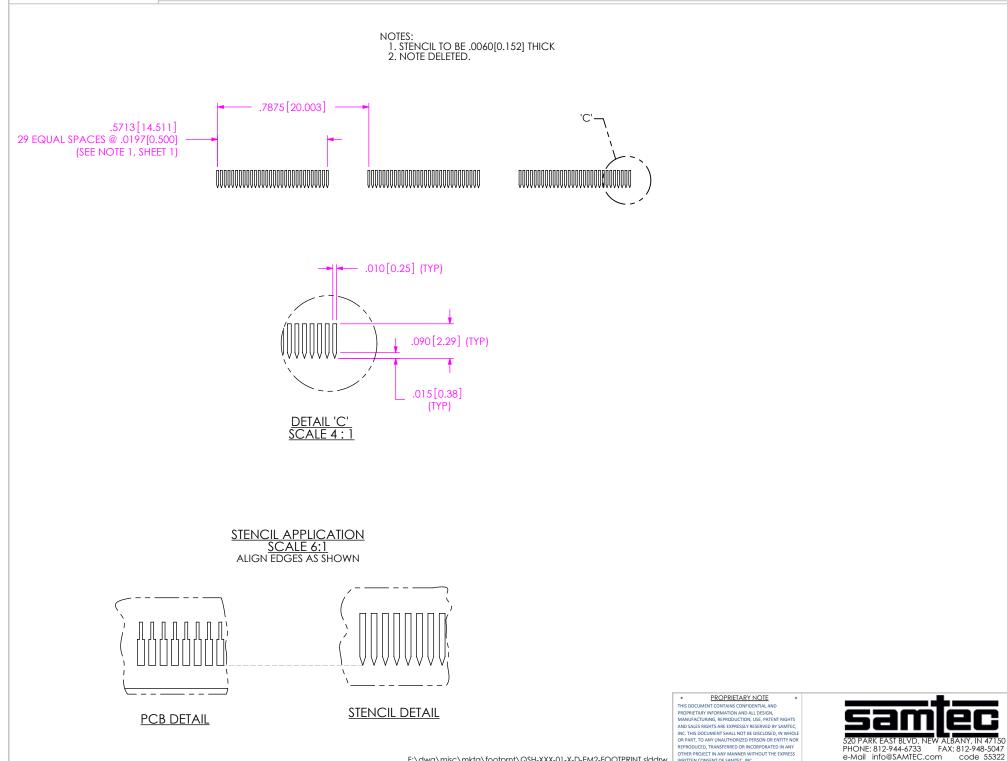
- 3. PLATED EDGE MAY BE DIVIDED UP IN BANKS DEPENDING UPON DESIGNED
- FUNCTION.
- 4. REVIEW THE "EDGE MOUNT Q1 INTERFACES" ON THE PROCESSING LITERATURE SECTION OF SAMTEC.COM.

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REVISION L

RECOMMENDED STENCIL LAYOUT FOR QSH-XXX-01-X-D-EM2



IN 47150

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