

MOSFETs Silicon N-Channel MOS (U-MOSVII-H)

# SSM6N7002KFU

#### 1. Applications

• High-Speed Switching

#### 2. Features

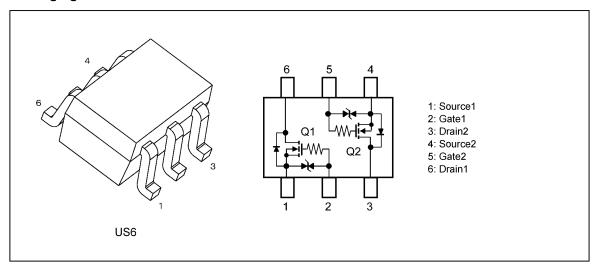
- (1) AEC-Q101 qualified (Note 1)
- (2) ESD(HBM) level 2 kV
- (3) Low drain-source on-resistance
  - :  $R_{DS(ON)} = 1.05 \Omega (typ.) (@V_{GS} = 10 V)$

 $R_{DS(ON)} = 1.15 \Omega \text{ (typ.) } (@V_{GS} = 5.0 \text{ V})$ 

 $\rm R_{DS(ON)}=1.2~\Omega~(typ.)~(@V_{GS}=4.5~V)$ 

Note 1: For detail information, please contact to our sales.

#### 3. Packaging and Internal Circuit



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### 4. Absolute Maximum Ratings (Note) (Unless otherwise specified, Ta = 25 °C)

	Characteristics	Symbol	Rating	Unit
Drain-source voltage		V <sub>DSS</sub>	60	٧
Gate-source voltage		V <sub>GSS</sub>	±20	
Drain current (DC)	(Note 1), (Note 2)	I <sub>D</sub>	300	mA
Drain current (pulsed)	(Note 1), (Note 3)	I <sub>DP</sub>	1200	
Power dissipation	(Note 2), (Note 4)	P <sub>D</sub>	500	mW
Power dissipation	(Note 4), (Note 5)		285	
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature		T <sub>stg</sub>	-55 to 150	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

- Note 1: Ensure that the channel temperature does not exceed 150 °C.
- Note 2:  $T_{sp} = 25 \,^{\circ}\text{C}$
- Note 3: Repetitive rating; pulse width limited by maximum channel temperature. pulse width  $\leq$  10  $\mu$ s, Duty 1 %
- Note 4: Total rating
- Note 5: Device mounted on a 25.4 mm × 25.4 mm × 1.6 mm FR-4 glass epoxy board (Cu pad: 645 mm<sup>2</sup>)

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

Note: The MOSFETs in this device are sensitive to electrostatic discharge. When handling this device, the worktables, operators, soldering irons and other objects should be protected against anti-static discharge.

Note: The channel-to-ambient thermal resistance, R<sub>th(ch-a)</sub>, and the drain power dissipation, P<sub>D</sub>, vary according to the board material, board area, board thickness and pad area. When using this device, be sure to take heat dissipation fully into account.



#### 5. Electrical Characteristics

## 5.1. Static Characteristics (Unless otherwise specified, $T_a = 25$ °C)

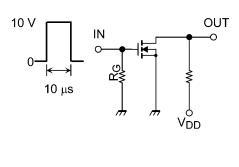
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I <sub>GSS</sub>	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$			±10	μΑ
Drain cut-off current		I <sub>DSS</sub>	V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V	I	I	1	
Drain-source breakdown voltage		V <sub>(BR)DSS</sub>	$I_D = 250 \mu A, V_{GS} = 0 V$	60		_	٧
Gate threshold voltage		$V_{th}$	$I_D = 250 \mu A, V_{DS} = V_{GS}$	1.1		2.1	
Drain-source on-resistance	(Note 1)	R <sub>DS(ON)</sub>	$I_D$ = 100 mA, $V_{GS}$ = 10 V		1.05	1.5	Ω
			$I_D$ = 100 mA, $V_{GS}$ = 5.0 V		1.15	1.65	
			I <sub>D</sub> = 100 mA, V <sub>GS</sub> = 4.5 V		1.2	1.75	
Forward transfer admittance	(Note 1)	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 200 mA	_	1.0	_	S

Note 1: Pulse measurement.

# 5.2. Dynamic Characteristics (Unless otherwise specified, $T_a = 25$ °C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C <sub>iss</sub>	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V},$	_	26	40	pF
Reverse transfer capacitance	C <sub>rss</sub>	f = 1 MHz	_	1.3	_	
Output capacitance	Coss		_	5.5	_	
Switching time (rise time)	t <sub>r</sub>	$V_{DD} = 30 \text{ V}, I_{D} = 200 \text{ mA}$	_	3.6	_	ns
Switching time (turn-on delay time)	t <sub>d(on)</sub>	$V_{GS}$ = 0 to 10 V, $R_{G}$ = 50 $\Omega$ Duty $\leq$ 1%, $V_{IN}$ : $t_{r}$ , $t_{f}$ < 5 ns,	_	5.5	11	
Switching time (fall time)	t <sub>f</sub>	Common source, See Chapter	_	17	_	
Switching time (turn-off delay time)	t <sub>d(off)</sub>	5.3	_	38	90	

## 5.3. Switching Time Test Circuit



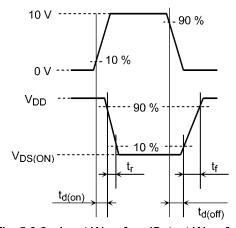


Fig. 5.3.1 Switching Time Test Circuit

Fig. 5.3.2 Input Waveform/Output Waveform

### 5.4. Gate Charge Characteristics (Unless otherwise specified, T<sub>a</sub> = 25 °C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	$Q_g$	$V_{DD} = 30 \text{ V}, V_{GS} = 4.5 \text{ V},$	_	0.39	0.6	nC
Gate-source charge	$Q_{gs}$	]I <sub>D</sub> = 200 mA		0.2	_	
Gate-drain charge	$Q_{gd}$			0.11	_	

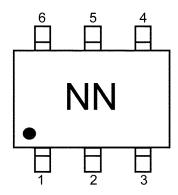


# 5.5. Source-Drain Characteristics (Unless otherwise specified, T<sub>a</sub> = 25 °C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Diode forward voltage	(Note 1)	$V_{DSF}$	I <sub>D</sub> = -115 mA, V <sub>GS</sub> = 0 V	_	-0.79	-1.1	٧

Note 1: Pulse measurement.

## 6. Marking





#### 7. Characteristics Curves (Note)

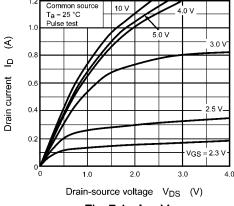


Fig. 7.1 I<sub>D</sub> - V<sub>DS</sub>

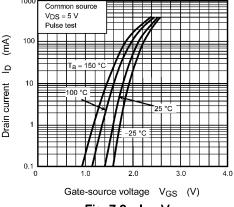


Fig. 7.2 I<sub>D</sub> - V<sub>GS</sub>

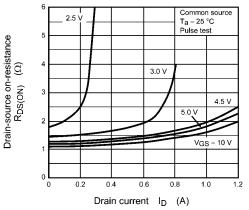


Fig. 7.3 R<sub>DS(ON)</sub> - I<sub>D</sub>

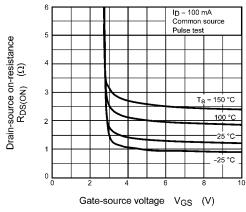


Fig. 7.4 R<sub>DS(ON)</sub> - V<sub>GS</sub>

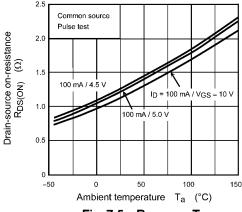


Fig. 7.5 R<sub>DS(ON)</sub> - T<sub>a</sub>

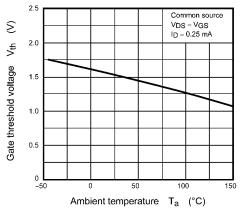
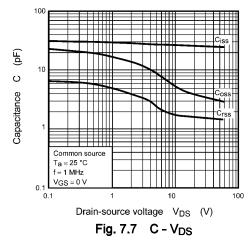
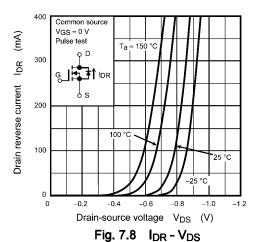
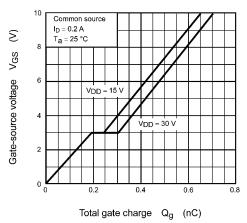


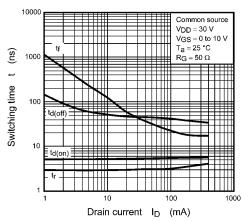
Fig. 7.6 V<sub>th</sub> - T<sub>a</sub>





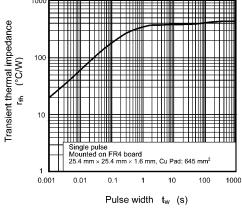












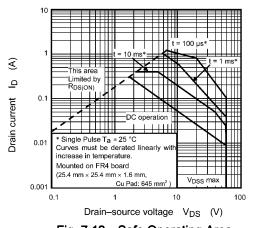


Fig. 7.11 r<sub>th</sub> - t<sub>w</sub>

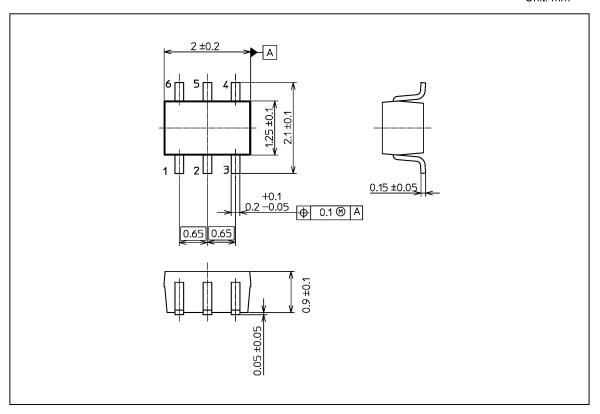
Fig. 7.12 Safe Operating Area

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



## **Package Dimensions**

Unit: mm



Weight: 6.8 mg (typ.)

	Package Name(s)
TOSHIBA: 1-2T1S	
Nickname: US6	



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