

NOT RECOMMENDED FOR NEW DESIGN **USE DMP2110UQ**



DMP2225LQ

August 2022

P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	RDS(ON) MAX	Package	I _{D MAX} $T_A = +25^{\circ}C$
001/	$110m\Omega$ @ V _{GS} = -4.5V	0.0700	-2.6A
-20V	$225m\Omega$ @ V _{GS} = -2.5V	SOT23	-2.0A

Description

This new generation MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- General purpose interfacing switches
- Power management functions

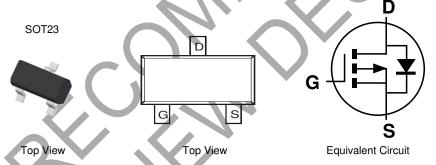
Features

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DIODES™ DMP2225LQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Package: SOT23
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (e3)
- Terminal Connections: See Diagram
- Weight: 0.008 grams (Approximate)



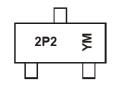
Ordering Information (Note 4)

Part Number	Package	Packing		
Part Number	Package	Qty.	Carrier	
DMP2225LQ-7	SOT23	3,000	Tape & Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



2P2 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: J = 2022)M = Month (ex: 9 = September)

Date Code Key

,												
Year	2019		2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Code	G		J	K	L	М	N	0	Р	R	S	Т
				•	•							
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteri	stic		Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	-20	V
Gate-Source Voltage			Vgss	±12	V
Continuous Drain Current (Note 5) Steady $T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$			I _D	-2.6 -2	Α
Pulsed Drain Current (Note 6)				-8	Α

Thermal Characteristics

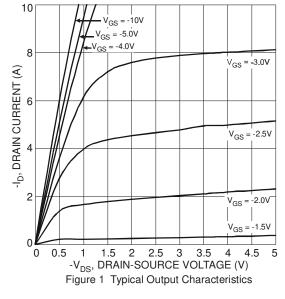
Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P _D	1.08	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 5)	Reja	115	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

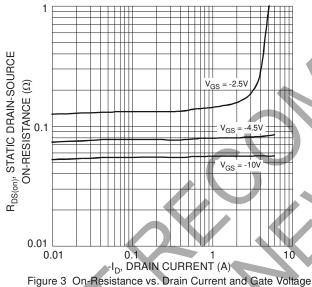
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	-20	7 –		V	$V_{GS} = 0V, I_{D} = -250\mu A$
Zero Gate Voltage Drain Current	IDSS	17.1	. –	-800	nA	$V_{DS} = -20V, V_{GS} = 0V$
On-State Drain Current	I _{D(ON)}	-6 -3	-	<u> </u>	Α	$V_{DS} \le -5V$, $V_{GS} = -4.5V$ $V_{DS} \le -5V$, $V_{GS} = -2.5V$
Gate-Source Leakage	Igss	_		±80	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)		<u> </u>				
Gate Threshold Voltage	V _{GS(TH)}	-0.45		-1.25	V	$V_{DS} = V_{GS}, I_D = -250 \mu A$
Static Drain-Source On-Resistance	RDS(ON)	7	80 165	110 225	mΩ	V _{GS} = -4.5V, I _D = -2.6A V _{GS} = -2.5V, I _D = -2.0A
Forward Transfer Admittance	Y _{fs}	- + -	4	_	S	$V_{DS} = -5V$, $I_D = -2.6A$
Diode Forward Voltage (Note 6)	Vsp		_	-1.26	V	V _G S = 0V, I _S = -2.6A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss		250		рF	101/11/01/
Output Capacitance	Coss	_	88		рF	V _{DS} = -10V, V _{GS} = 0V f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	58	_	рF	T = T.OIVII IZ
Gate Resistance	Rg	_	12	16	Ω	$V_{GS} = 0V$, $V_{DS} = 0V$, $f = 1MHz$
Total Gate Charge	Qg	_	4.3	5.3	•	45)/ // 10)/
Gate-Source Charge	Qgs	_	0.9	_	nC	$V_{GS} = -4.5V$, $V_{DS} = -10V$, $I_{D} = -2.7A$
Gate-Drain Charge	Q _{gd}	_	2.1	_		ID = -2.7A

 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Repetitive rating, pulse width limited by junction temperature.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to production testing. Notes:









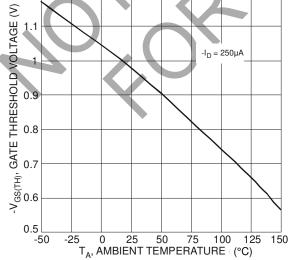
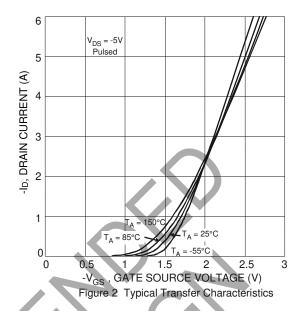
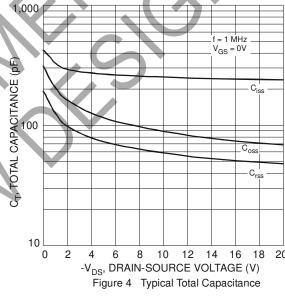


Figure 5 Gate Threshold Voltage vs. Ambient Temperature





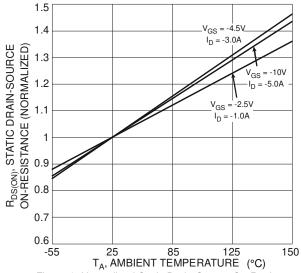


Figure 6 Normalized Static Drain-Source On-Resistance vs. Ambient Temperature



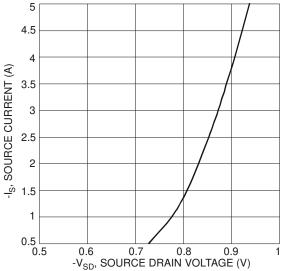
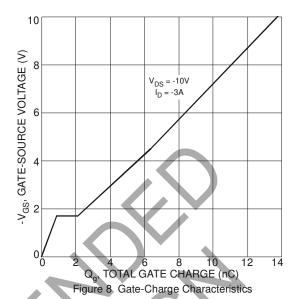
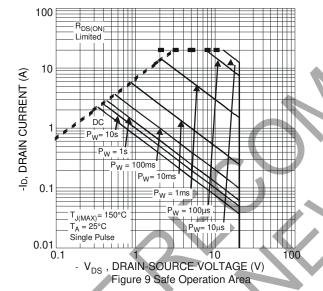
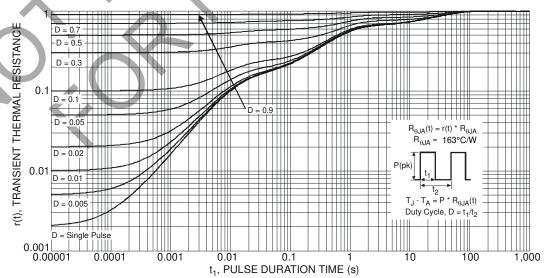


Figure 7 Reverse Drain Current vs. Source-Drain Voltage





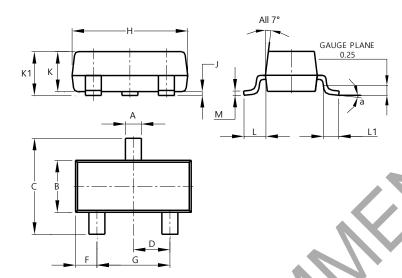




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23

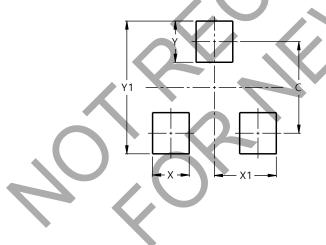


SOT23						
Dim	Min	Max	Тур			
A	0.37	0.51	0.40			
В	1.20	1.40	1.30			
O	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
I	2.80	3.00	2.90			
J	0.013	0.10	0.05			
K	0.890	1.00	0.975			
K1	0.903	1.10	1.025			
J	0.45	0.61	0.55			
L1	0.25	0.55	0.40			
M	0.085	0.150	0.110			
a	°	8°				
All	Dimens	ions in	mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23



Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Υ	0.9
Y1	2.9



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