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April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

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# 2SK2315

# Silicon N Channel MOS FET

REJ03G1006-0200

(Previous: ADE-208-1354)

Rev.2.00 Sep.07,2005

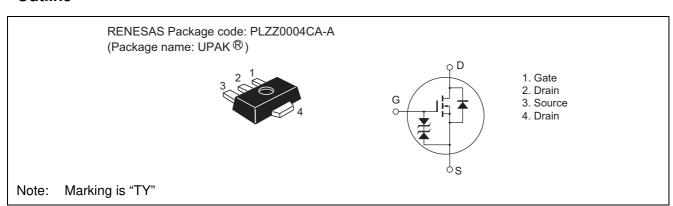
### **Application**

High speed power switching

#### **Features**

- Low on-resistance
- High speed switching
- Low drive current
- 2.5 V gate drive device can be driven from 3 V source.
- Suitable for DC-DC converter, motor drive, power switch, solenoid drive

#### **Outline**



\*UPAK is a trademark of Renesas Technology Corp.

# **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Ratings	Unit
Drain to source voltage	$V_{DSS}$	60	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	I <sub>D</sub>	2	Α
Drain peak current	I <sub>D(pulse)</sub> *1	4	Α
Body to drain diode reverse drain current	I <sub>DR</sub>	2	Α
Channel dissipation	Pch*2	1	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	−55 to +150	°C

Notes: 1.  $PW \le 10 \infty$ , duty cycle  $\le 1 \%$ 

### **Electrical Characteristics**

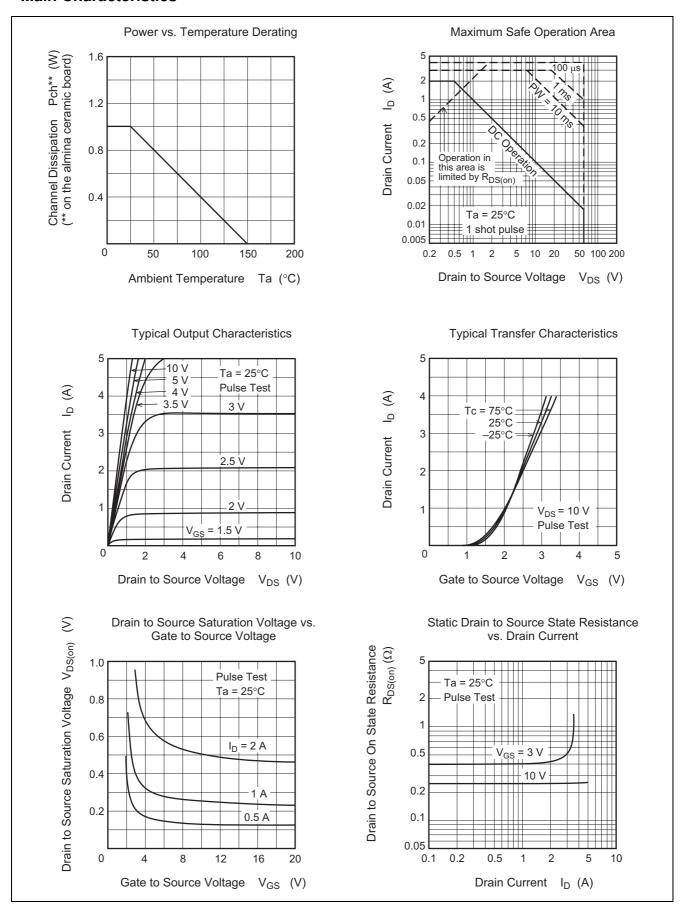
 $(Ta = 25^{\circ}C)$ 

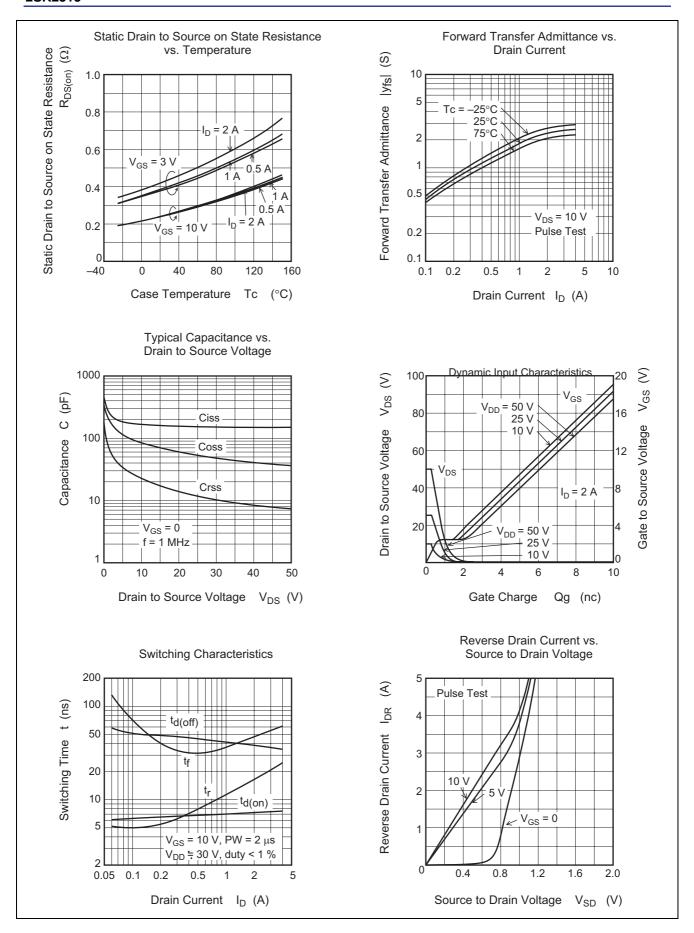
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	60	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	_	_	V	$I_G=\pm 100        $
Gate to source leak current	I <sub>GSS</sub>	_	_	±5	∞A	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	_	_	5	∞A	$V_{DS} = 50 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	0.5	_	1.5	V	$I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}$
Static drain to source on state	R <sub>DS(on)</sub>	_	0.4	0.6	Ω	$I_D = 0.3 \text{ A}, V_{GS} = 3 \text{ V}^{*3}$
resistance		_	0.35	0.45	Ω	$I_D = 1 A, V_{GS} = 4 V^{*3}$
Forward transfer admittance	y <sub>fs</sub>	1.5	1.8	_	S	$I_D = 1 A, V_{DS} = 10 V^{*3}$
Input capacitance	Ciss	_	173	_	pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0,$
Output capacitance	Coss	_	85	_	pF	f = 1 MHz
Reverse transfer capacitance	Crss	_	23	_	pF	
Turn-on time	t <sub>on</sub>	_	21		ns	$I_D = 1 \text{ A}, R_L = 30 \Omega,$
Turn-off time	t <sub>off</sub>	_	85	_	ns	V <sub>GS</sub> = 10 V

Note: 3. Pulse Test

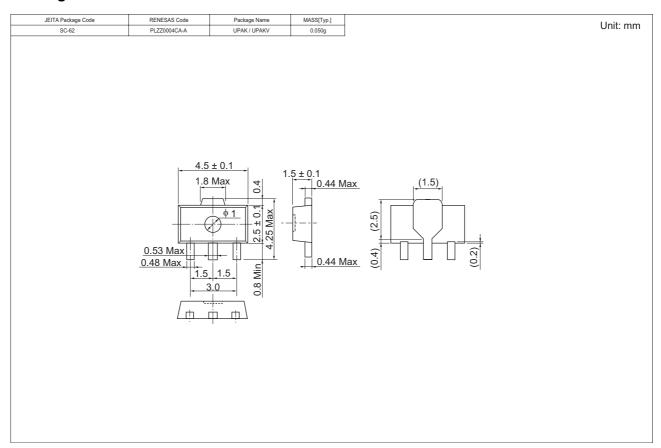
<sup>2.</sup> When using the alumina ceramic board (12.5  $\cdot$  20  $\cdot$  0.7mm)

#### **Main Characteristics**





## **Package Dimensions**



## **Ordering Information**

Part Name	Quantity	Shipping Container
2SK2315TYTL-E	1000 pcs	Taping
2SK2315TYTR-E	1000 pcs	Taping

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