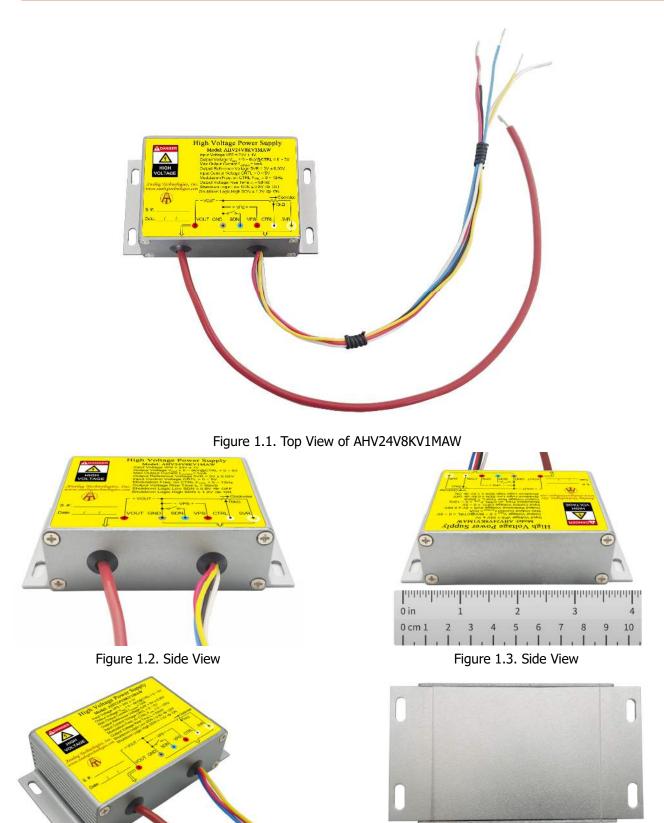


# AHV24V8KV1MAW



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Figure 1.5. Bottom View

Figure 1.4. Side View



## **FEATURES**

- Input Power Voltage: 24V ± 1V
- Input Current Range: 40mA to 500mA
- Output Voltage: 0 to 8kV@CTRL = 0 to 5V
- Max. Output Current: 1mA
- Reference Voltage: 5V ± 0.05V
- Input Control Voltage: 0 to 5V
- Full Span Modulation on Output Voltage
- Electronic Shutdown Control



Figure 2. The Connecting Lead Wires of AHV24V8KV1MA

Table 1. Pin Names, Colors, Functions and Specifications.

## **APPLICATIONS**

This power module, AHV24V8KV1MAW, is designed for achieving DC-DC conversion from low voltage to high voltage as a power supply source which is widely used in scientific research and other fields including:

- X-ray Machine
- Spectral Analysis
- Nondestructive Inspection
- Semiconductor Manufacturing Equipment
- CRT Monitor Test
- Particle Accelerator
- Capillary Electrophoresis
- Particles Injection
- Semiconductor Technology
- Physical Vapor Phase Deposition
- Radio Frequency Amplification
- Electrospinning Preparation of Nanofiber
- Glass / Fabric Coating
- DC Reactive Magnetron Sputtering
- Cyclotron Accelerator

No.	Name	Co	olor	Туре	Description	Min.	Тур.	Max.
1	SDN	Blue		Digital input	Shutdown logic low	0V		0.8V
Ţ	3011	Diue		Digital input	Shutdown logic high	1.2V		5V
2	5VR	Yellow	$\bigcirc$	Analog output	Reference voltage		5V	
3	CTRL	White	$\bigcirc$	Analog input	Regulation	0V		5V
4	VPS	Red		Power input	Input voltage		24V	
5	GND	Black		Ground for analog, digital and power signals.	Ground electrode		0V	
6	VOUT	Brown		Power output	Output high voltage	0V		8kV

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 2



# AHV24V8KV1MAW

## DESCRIPTION

Figure 2 shows the connecting wires of AHV24V8KV1MAW, of which their detail information given in Table 1. The output voltage can be set to a constant value by connecting the CTRL port to the central tap of a POT (Potentiometer) or modulated by an AC signal ranging from 0V to 5V corresponding to 0V to 8kV proportionally at the output VOUT port as shown in Figure 3 and Figure 4 respectively.

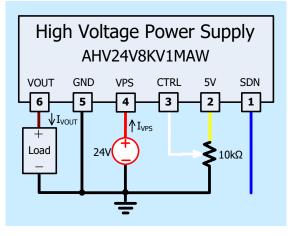


Figure 3. Setting Output to be a Constant Voltage

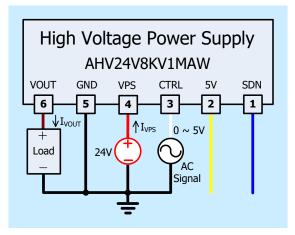


Figure 4. Modulating Output by an AC Signal Source

Please note that the modulation signal must have a low frequency  $\leq$  10Hz and the value range must be 0V  $\leq$  V<sub>CTRL</sub>  $\leq$  5V. The equivalent input circuit for the CTRL is shown in Figure 5.

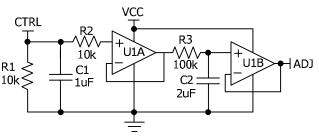


Figure 5. The Equivalent Circuit for CTRL Port

To shutdown AHV24V8KV1MAW, pull down SDN pin to <0.8V; to turn it on, leave SDN pin unconnected or pull it >1.2V. The maximum voltage allowed on the SDN pin is 5V. The equivalent circuit for SDN port is shown in Figure 6.

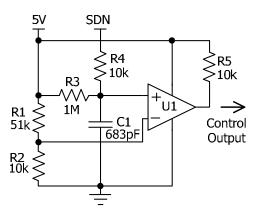


Figure 6. The Equivalent Circuit for SDN Port

## USING AHV24V8KV1MAW

This high voltage power supply must be mounted tightly onto a metal plate, ideally, thus expanding its heating sinking capacity of the metal enclosure. Sufficient ventilation must be provided to keep the power supply surface temperature under 55°C.

# **SAFETY PRECAUTIONS**

Although AHV24V8KV1MAW high voltage power supply comes with an over current protection circuit, a short circuit at the output should always be avoided. Make sure the high voltage wire for connecting VOUT node has sufficient insulation capability with its surrounding objects.



# **SPECIFICATIONS**

### Table 2. Characteristics. $T_A = 25^{\circ}C_r$ , unless otherwise noted.

Parameter		Symbol	Test Conditions	Min.	Тур.	Max.	Unit/Note
Input Power Voltage		V <sub>VPS</sub>		23	24	25	V
Input Power C	Quiescent Current	$I_{VPS_QC}$	$I_{VOUT} = 0mA$	40	50	60	mA
Input Power C	urrent at Full Load	$I_{\text{VPS}\_\text{FL}}$	$I_{VOUT} = 1.0 \text{mA}$	400	500	600	mA
	ver Current at utdown	$I_{\text{VPS}\_\text{SHDN}}$	$T_A = -10^{\circ}C \sim 55^{\circ}C$		15		mA
Power Supply	y Rejection Ratio	PSRR <sup>(1)</sup>	$V_{VPS} = 23V \sim 25V$ $V_{CTRL} = V_{5VR} = 5V$ $V_{VOUT} = 8kV$ $I_{VOUT} = 1.0mA$		TBD		dB
	Voltage Range cy on CTRL	f <sub>CTRL</sub>		0		12	Hz
Shutdown	Port Current	$I_{SDNL}$	$V_{SDNL} < 0.8V$	-5		-4.2	μA
Shutuowi	i Port Current	$\mathrm{I}_{SDNH}$	$1.2V < V_{SDNL} < 5V$	0		3.8	μA
Shutdown Vo	oltage Logic Low	$V_{SDNL}$		0		0.8	V
Shutdown Vo	ltage Logic High	$V_{\text{SDNH}}$		1.2		5	V
Outpu	ut Voltage	V <sub>VOUT</sub>	$\mathrm{I}_{\text{VOUT}}=0\sim1.0\text{mA}$	0		8000	V
Output C	urrent Range	$I_{\text{VOUTMAX}}$	$V_{VPS} = 23V \sim 25V$	0		1.0	mA
Reference Volta	age Output Range	V <sub>5VR</sub>	$\begin{array}{l} T_{A} = -10^{\circ}C \sim 55^{\circ}C \\ I_{5VR} \leq 5mA \end{array}$	4.98	5	5.02	V
Output I	Load Range			8		ø	MΩ
Output Vo	oltage Ripple	V <sub>VOUT_RP</sub>	Bandwidth = 1MHz $R_{LOAD} = 8 M\Omega$	≤4.0			V <sub>P-P</sub>
Output Voltage	e Ripple Frequency	f <sub>vout_rp</sub>		TBD			Hz
	ge Temperature fficient	TCV <sub>VOUT</sub> <sup>(2)</sup>	$V_{VPS} = 24V$ $V_{CTRL} = V_{5VR} = 5V$ $V_{VOUT} = 8kV$ $I_{VOUT} = 1mA$ $T_A = -10^{\circ}C \sim 55^{\circ}C$		≤0.01		%/°C
	age Range v.s. perature	V <sub>VOUT</sub> (T)	$V_{VPS} = 24V$ $V_{CTRL} = V_{5VR} = 5V$ $V_{VOUT} = 8kV$ $I_{VOUT} = 1mA$ $T_A = -10^{\circ}C \sim 55^{\circ}C$	0.99V <sub>VOUT</sub>	V <sub>VOUT</sub>	1.01V <sub>VOUT</sub>	v
Output	Short Term Drift	$\frac{\left \Delta V_{\text{VOUT}}/V_{\text{VOUT}}\right }{\Delta t \text{ (min)}}$	$V_{VPS} = 24V$ $V_{CTRL} = V_{5VR} = 5V$ $V_{CTRL} = 8VV$		≤0.5		%/min
Voltage Drift	Long Term Drift	$\frac{\left \Delta V_{\text{VOUT}}/V_{\text{VOUT}}\right }{\Delta t \text{ (h)}}$	$V_{VOUT} = 8kV$ $I_{VOUT} = 1mA$ $T_A = -10^{\circ}C \sim 55^{\circ}C$		≤1		%/h



Output Voltage Dice Time	+	$\begin{array}{l} V_{\text{VOUT}}\left(t_{1}\right)=800\text{V}\\ V_{\text{VOUT}}\left(t_{2}\right)=7200\text{V}\\ \text{No-Load} \end{array}$		30		ms
Output Voltage Rise Time	t <sub>r</sub>	$\begin{array}{l} V_{\text{VOUT}}\left(t_{1}\right)=800V\\ V_{\text{VOUT}}\left(t_{2}\right)=7200V\\ R_{\text{Load}}=8\ M\Omega \end{array}$		TBD		ms
Output Voltage Fall Time	t <sub>f</sub>	$V_{VOUT}(t_2) = 7200V$ $V_{VOUT}(t_3) = 800V$ No-Load		100		ms
Output Voltage Fall Time		$\begin{array}{l} V_{\text{VOUT}}\left(t_2\right) = 7200V\\ V_{\text{VOUT}}\left(t_3\right) = 800V\\ R_{\text{Load}} = 8\ M\Omega \end{array}$		TBD		ms
Mean Time Between Failure	MTBF			TBD		h
Instantaneous Short Circuit Current at the Output	I <sub>VOUT_SC</sub>			≤500		mA
Load Regulation	$\frac{\left \Delta V_{\text{vout}}/V_{\text{vout}}\right }{\Delta I_{\text{vout}}}$	$V_{VOUT} = 8kV$ $I_{VOUT} = 1mA$		≤0.05		%/mA
Full Load Efficiency	η <sup>(3)</sup>	$V_{VPS} = 24V$ $V_{VOUT} = 8kV$ $I_{VOUT} = 1mA$		≥80		%
Operating Temperature Range	T <sub>opr</sub>		-10		55	°C
Storage Temperature Range	T <sub>stg</sub>		-20		85	°C
Thermal resistance housing- ambient	θ <sub>на</sub> <sup>(4)</sup>	$V_{VPS} = 24V$ $V_{CTRL} = V_{5VR} = 5V$ $V_{VOUT} = 8kV$ $I_{VOUT} = 1mA$		TBD		°C/W
External Dimensions			82×55×28		3	mm
			3.23×2.17×1.10		inch	
				210		g
Weight				0.46		lbs
				7.4		Oz

Note 1: PSRR = 
$$20 \log_{10} \frac{\Delta V_{VOUT} / V_{VOUT}}{\Delta V_{VPS} / V_{VPS}}$$
 (dB)

 $\Delta V_{VOUT} = V_{VOUT} (V_{VPS} = 24.5V) - V_{VOUT} (V_{VPS} = 23.5V), V_{VOUT} (V_{VPS} = 24.5V) = V_{VOUT} (V_{VPS} = 24V)$  $\Delta V_{VPS} = 24.5V - 23.5V, V_{VPS} = 24V$ 

Note 2: TCV<sub>VOUT</sub> =  $\frac{\left|\Delta V_{VOUT}\right|}{V_{VOUT} \Delta T}$ 

Note 3:  $\eta = \frac{V_{VOUT} \quad I_{VOUT}}{V_{VPS} \quad I_{VPS}}$ 

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### **TESTING DATA**

Test conditions:  $V_{VPS} = 24V$ ,  $T_A = 25^{\circ}C$ ,  $R_{LOAD} = 8M\Omega$ 

### **DC Testing**

The measured output voltage,  $V_{VOUT}$ , corresponding to the control port input voltage,  $V_{CTRL}$ , is shown in Figure 7.

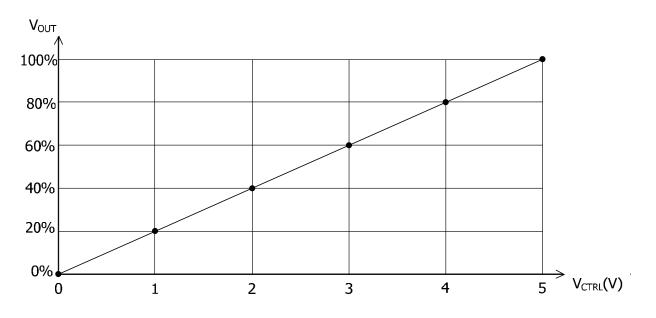
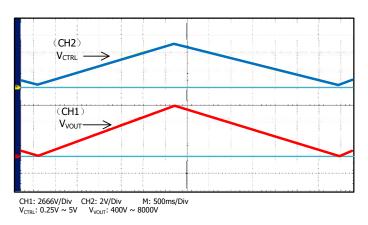
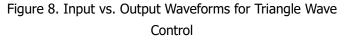


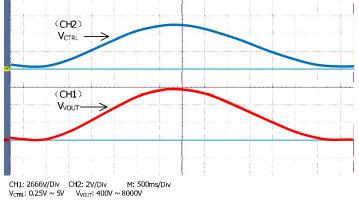
Figure 7. V<sub>CTRL</sub> vs. V<sub>VOUT</sub>

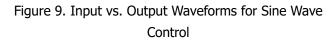
#### **AC Testing**

To test the analog modulation function, a triangle and sine-wave voltage signals are applied to the CTRL port as the input source signal respectively. Figure 8 and 9 show both the input signal and the output signal waveforms when using the triangle and sine-wave signals at the CTRL port respectively.





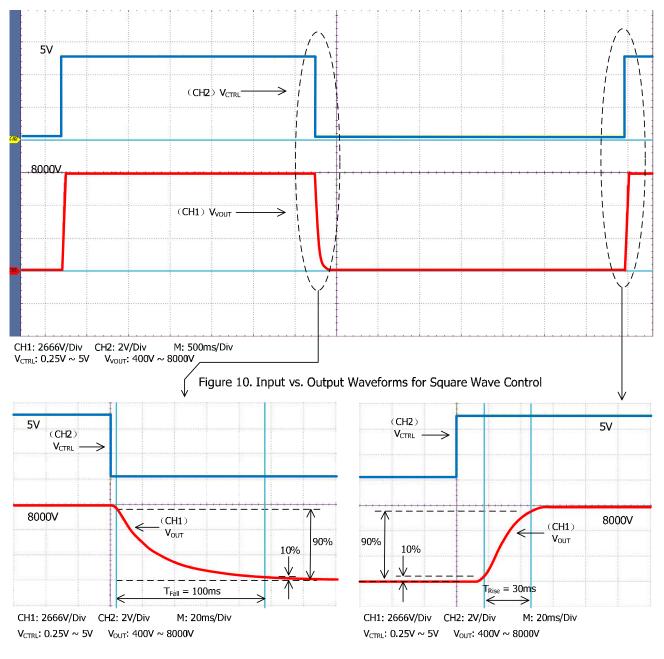








To test the rise and fall times at the output, a step function signal is applied to the CTRL port. The testing results are shown in Figure 10, Figure 11, and Figure 12. As shown in Figure 11 and Figure 12, a square wave of  $0.25V \sim 5V$ , f = 0.10Hz, is applied to CTRL port, the output waveform fall time is measured to be about 100ms and the rise time is about 30ms. These two values are not the same, that is because on the rising trail, the power supply injects a current to the load; while on the falling trail, the best the power supply can do is to stop its output current and let the load resistor drain the output filtering capacitor to a lower voltage, and the draining current is much smaller than the injection current.



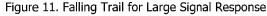
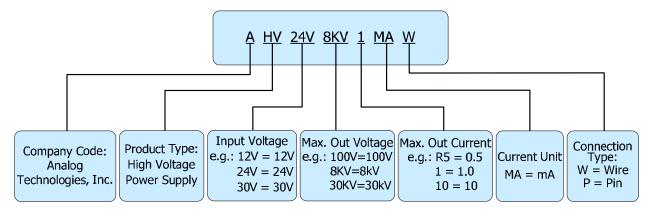


Figure 12. Rising Trail for Large Signal Response



# NAMING PRINCIPLE



Naming Principle of AHV24V8KV1MAW

## **DIMENSIONS**

#### **Connecting Lead Wire Sizes and Lengths**

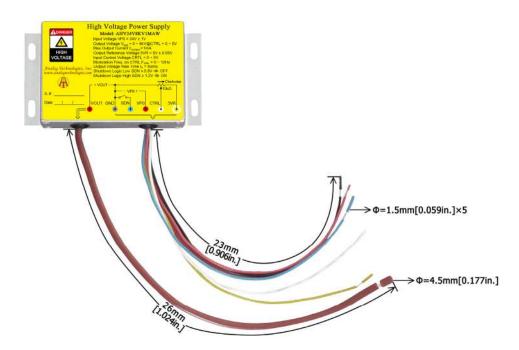


Figure 13. Connecting Lead Wires of AHV24V8KV1MAW

Lead Wires	Diameter		Length	
	mm	inch	mm	inch
Thick brown lead wire	4.5	0.177	26 ± 1	1.024 ± 0.039
Yellow, red, blue, black and white lead wires	1.5	0.059	23 ± 1	0.906 ± 0.039

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AHV24V8KV1MAW

#### **Outline Dimensions**

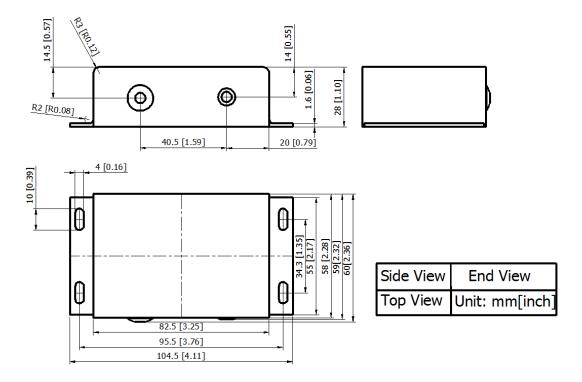


Figure 14. Outline Dimensions

### **ORDERING INFORMATION**

Quantity	1~9pcs	<b>10~49pcs</b>	10~49pcs 50~99pcs	
AHV24V8KV1MAW	\$169	\$159	\$149	\$139



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