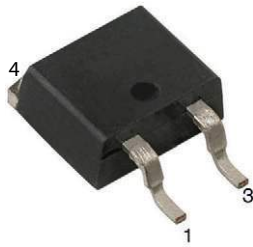
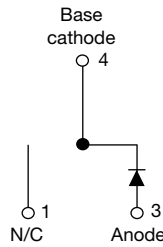


650 V Gen 3 Power SiC Merged PIN Schottky Diode, 6 A


D²PAK 2L (TO-263AB 2L)

RoHS
 COMPLIANT
 HALOGEN
FREE

FEATURES

- Majority carrier diode using Schottky technology on SiC wide band gap material
- Improved V_F and efficiency by thin wafer technology
- Positive V_F temperature coefficient for easy paralleling
- Virtually no recovery tail and no switching losses
- Temperature invariant switching behavior
- 175 °C maximum operating junction temperature
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- MPS structure for high ruggedness to forward current surge events
- Meets JESD 201 class 1A whisker test
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS	
$I_{F(AV)}$	6 A
V_R	650 V
V_F at I_F at 150 °C	1.5 V
T_J max.	175 °C
I_R at V_R at 175 °C	1.3 μ A
Q_C ($V_R = 400$ V)	17 nC
Package	D ² PAK 2L (TO-263AB 2L)
Circuit configuration	Single

DESCRIPTION / APPLICATIONS

Wide band gap SiC based 650 V Schottky diode, designed for high performance and ruggedness.

Optimum choice for high speed hard switching and efficient operation over a wide temperature range, it is also recommended for all applications suffering from Silicon ultrafast recovery behavior.

Typical applications include AC/DC PFC and DC/DC ultra high frequency output rectification in FBPS and LLC converters.

MECHANICAL DATA

Case: D²PAK 2L (TO-263AB 2L)

Molding compound meets UL 94 V-0 flammability rating
 Base P/N-M3 - halogen-free, RoHS-compliant

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

ABSOLUTE MAXIMUM RATINGS ($T_A = 25$ °C unless otherwise specified)				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	V_{RRM}		650	V
Average rectified forward current	$I_{F(AV)}$	$T_C = 141$ °C (DC)	6	A
DC blocking voltage	V_{DC}		650	V
Repetitive peak forward current	I_{FRM}	$T_C = 25$ °C, $f = 50$ Hz, square wave, DC = 25 %	28	A
Non-repetitive peak forward surge current	I_{FSM}	$T_C = 25$ °C, $t_p = 10$ ms, half sine wave	42	
		$T_C = 110$ °C, $t_p = 10$ ms, half sine wave	40	
Power dissipation	$P_{tot}^{(1)}$	$T_C = 25$ °C	50	W
		$T_C = 110$ °C	22	
I^2t value	$\int i^2 dt$	$T_C = 25$ °C	9	A ² s
		$T_C = 110$ °C	8	
Operating junction and storage temperatures	$T_J^{(2)}, T_{Stg}$		-55 to +175	°C

Notes

(1) Based on maximum R_{th}

(2) The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{thJA}$



ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Forward voltage	V _F	I _F = 6 A	-	1.3	1.5	V
		I _F = 6 A, T _J = 150 °C	-	1.5	1.75	
		I _F = 6 A, T _J = 175 °C	-	1.58	-	
Reverse leakage current	I _R	V _R = V _R rated	-	0.2	35	μA
		V _R = V _R rated, T _J = 150 °C	-	0.8	75	
		V _R = V _R rated, T _J = 175 °C	-	1.3	-	
Total capacitance	C	V _R = 1 V, f = 1 MHz	-	255	-	pF
		V _R = 400 V, f = 1 MHz	-	27	-	
Total capacitive charge	Q _C	V _R = 400 V, f = 1 MHz	-	17	-	nC

THERMAL - MECHANICAL SPECIFICATIONS (T _A = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Thermal resistance, junction to case	R _{thJC}		-	2.3	3.0	°C/W
Marking device			3C06ET07S			

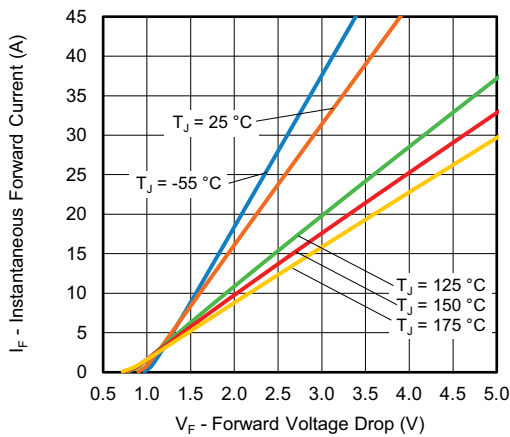


Fig. 1 - Typical Forward Voltage Drop Characteristics

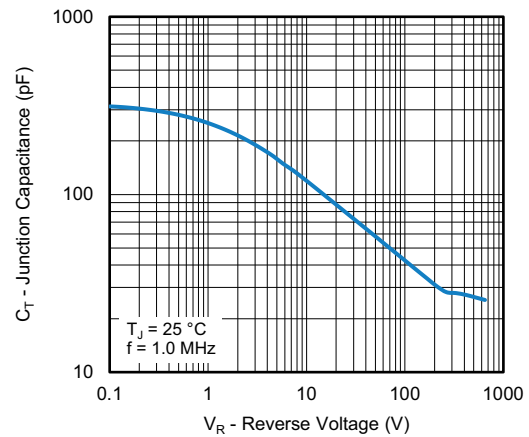


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

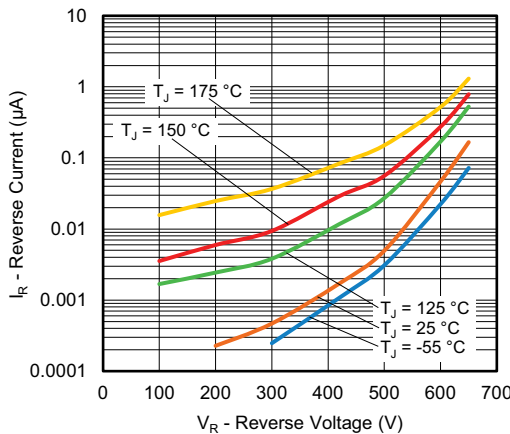


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

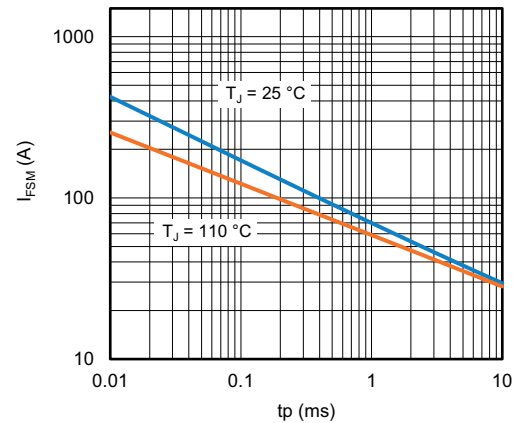


Fig. 4 - Non-Repetitive Peak Forward Surge Current vs. Pulse Duration (Square Wave)

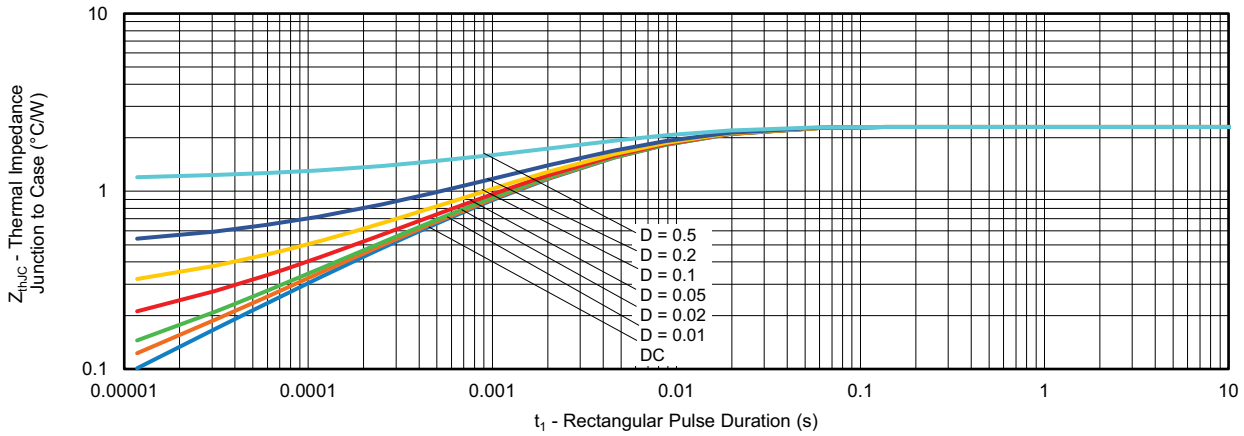


Fig. 5 - Typical Thermal Impedance Z_{thJC} Characteristics

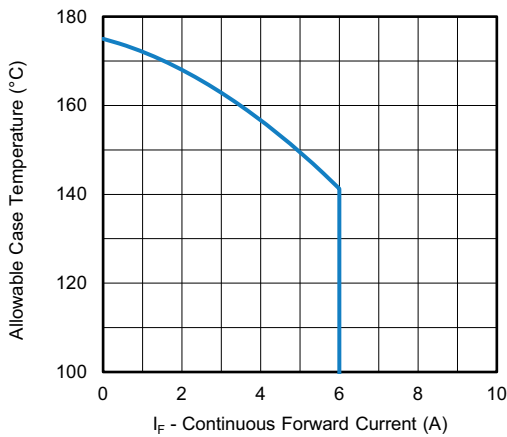


Fig. 6 - Maximum Allowable Case Temperature vs. Average Forward Current

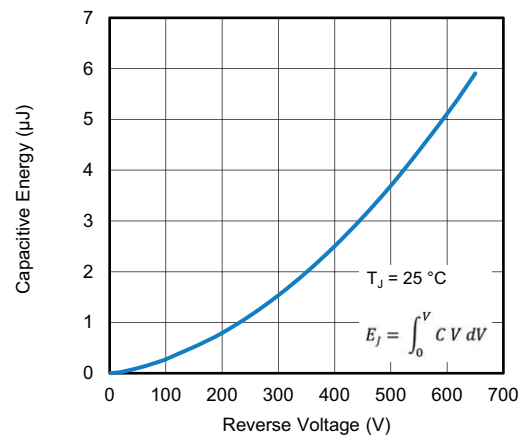


Fig. 8 - Typical Capacitive Energy vs. Reverse Voltage

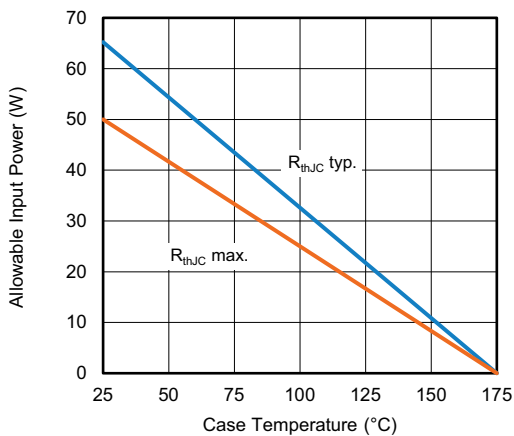


Fig. 7 - Forward Power Loss Characteristics

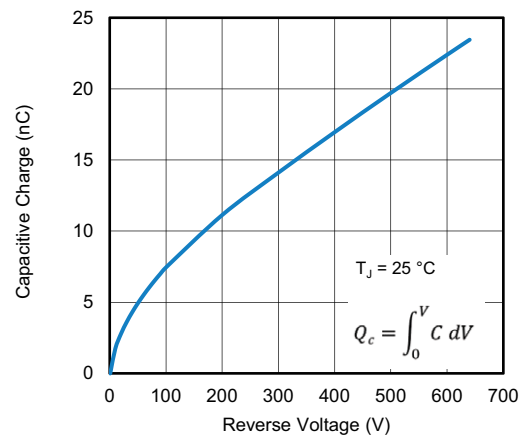
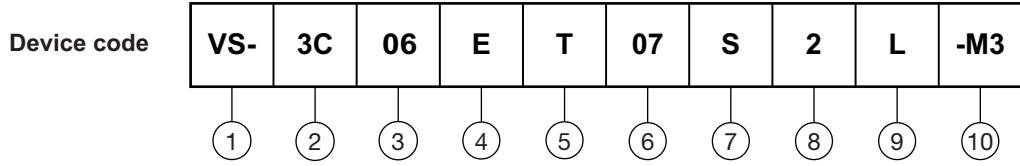


Fig. 9 - Typical Capacitive Charge vs. Reverse Voltage



ORDERING INFORMATION TABLE



- 1** - Vishay Semiconductors product
- 2** - 3C = SiC diode, generation 3
- 3** - Current rating (06 = 6 A)
- 4** - E = single diode
- 5** - T = D²PAK package
- 6** - Voltage rating: (07 = 650 V)
- 7** - S = surface mountable
- 8** - 2 = true 2 pin D²PAK
- 9** - L = tape and reel (left oriented)
- 10** - Environmental digit:
-M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

ORDERING INFORMATION		
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION
VS-3C06ET07S2L-M3	800 per reel	13" diameter reel

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?96683
Part marking information	www.vishay.com/doc?96693
Packaging information	www.vishay.com/doc?95032



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