RoHS

COMPLIANT

HALOGEN FREE

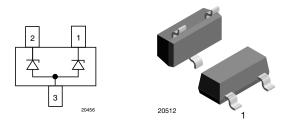
<u>GREEN</u>

(5-2008)



× /· S

# **Dual-Line Unidirectional ESD Protection Diode in SOT-23**



#### **MARKING** (example only)



YYY = type code (see table below) XX = date code

#### LINKS TO ADDITIONAL RESOURCES



Vishay	Semicond	luctors
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#### **FEATURES**

- Small SOT-23 package
- AEC-Q101 qualified available
- 2-line unidirectional ESD protection
- Working range 33 V
- Low leakage current I<sub>R</sub> < 0.05 μA</li>
- Low load capacitance C<sub>D</sub> < 18 pF</li>
- ESD immunity acc. IEC 61000-4-2 ± 15 kV contact discharge ± 15 kV air discharge
- e3 pins plated with tin (Sn)
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

ORDERING INFORMATION							
	ENVIRONMENTAL AND QUALITY CODE				PACKAG		
PART NUMBER AEC-Q1( (EXAMPLE) QUALIFI	AEC-Q101 OLIAL FIED RoHS-COMPLIANT + LEAD (Pb)-FREE TERMINATIONS		TIN PLATED	3K PER 7" REEL (8 mm TAPE)	10K PER 13" REEL (8 mm TAPE)	ORDERING CODE (EXAMPLE)	
	QUALIFIED	STANDARD	GREEN	FLAIED	15K/BOX = MOQ	10K/BOX = MOQ	
VESD33A2-03S	-	G	-	3	-08	-	VESD33A2-03S-G3-08
VESD33A2-03S	Н	G	-	3	-08	-	VESD33A2-03SHG3-08
VESD33A2-03S	-	G	-	3	-	-18	VESD33A2-03S-G3-18
VESD33A2-03S	Н	G	-	3	-	-18	VESD33A2-03SHG3-18

PACKAGE DATA							
DEVICE NAME	PACKAGE NAME	TYPE CODE	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS	
VESD33A2-03S	SOT-23	D33	8.1 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	Peak temperature max. 260 °C	

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, between pin 1 - 3 or 2 - 3, unless otherwise specified)						
PARAMETER	R TEST CONDITIONS			UNIT		
Peak pulse current	Acc. IEC 61000-4-5, 8/20 µs/single shot	I <sub>PPM</sub>	1.6	А		
Peak pulse power	Acc. IEC 61000-4-5, 8/20 µs/single shot	P <sub>PP</sub>	100	W		
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V	15	kV		
	Air discharge acc. IEC 61000-4-2; 10 pulses	V <sub>ESD</sub>	15	kV		
Operating temperature	Junction temperature	TJ	-55 to +150	°C		
Storage temperature		T <sub>stg</sub>	-55 to +150	°C		



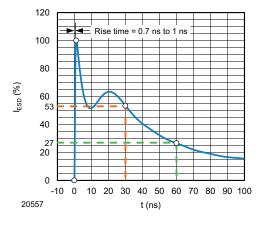
www.vishay.com

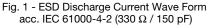
### VESD33A2-03S

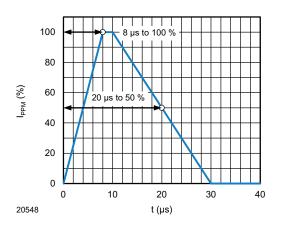
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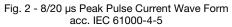
<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, between pin 1 - 3 or 2 - 3, unless otherwise specified)							
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines	
Reverse stand off voltage	Max. reverse working voltage	V <sub>RWM</sub>	-	-	33	V	
Reverse voltage	at I <sub>R</sub> = 0.1 μA	V <sub>R</sub>	33	-	-	V	
Reverse current	at V <sub>R</sub> = 33 V	I <sub>R</sub>	-	< 0.01	0.1	μA	
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	V <sub>BR</sub>	35.5	37.4	39.3	V	
Reverse clamping voltage	at I <sub>PP</sub> = I <sub>PPM</sub> = 1.6 A, $t_p$ = 8/20 µs	Vc	-	56	62.5	V	
Forward clamping voltage	at I <sub>PP</sub> = 1 A, t <sub>p</sub> = 300 μs	V <sub>F</sub>	0.9	1.1	1.2	V	
	at $I_{PP} = I_{PPM} = 1.6 \text{ A}, t_p = 8/20 \ \mu\text{s}$	V <sub>F</sub>	-	1.22	1.32	V	
Dynamic resistance	t <sub>p</sub> = 100 ns (TLP; 1 A to 12 A)	r <sub>dyn</sub>	-	3.6	-	Ω	
Capacitance	at $V_R = 0 V$ ; f = 1 MHz	CD	12	15	18	pF	

TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)









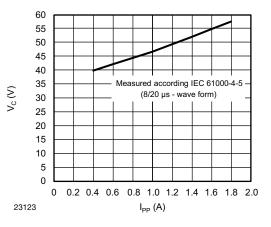


Fig. 3 - Typical Peak Clamping Voltage vs. Peak Pulse Current

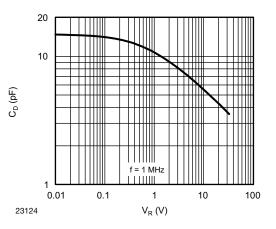


Fig. 4 - Typical Capacitance vs. Reverse Voltage

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### Not For New Designs



**VESD33A2-03S** 

## Vishay Semiconductors

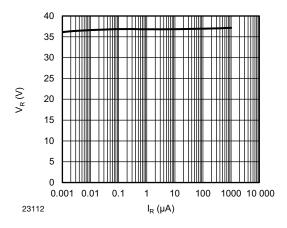


Fig. 5 - Typical Reverse Voltage vs. Reverse Current

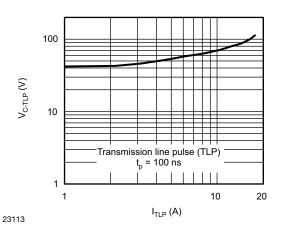


Fig. 6 - Typical Clamping Voltage vs. Peak Pulse Current

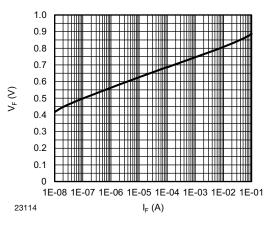


Fig. 7 - Typical Forward Voltage vs. Forward Current

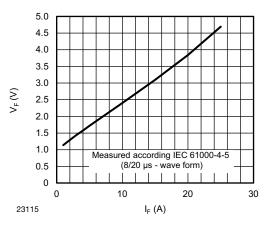
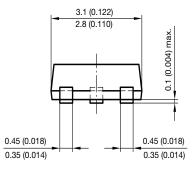


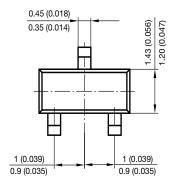
Fig. 8 - Typical Forward Voltage vs. Forward Current

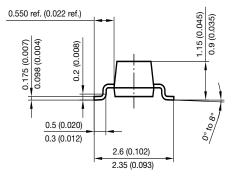


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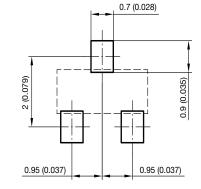
#### PACKAGE DIMENSIONS in millimeters (inches) SOT-23





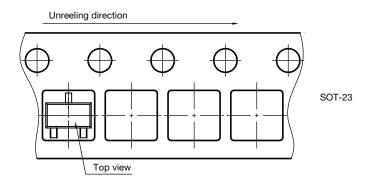


Foot print recommendation:



Document no.: 6.541-5014.01-4 Rev. 8 - Date: 23. Sep. 2009 17418

#### **ORIENTATION IN CARRIER TAPE SOT-23**



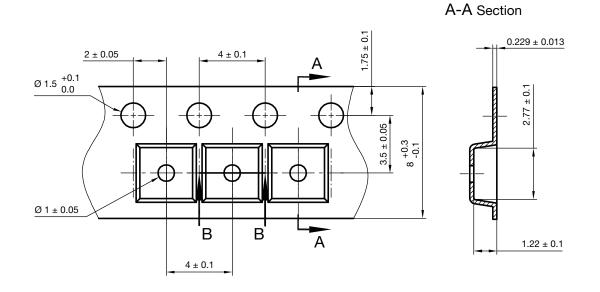
Orientation in carrier tape SOT-23 S8-V-3929.01-006 (4) 04.02.2010 22607



### **VESD33A2-03S**

### Vishay Semiconductors

### **CARRIER TAPE SOT-23**



B-B Section



Carrier tape SOT-23 Document no.: S8-V-3929.01-005 (4) Created - Date: 04. Feb. 2010 22856

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