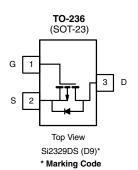




# P-Channel 8 V (D-S) MOSFET

MOSFET PRODUCT SUMMARY						
V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)			
	0.030 at V <sub>GS</sub> = - 4.5 V	- 6 <sup>e</sup>				
- 8	0.036 at V <sub>GS</sub> = - 2.5 V	- 6 <sup>e</sup>				
	0.048 at V <sub>GS</sub> = - 1.8 V	- 5.9	11.8 nC			
	0.068 at V <sub>GS</sub> = - 1.5 V	- 5				
	0.120 at $V_{GS} = -1.2 \text{ V}$	- 3.7				



#### **FEATURES**

- Halogen-free According to IEC 61249-2-21 **Definition**
- TrenchFET® Power MOSFET
- 100 % R<sub>q</sub> Tested
- Compliant to RoHS Directive 2002/95/EC

HALOGEN **FREE** 

#### **APPLICATIONS**

- Load Switch
- Low Voltage Gate Drive
  - Low On-Resistance
- Battery Management in Portable Equipment

Ordering Information: Si2329DS-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS $(T_A =$	25 °C, unless otl	nerwise noted)		
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V <sub>DS</sub>	- 8	V	
Gate-Source Voltage	$V_{GS}$	± 5	v	
	T <sub>C</sub> = 25 °C		- 6 <sup>e</sup>	
Continuous Drain Current (T <sub>.1</sub> = 150 °C)	T <sub>C</sub> = 70 °C		- 6	
Continuous Diain Curient (1) = 130 C)	T <sub>A</sub> = 25 °C	ID	- 5.3 <sup>b, c</sup>	
	T <sub>A</sub> = 70 °C		- 4.2 <sup>b, c</sup>	A
Pulsed Drain Current (t = 300 μs)	I <sub>DM</sub>	- 20		
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	l-	- 2.1	
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	ls -	- 1.0 <sup>b, c</sup>	
	T <sub>C</sub> = 25 °C		2.5	
Maximum Power Dissipation	T <sub>C</sub> = 70 °C	P <sub>D</sub>	1.6	w
iviaximum rowei Dissipation	T <sub>A</sub> = 25 °C	' D	1.25 <sup>b, c</sup>	vv
	T <sub>A</sub> = 70 °C		0.8 <sup>b, c</sup>	
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>sta</sub>	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient <sup>b, d</sup>	≤ 5 s	R <sub>thJA</sub>	75	100	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	$R_{thJF}$	40	50			

#### Notes:

- a. Based on  $T_C = 25$  °C.
- b. Surface mounted on 1" x 1" FR4 board.
- d. Maximum under steady state conditions is 166 °C/W.
- e. Package limited.



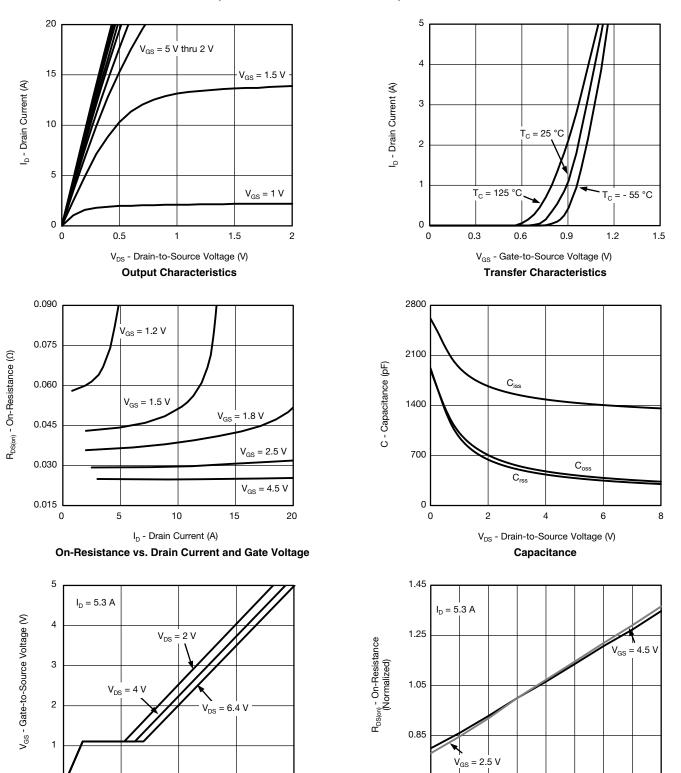
MOSFET SPECIFICATIONS			I	_			
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	,	V 0VI 050 A	1 -	1	T	1	
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 8			V	
V <sub>DS</sub> Temperature Coefficient	ΔV <sub>DS</sub> /T <sub>J</sub>	I <sub>D</sub> = - 250 μA		- 6		mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	<u>-</u>		2.3			
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu\text{A}$	- 0.35		- 0.8	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 5 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = -8 \text{ V}, V_{GS} = 0 \text{ V}$ $V_{DS} = -8 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 55 \text{ °C}$			- 1	μΑ	
	500				- 10		
On-State Drain Current <sup>a</sup>	$I_{D(on)}$	$V_{DS} \le -5 \text{ V}, V_{GS} = -5.3 \text{ V}$	- 20			Α	
		$V_{GS} = -4.5 \text{ V}, I_D = -5.3 \text{ A}$		0.025	0.030		
		$V_{GS} = -2.5 \text{ V}, I_D = -4.8 \text{ A}$		0.030	0.036	7	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 1.8 V, I <sub>D</sub> = - 4.2 A		0.037	0.048	Ω	
		$V_{GS} = -1.5 \text{ V}, I_D = -3.5 \text{ A}$		0.045	0.068		
		V <sub>GS</sub> = - 1.2 V, I <sub>D</sub> = - 0.8 A		0.060	0.120		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 4 V, I <sub>D</sub> = - 5.3 A		2.0		S	
Dynamic <sup>b</sup>						•	
Input Capacitance	C <sub>iss</sub>			1485			
Output Capacitance	C <sub>oss</sub>	$V_{DS} = -4 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		480		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			435			
Total Cata Chause	Qg	$V_{DS} = -4 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -5.3 \text{ A}$		19.3	29		
Total Gate Charge				11.8	18		
Gate-Source Charge	Q <sub>gs</sub>	$Q_{gs}$ $V_{DS} = -4 \text{ V}, V_{GS} = -2.5 \text{ V}, I_D = -5.3 \text{ A}$		1.7		nC	
Gate-Drain Charge	$Q_{gd}$			6.2			
Gate Resistance	$R_g$	f = 1 MHz	0.8	4.2	8.4	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			20	30		
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 4 V, $R_L$ = 0.9 $\Omega$		22	33		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D = -4.2 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_g = 1 \Omega$		46	69	ns	
Fall Time	t <sub>f</sub>	-		20	30		
<b>Drain-Source Body Diode Characteristi</b>	cs		l		l		
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			- 2.1		
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				- 20	A	
Body Diode Voltage	$V_{SD}$	I <sub>S</sub> = - 4.2 A		- 0.8	- 1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			40	60	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			26	39	nC	
Reverse Recovery Fall Time	t <sub>a</sub>	$I_F = -4.2 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		17			
Reverse Recovery Rise Time		t <sub>b</sub>		23		ns	

- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



0

0

5

Q<sub>a</sub> - Total Gate Charge (nC)

**Gate Charge** 

125

150

0.65

- 50

- 25

0

25

50

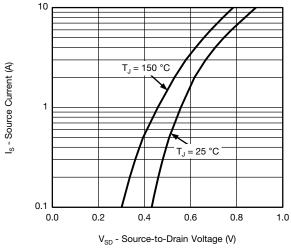
T<sub>J</sub> - Junction Temperature (°C)

On-Resistance vs. Junction Temperature

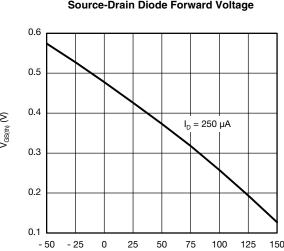
75

100

### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Source-Drain Diode Forward Voltage

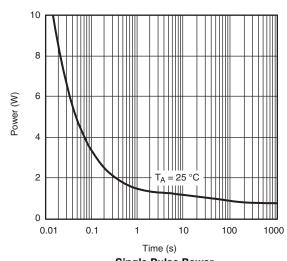


T<sub>J</sub> - Temperature (°C)

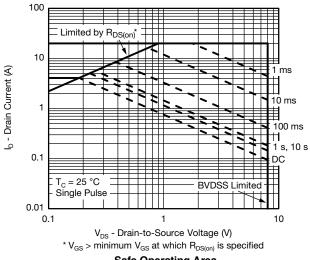
**Threshold Voltage** 

0.06  $I_D = 5.3 A$ 0.05 R<sub>DS(on)</sub> - On-Resistance (Ω) 0.04 T<sub>J</sub> = 125 °C 0.03  $T_J = 25$  °C 0.02 3 5

 $V_{\rm GS}$  - Gate-to-Source Voltage (V) On-Resistance vs. Gate-to-Source Voltage

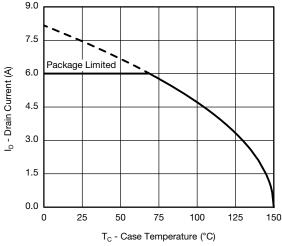


Single Pulse Power

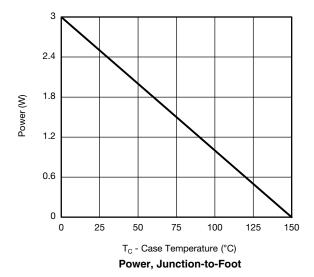


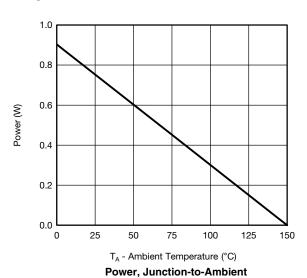


### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



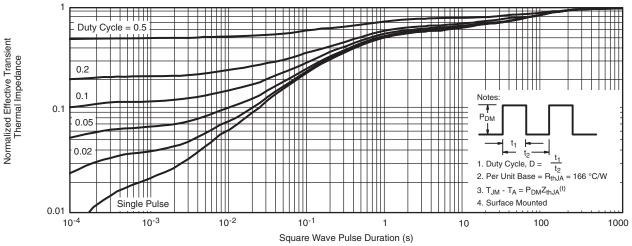




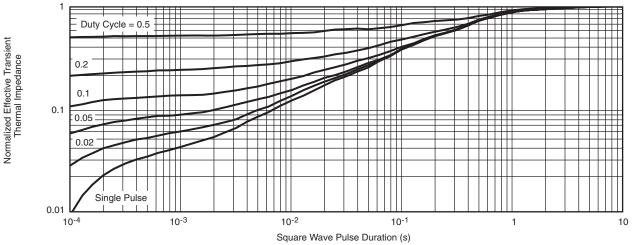


 $<sup>^*</sup>$  The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



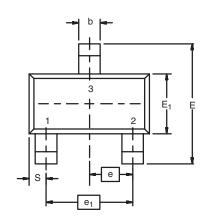
Normalized Thermal Transient Impedance, Junction-to-Ambient

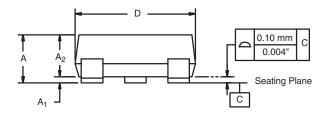


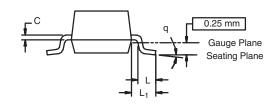
Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppq?67690.

## SOT-23 (TO-236): 3-LEAD







Dim	MILLI	METERS	INCHES			
	Min	Max	Min	Max		
Α	0.89	1.12	0.035	0.044		
A <sub>1</sub>	0.01	0.10	0.0004	0.004		
A <sub>2</sub>	0.88	1.02	0.0346	0.040		
b	0.35	0.50	0.014	0.020		
С	0.085	0.18	0.003	0.007		
D	2.80	3.04	0.110	0.120		
Е	2.10	2.64	0.083	0.104		
E <sub>1</sub>	1.20	1.40	0.047	0.055		
е	0.9	5 BSC	0.037	4 Ref		
e <sub>1</sub>	1.9	0 BSC	0.074	0.0748 Ref		
L	0.40	0.60	0.016	0.024		
L <sub>1</sub>	0.6	64 Ref 0.025 Re		5 Ref		
S	0.5	50 Ref	0.020 Ref			
q	3°	8°	3°	8°		
FCN: S-03946-Rev K 09-	lul-01					

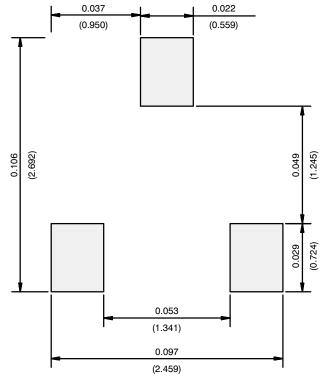
ECN: S-03946-Rev. K, 09-Jul-01

DWG: 5479

Document Number: 71196 www.vishay.com 09-Jul-01



### **RECOMMENDED MINIMUM PADS FOR SOT-23**



Recommended Minimum Pads Dimensions in Inches/(mm)

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APPLICATION NOTE



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