



# OM13053



LPC812-LPCXpresso Board

## Demo board description

The LPC812-LPCXpresso board with NXP's LPC812 Cortex-M0+ microcontroller is designed to make it as easy as possible to get started with your project. Combined with the the full-featured, easy-to-use Eclipse-based LPCXpresso IDE, the entire product design cycle for the LPC800 is supported.

The LPCXpresso development platform is jointly developed by NXP, Code Red, and Embedded Artists.

The order code for this board is **OM13053**

## Features

- NXP's LPC812 Cortex-M0+ microcontroller in TSSOP20 package
- 16kB of Flash, 4kB of Data Memory
- 12.000 MHz crystal for CPU
- 3.15V-3.3V external powering, or from USB via on-board LPC-LINK JTAG probe.
- UART header compatible with C232HD-DDHSP-0 cable (or similar) for In-System Programming
- All LPC812 pins available on expansion connector (2x27 pin rows, 100 mil pitch, 900 mil between rows)
- Embedded JTAG (LPC-LINK) functionality via LPCXpresso IDE.
- LPC-LINK can be connected to external target processor after modifications to the LPCXpresso board
- RGB LED connected to PIO0\_7, PIO0\_16 and PIO0\_17
- Potentiometer connected to ACMP\_11

## Descriptive summary

### LPCXpresso Integrated Development Environment

This Eclipse-based software development environment, available for Window, Linux and Mac OS X, includes all the tools necessary to develop high-quality software solutions in less time and with a lower budget. Along with the latest version of the industry-standard Eclipse GNU toolchain, the IDE includes a proprietary, optimized C library, and can be used to build an executable of any size, with full code optimization

### Available downloads

[LPCWare](#) - Sample code and LPC800 downloads.

[LPCXpresso Forums](#)

Download the [LPCXpresso IDE](#)

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## Demo boards

Type number	Ordering code(12NC)	Orderable part number	Products status	Region	Distributor	In stock	Order quantity	Inventory date	Buy online
OM13053	9352 999 66598		Volume production						

## Products

Type number	Description	Status	Quick access
<a href="#">LPC810M021FN8</a>	32-bit ARM Cortex-M0+ microcontroller; 4 kB flash and 1 kB SRAM	<a href="#">Development</a>	<a href="#">Download datasheet</a>
<a href="#">LPC811M001FDH16</a>	32-bit ARM Cortex-M0+ microcontroller; 8 kB flash and 2 kB SRAM	<a href="#">Development</a>	<a href="#">Download datasheet</a>
<a href="#">LPC812M101FD20</a>	32-bit ARM Cortex-M0+ microcontroller; 16 kB flash and 4 kB SRAM	<a href="#">Development</a>	<a href="#">Download datasheet</a>
<a href="#">LPC812M101FDH16</a>	32-bit ARM Cortex-M0+ microcontroller; 16 kB flash and 4 kB SRAM	<a href="#">Development</a>	<a href="#">Download datasheet</a>
<a href="#">LPC812M101FDH20</a>	32-bit ARM Cortex-M0+ microcontroller; 16 kB flash and 4 kB SRAM	<a href="#">Development</a>	<a href="#">Download datasheet</a>

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