

Ordering Guide

Sample Part Number: SKHC1-071-06-C-T100-NS-T0-F0-AIO

Chip Type	Solder Type	Sealant	Thickness Tolerance	Flatness Tolerance	Ceramic Type	Ceramic Surface
SKHC1-071-06-C	T100	NS	T0	F0	AIO	-
SKHC1-071-06-C 71 couples, 6A max	T100 BiSn (up to 138C)	NS No Sealant	T0 ±0.1 mm	F0 ±0.05 mm	AIO Alumina (96% white)	- None

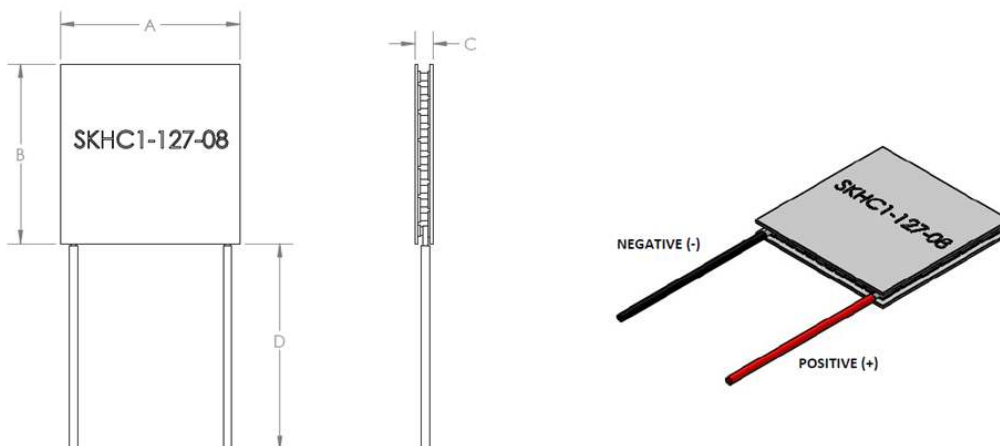
Performance Metrics

Cat Logic	Hot Side Temp T_h (°C)	ΔT_{max} (°C)	Q_{max} (Watts)	I_{max} (Amps)	V_{max} (DC Volts)	R_{AC} (Ω)	TE Dimensions (mm)			
							A	B	C	D
SKHC1-071-06-C	27	72	35.6	6.4	8.9	1.06	22	22	3.8	150
SKHC1-071-06-C	50	81	41.1	6.8	9.6	1.06	22	22	3.8	150

Notes:

- All performance values fall within $\pm 10\%$ tolerance of tested data and/or models.
- Dimensions are typical and subject to change based on exact configuration ordered.
- Contact Sheetak at info@sheetak.com for additional performance data, chip options or customization

Chip Dimensions



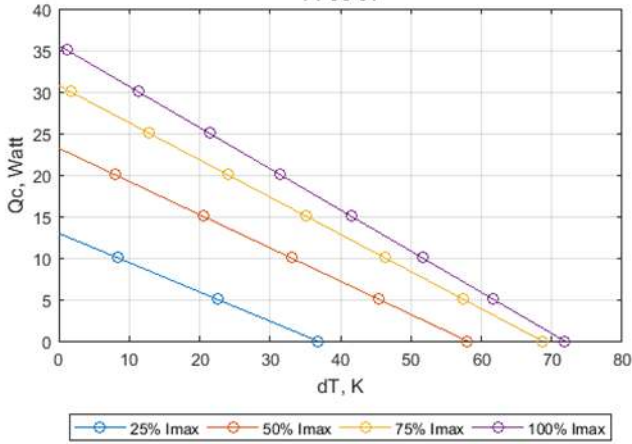
Notes:

- Please see the *Ordering Guide* for flatness and thickness tolerances.
- Ceramic face with part number is the cold side and wires are soldered on the hot side.
- Some TECs may have a porch design or wires soldered on the side.

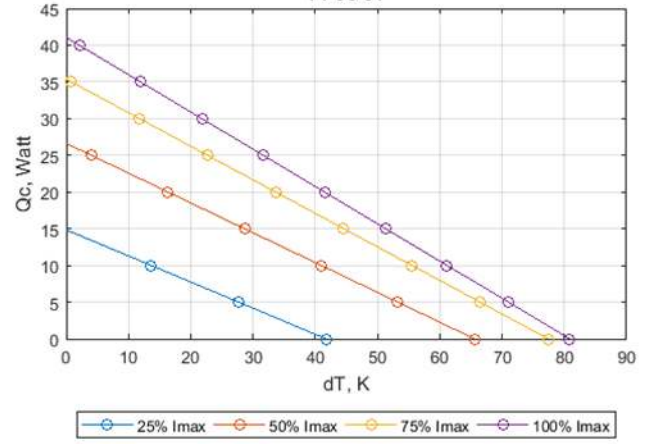
Performance Curves @ $T_H = 27^\circ\text{C}$

Performance Curves @ $T_H = 50^\circ\text{C}$

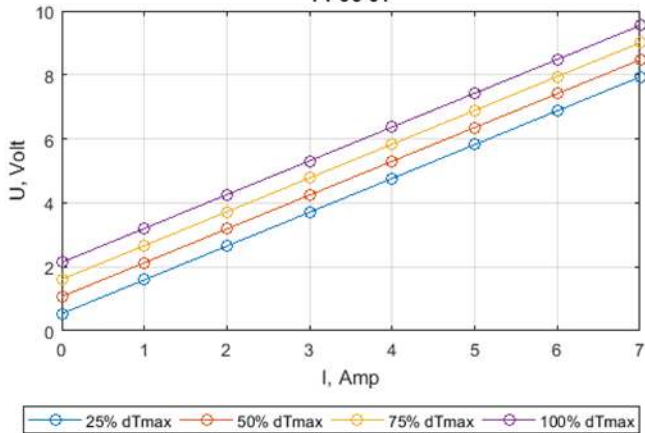
Standard Performance Graph No1: $Q_c = f(dT)$
71-06-01



Standard Performance Graph No1: $Q_c = f(dT)$
71-06-01



Standard Performance Graph No4: $U = f(I)$
71-06-01



Standard Performance Graph No4: $U = f(I)$
71-06-01

