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November 2013

FDP100N10

N-Channel PowerTrench® MOSFET 100 V, 75 A, 10 m Ω

Features

- $R_{DS(on)}$ = 8.2 m Ω (Typ.) @ V_{GS} = 10 V, I_D = 75 A
- · Fast Switching Speed
- · Low Gate Charge
- High Performance Trench Technology for Extremely Low $R_{DS(\text{on})}$
- · High Power and Current Handling Capability
- RoHS Compliant

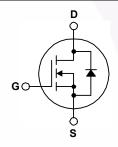
Description

This N-Channel MOSFET is produced using Fairchild Semiconductor's advanced PowerTrench® process that has been tailored to minimize the on-state resistance while maintaining superior switching performance.

Applications

- · Synchronous Rectification for ATX / Server / Telecom PSU
- · Battery Protection Circuit
- · Motor Drives and Uninterruptible Power Supplies
- · Micor Solar Inverter





MOSFET Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol		Parameter		FDP100N10	Unit
V _{DSS}	Drain to Source Voltage	Drain to Source Voltage			V
V_{GSS}	Gate to Source Voltage	Gate to Source Voltage			V
I _D	Drain Current	- Continuous (T _C = 75°C)		75	Α
I _{DM}	Drain Current	- Pulsed	(Note 1)	300	Α
E _{AS}	Single Pulsed Avalanche Energy (Note 2)			365	mJ
dv/dt	Peak Diode Recovery dv/o	Peak Diode Recovery dv/dt (Note 3)		6	V/ns
D	Dower Dissination	$(T_C = 25^{\circ}C)$		208	W
P_{D}	Power Dissipation	- Derate Above 25°C		1.4	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range			-55 to +175	°С
T_L	Maximum Lead Temperate	ure for Soldering, 1/8" from Case t	for 5 Seconds	300	°C

Thermal Characteristics

Symbol	Parameter	FDP100N10	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max. 0		°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	*C/VV

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FDP100N10	FDP100N10	TO-220	Tube	N/A	N/A	50 units

Electrical Characteristics $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Off Charac	cteristics					
BV_{DSS}	Drain to Source Breakdown Voltage	$I_D = 250 \mu A, V_{GS} = 0 V, T_J = 25^{\circ} C$	100	-	-	V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C	-	0.1	-	V/°C
	Zero Gate Voltage Drain Current	V _{DS} = 100 V, V _{GS} = 0 V	-	-	1	
IDSS	Zero Gate voltage Drain Current	$V_{DS} = 100 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 150^{\circ}\text{C}$	-	-	500	μA
I _{GSS}	Gate to Body Leakage Current	V _{GS} = ±20 V, V _{DS} = 0 V	-	-	±100	nA

On Characteristics

V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = 250 \mu\text{A}$	2.5	-	4.5	V
R _{DS(on)}	Static Drain to Source On Resistance	V _{GS} = 10 V, I _D = 75 A	-	8.2	10	mΩ
g _{FS}	Forward Transconductance	V _{DS} = 10 V, I _D = 37.5 A	-	110	-	S

Dynamic Characteristics

C _{iss}	Input Capacitance	V 05 V V 0 V	-	5500	7300	pF
C _{oss}	Output Capacitance	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	-	530	710	pF
C _{rss}	Reverse Transfer Capacitance	1 - 1 1011 12	-\	220	325	pF
Q _{g(tot)}	Total Gate Charge at 10V		- \	76	100	nC
Q _{gs}	Gate to Source Gate Charge	$V_{DS} = 50 \text{ V}, I_D = 75 \text{ A},$	- \	30	-	nC
Q_{gd}	Gate to Drain "Miller" Charge	$V_{GS} = 10 \text{ V}$ (Note 4)	-	20	-	nC

Switching Characteristics

t _{d(on)}	Turn-On Delay Time			-	70	150	ns
t _r	Turn-On Rise Time	$V_{DD} = 50 \text{ V}, I_{D} = 75 \text{ A},$		-	265	540	ns
t _{d(off)}	Turn-Off Delay Time	V_{GS} = 10 V, R_G = 25 Ω		-	125	260	ns
t _f	Turn-Off Fall Time		(Note 4)	-	115	240	ns

Drain-Source Diode Characteristics

I _S	Maximum Continuous Drain to Source Dioc	Maximum Continuous Drain to Source Diode Forward Current		-	75	Α
I _{SM}	Maximum Pulsed Drain to Source Diode Fo	Maximum Pulsed Drain to Source Diode Forward Current		-	300	Α
V_{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0 V, I _{SD} = 75 A	-	-	1.25	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _{SD} = 75 A,	-	71	-	ns
Q _{rr}	Reverse Recovery Charge	$dI_F/dt = 100 A/\mu s$	-	164	_	nC

Notes:

- 1: Repetitive rating: pulse-width limited by maximum junction temperature.
- 2: L = 0.13 mH, I $_{AS}$ = 75 A, V $_{DD}$ = 25 V, R $_{G}$ = 25 Ω , starting T $_{J}$ = 25°C.
- 3: $I_{SD} \le 75$ A, di/dt ≤ 200 A/ μ s, $V_{DD} \le BV_{DSS}$, starting T_J = 25°C.
- 4: Essentially independent of operating temperature typical characteristics.

Typical Performance Characteristics

Figure 1. On-Region Characteristics

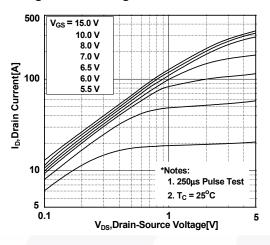


Figure 3. On-Resistance Variation vs.

Drain Current and Gate Voltage

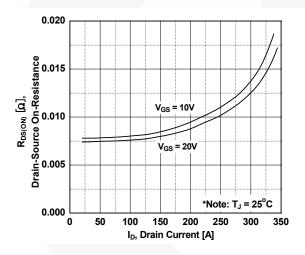


Figure 5. Capacitance Characteristics

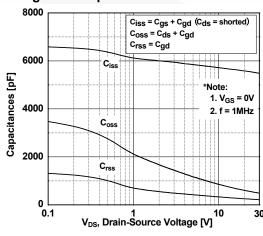


Figure 2. Transfer Characteristics

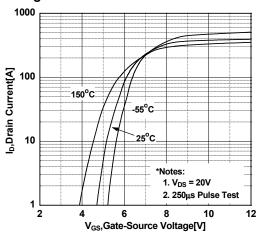


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

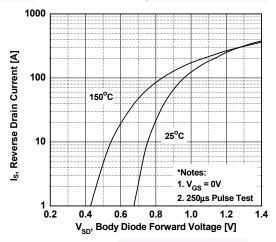
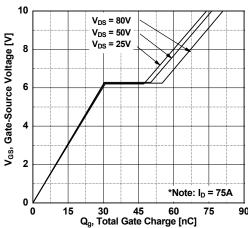


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

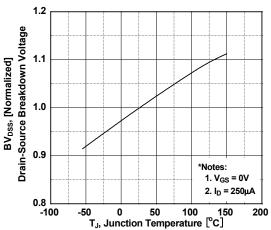


Figure 8. On-Resistance Variation vs. Temperature

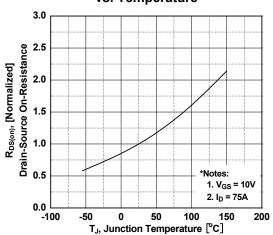
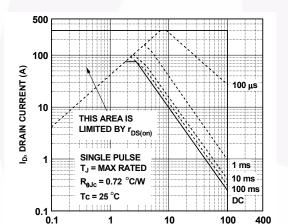


Figure 9. Maximum Safe Operating Area



V_{DS}, DRAIN to SOURCE VOLTAGE (V)

Figure 10. Maximum Drain Current vs. Case Temperature

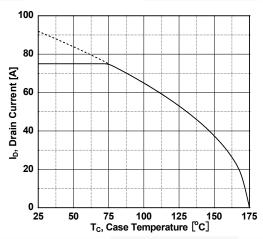
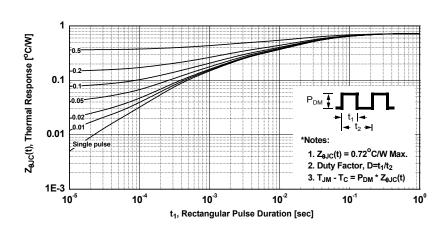


Figure 11. Transient Thermal Response Curve



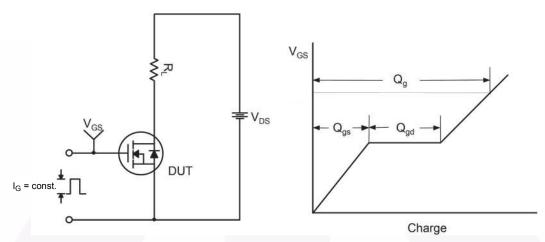


Figure 12. Gate Charge Test Circuit & Waveform

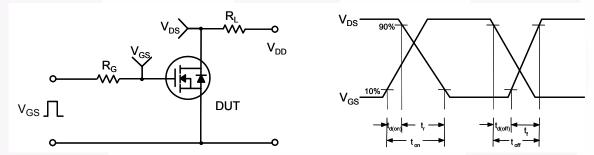


Figure 13. Resistive Switching Test Circuit & Waveforms

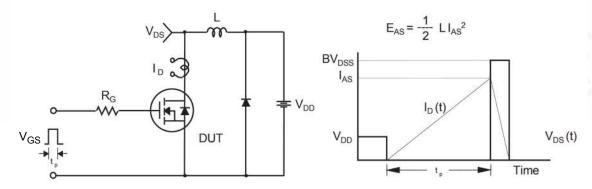


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms

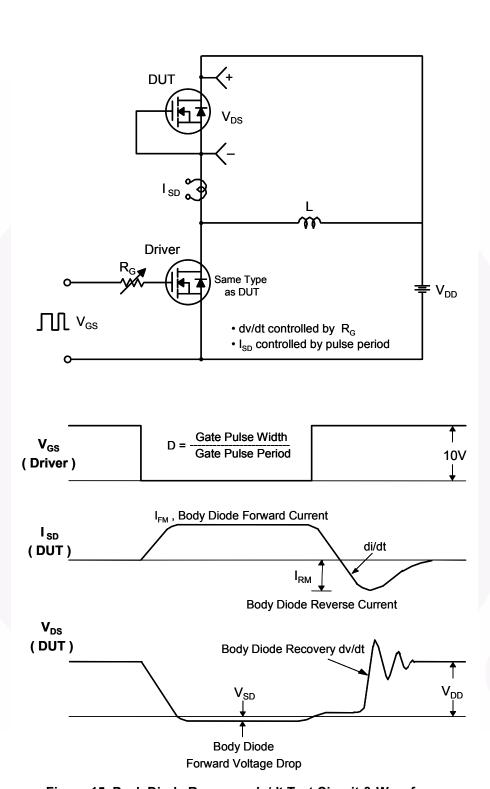


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

Mechanical Dimensions

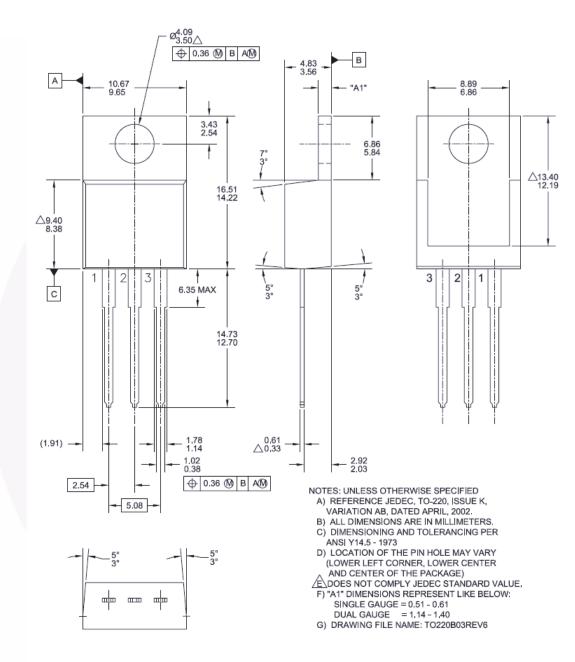


Figure 16. TO-220, Molded, 3-Lead, Jedec Variation AB

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Rev 166

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