

Engineering/Process Change Notice

ECN/PCN No.: 4135

For Manufacturer						
Product Description: PLASTIC SMD MEMS OSCILLATOR	Abracon Part Numb	e r / Part Series: RA21	□ Documentation only□ ECN⋈ EOL	Series □ Part Number		
Affected Revision:	New Revision:	OL	Application:	☐ Safety ☑ Non-Safety		
Prior to Change: Active https://abracon.com/datasheets/Ecliptek/EMRA21.pdf						
After Change: EOL						
Cause/Reason for Change: Discontinuation of manufacturing capability.						
	Char	ge Plan				
Effective Date: 2/7/2022	Additional Remarks: N/A					
Change Declaration: N/A						
Issued Date: 2/7/2022	Issued By: Brooke Cushman Product Engineer		Issued Department: Engineering			
Approval: Thomas Culhane Engineering Director	Approval: Reuben Quintanilla Quality Director		Approval: Ying Huang Purchasing Director			
	For Abrad	on EOL only				
Last Time Buy (if applicable): 5/7/2022		Alternate Part Number / Part Series: ASFLDV (5.0x3.2mm)				
Additional Approval:	Additional Approva	:	Additional Approval:			
	Customer Appr	oval (If Applicable)				
Qualification Status: Note: It is considered approved if there is n	☐ Approved	☐ Not accepted	r ECN/PCN is released.			
Customer Part Number:		Customer Project:				
Company Name:	Company Represent	rative:	Representative Signature	:		
Customer Remarks:						



Form #7020 | Rev. G | Effective: 02/22/2021 |













REGULATORY COMPLIANCE











ITEM DESCRIPTION

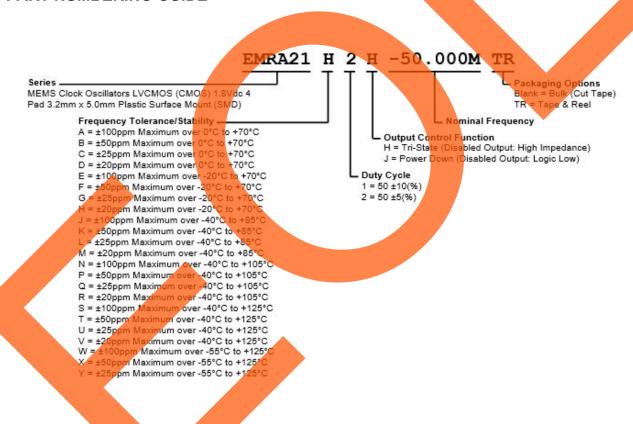
MEMS Clock Oscillators LVCMOS (CMOS) 1.8Vdc 4 Pad 3.2mm x 5.0mm Plastic Surface Mount (SMD)

ELECTRICAL SPECIFICA	TIONS
Nominal Frequency	1MHz to 137MHz
Frequency Tolerance/Stability	Inclusive of all conditions: Calibration Tolerance at 25°C, Prequency Stability over the Operating Temperature Range, Supply Voltage Change, and Output Load Change ±100ppm Maximum over 0°C to +70°C ±50ppm Maximum over 0°C to +70°C ±25ppm Maximum over 0°C to +70°C ±20ppm Maximum over -20°C to +70°C ±100ppm Maximum over -20°C to +70°C ±25ppm Maximum over -20°C to +70°C ±25ppm Maximum over -20°C to +70°C ±25ppm Maximum over -40°C to +85°C ±25ppm Maximum over -40°C to +85°C ±25ppm Maximum over -40°C to +85°C ±20ppm Maximum over -40°C to +105°C ±100ppm Maximum over -40°C to +105°C ±20ppm Maximum over -40°C to +105°C ±25ppm Maximum over -40°C to +105°C ±25ppm Maximum over -40°C to +105°C ±25ppm Maximum over -40°C to +125°C ±20ppm Maximum over -40°C to +125°C ±20ppm Maximum over -40°C to +125°C ±25ppm Maximum over -55°C to +125°C
Aging at 25°C	±1.5ppm Maximum First Year
Supply Voltage	1.8Vqc ±10%
Input Current	No Load 4.5mA Maximum over Nominal Frequency of 1MHz to 20MHz 5mA Maximum over Nominal Frequency of 20.000001MHz to 50MHz 6mA Maximum over Nominal Frequency of 50.000001MHz to 80MHz 7mA Maximum over Nominal Frequency of 80.000001MHz to 137MHz
Output Voltage Logic High (Voh)	IOH = -2mA 90% of Vdd Minimum
Output Voltage Logic Low (Vol)	IOL = +2mA 10% of Vdd Maximum
Rise/Fall Time	Measured from 20% to 80% of waveform 1.5nSec Typical, 3.5nSec Maximum
Duty Cycle	Measured at 50% of waveform 50 ±10(%) 50 ±5(%)
Load Drive Capability	15pF Maximum
Output Logic Type	CMOS
Output Control Function	Tri-State (Disabled Output: High Impedance) Power Down (Disabled Output: Logic Low)
Output Control Input Voltage Logic High (Vih)	70% of Vdd Minimum or No Connect to Enable Output



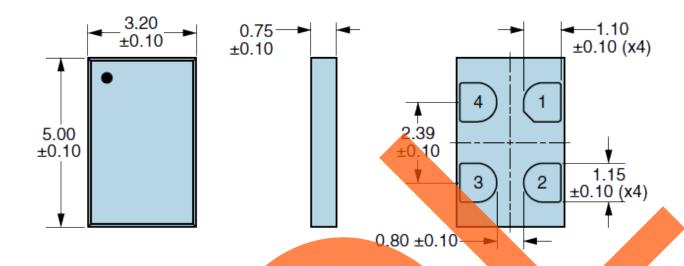
Output Control Input Voltage Logic Low (Vil)	30% of Vdd Maximum to Disable Output
Power Down Output Enable Time	5mSec Maximum (Disabled Output: Logic Low)
Tri-State Output Enable Time	150nSec Maximum (Disabled Output: High Impedance)
Power Down Output Disable Time	150nSec Maximum (Disabled Output: Logic Low)
Tri-State Output Disable Time	150nSec Maximum (Disabled Output: High Impedance)
Standby Current	5μA Maximum (Disabled Output: Logic Low)
Period Jitter (RMS)	2pSec Typical, 5pSec Maximum
RMS Phase Jitter (Fj = 900kHz to 7.5MHz; Random)	0.5pSec Typical, 1pSec Maximum
RMS Phase Jitter (Fj = 12kHz to 20MHz; Random)	1.5pSec Typical, 3pSec Maximum
Start Up Time	5mSec Maximum
Storage Temperature Range	-65°C to +150°C

PART NUMBERING GUIDE

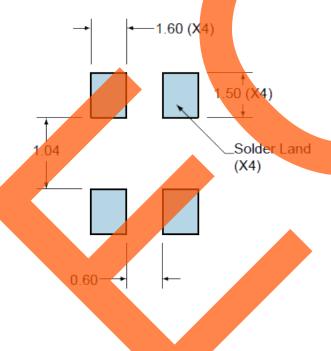




MECHANICAL DIMENSIONS



SUGGESTED SOLDER PAD LAYOUT



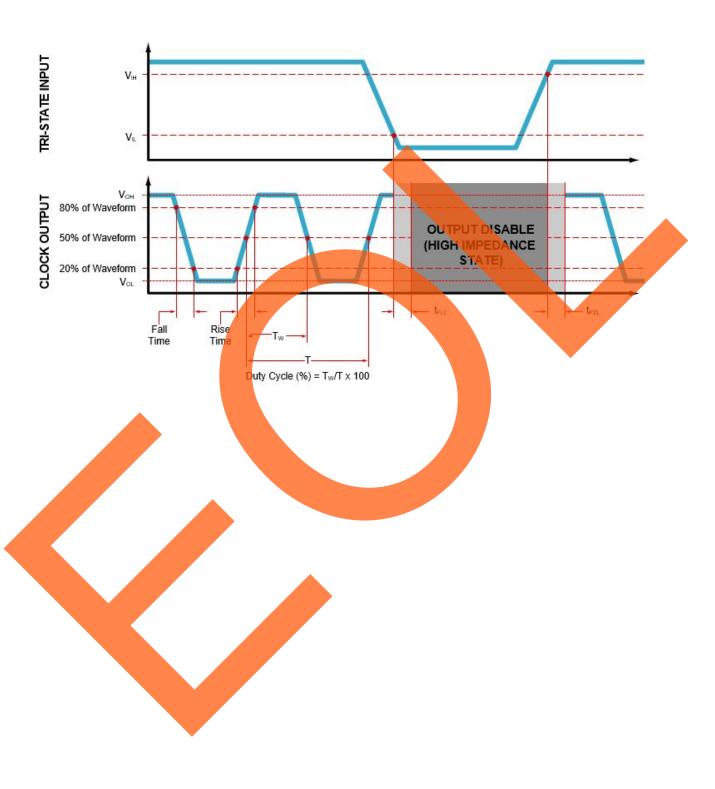
PIN	CONNECTION
1	Power Down Or
	Tri-State
2	Ground
3	Output
4	Supply Voltage

All Tolerances are ±0.1

All Dimensions in Millimeters

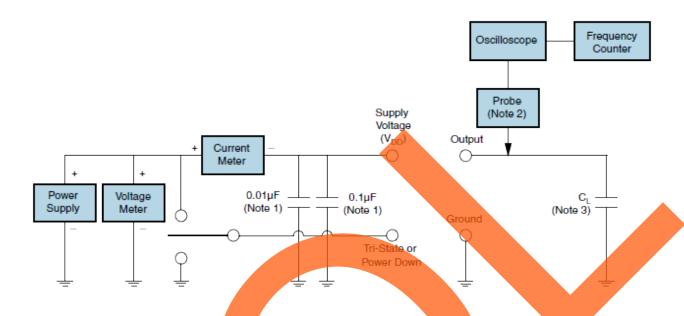


OUTPUT WAVEFORM & TIMING DIAGRAM





TEST CIRCUIT FOR CMOS OUTPUT



- Note 1: An external 0.01µF ceramic bypass capacitor in parallel with a 0.1µF high frequency ceramic bypass capacitor close (less Than 2mm) to the package ground and supply voltage pin is required.

 Note 2: A low capacitance (<12pF), 10X Attentuation Factor, High Impedance (>10Mohms), and High bandwidth (>300MHz)
- Passive probe is recommended.
- Note 3: Capacitance value CL includes sum of all probe and fixture capacitance. See applicable specification sheet for †Load Drive Capability'.



TAPE & REEL DIMENSIONS

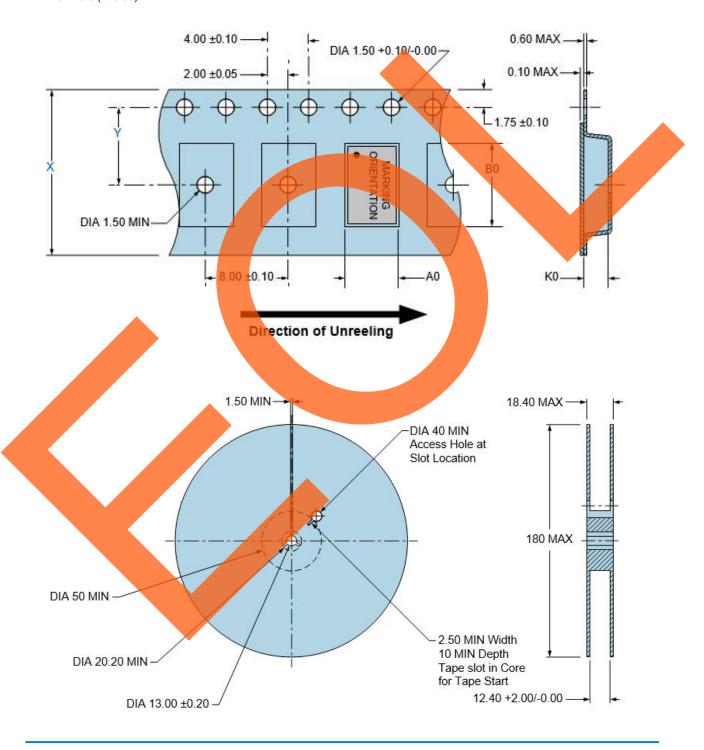
Quantity per Reel: 1000 Units

All Dimensions in Millimeters

Compliant to EIA-481

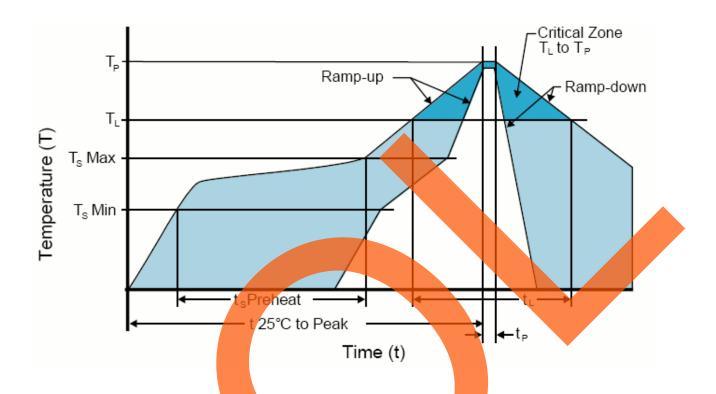
X = 16.00 or 12.00 (+/-0.3)

Y = 7.5 or 5.5 (+/-0.05)





RECOMMENDED SOLDER REFLOW METHOD



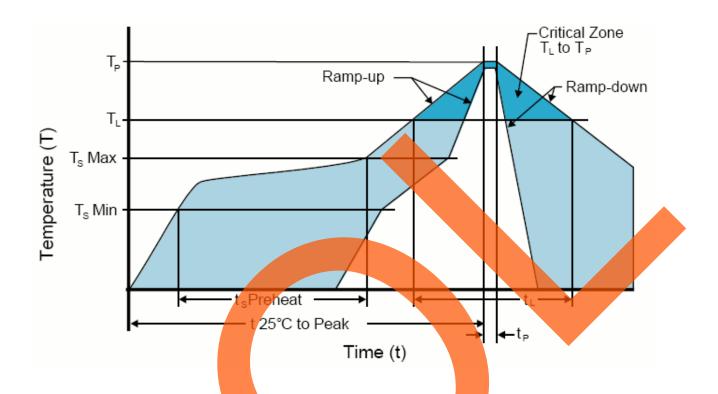
HIGH TEMPERATURE INFRARED/CONVECTION		
T _s MAX to T _L (Ramp-up Rate)	3°C/Second Maximum	
Preheat		
- Temperature Minimum (T _s MIN)	150°C	
- Temperature Typical (T _S TYP)	175°C	
- Temperature Maximum(T _s MAX)	200°C	
- Time (t _s)	60 - 180 Seconds	
Ramp-up Rate (T _L to T _P)	3°C/Second Maximum	
Time Maintained Above:		
- Temperature (T _L)	217°C	
- Time (t∟)	60 - 150 Seconds	
Peak Temperature (T _P)	260°C Maxim <mark>um for 10</mark> Seconds Maximum	
Target Peak Temperature(Tp Target)	250°C +0 <mark>/-5°C</mark>	
Time within 5°C of actual peak (tp)	20 - 40 Seconds	
Ramp-down Rate	6°C/Second Maximum	
Time 25°C to Peak Temperature (t)	8 Minutes Maximum	
Moisture Sensitivity Level	Level 1	
Additional Notes	remperatures shown are applied to body of device.	

High Temperature Manual Soldering

260°C Maximum for 5 Seconds Maximum, 2 times Maximum. (Temperatures shown are applied to body of device.)



RECOMMENDED SOLDER REFLOW METHOD



LOW TEMPERATURE INFRARED/CONVECTION		
T _s MAX to T _L (Ramp-up Rate)	5°C/Second Maximum	
Preheat		
- Temperature Minimum (T _s MIN)	N/A	
- Temperature Typical (T _s TYP)	150°C	
- Temperature Maximum(T _s MAX)	N/A	
- Time (t _s)	60 - 120 Seconds	
Ramp-up Rate (T _L to T _P)	5°C/Second Maximum	
Time Maintained Above:		
- Temperature (T _L)	150°C	
- Time (t∟)	200 Seconds Maximum	
Peak Temperature (T _P)	240°C Maximum	
Target Peak Temperature (Tp Target)	240°C Maximum 2 Times / 230°C Maximum 1 Time	
Time within 5°C of actual peak (tp)	10 Seconds Maximum 2 Times / 80 Seconds Maximum 1 Time	
Ramp-down Rate	5°C/Second Maximum	
Time 25°C to Peak Temperature (t)	N/A	
Moisture Sensitivity Level	Leyel 1	
Additional Notes	Temperatures shown are applied to body of device.	

Low Temperature Manual Soldering

185°C Maximum for 10 Seconds Maximum, 2 times Maximum. (Temperatures shown are applied to body of device.)