



100V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
100V	$9.2 m\Omega$ @ $V_{GS} = 10V$	12A

Features and Benefits

- High Conversion Efficiency
- Low R_{DS(ON)}—Minimizes On-State Losses
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Description and Applications

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

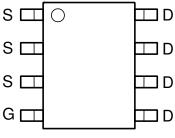
- High Frequency Switching
- Synchronous Rectification
- DC-DC Converters

Mechanical Data

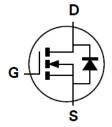
- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish—Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.074 grams (Approximate)











Equivalent Circuit

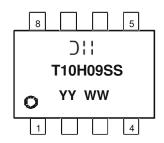
Ordering Information (Note 4)

Part Number	Case	Packaging
DMT10H009SSS-13	SO-8	2500/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



T10H09SS = Product Type Marking Code
YYWW = Date Code Marking
YY or YY = Year (ex: 18 = 2018)
WW = Week (01 to 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage		V_{DSS}	100	V
Gate-Source Voltage		V _{GSS}	±20	V
Continuous Dusin Comment (Note C) V 40V	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	12 10	Α
Continuous Drain Current (Note 6) V _{GS} = 10V	$T_C = +25$ °C $T_C = +70$ °C	I _D	42 34	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	100	Α	
Maximum Continuous Body Diode Forward Current (Note 6)	I _S	1.8	Α	
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle	I _{SM}	100	Α	
Avalanche Current, L = 0.3mH	I _{AS}	27	А	
Avalanche Energy, L = 0.3mH	E _{AS}	109.4	mJ	

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P_{D}	1.4	W
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\Theta JA}$	89	°C/W
Total Power Dissipation (Note 6)	P_{D}	2.1	W
Thermal Resistance, Junction to Ambient (Note 6)	R _{OJA}	61	°C/W
Thermal Resistance, Junction to Case (Note 6)	R _{eJC}	5	°C/W
Operating and Storage Temperature Range	$T_{J_1}T_{STG}$	-55 to +150	°C

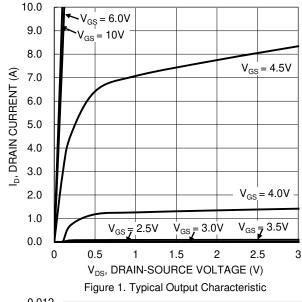
Electrical Characteristics (T_A = +25°C, unless otherwise specified.)

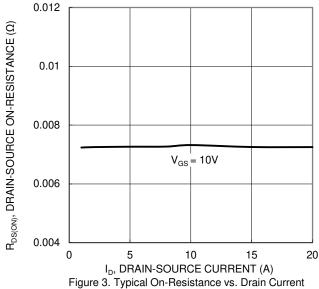
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	100	_	_	V	$V_{GS} = 0V$, $I_D = 1mA$	
Zero Gate Voltage Drain Current	I _{DSS}		_	1	μΑ	$V_{DS} = 80V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}		_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	$V_{GS(TH)}$	2	_	4	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	_	7.5	9.2	mΩ	$V_{GS} = 10V, I_D = 10A$	
Diode Forward Voltage	V _{SD}	_	_	1.2	V	V _{GS} = 0V, I _S = 20A	
DYNAMIC CHARACTERISTICS (Note 8)	•	,					
Input Capacitance	C _{iss}		2085	_	рF	V _{DS} = 50V, V _{GS} = 0V f = 1MHz	
Output Capacitance	Coss		609	_			
Reverse Transfer Capacitance	C _{rss}	_	13	_			
Gate Resistance	R_{g}	_	1.7	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	Q_g	_	29.8	_		$V_{DD} = 50V, I_D = 13A,$ $V_{GS} = 10V$	
Gate-Source Charge	Q _{gs}	_	9.5	_	nC		
Gate-Drain Charge	Q_{gd}	_	7.3	_			
Turn-On Delay Time	t _{D(ON)}	_	9.7	_		$V_{DD} = 50V, V_{GS} = 10V,$ $I_D = 13A, R_g = 6\Omega$	
Turn-On Rise Time	t _R	_	13.7	_	ns		
Turn-Off Delay Time	t _{D(OFF)}	_	25.1	_	115		
Turn-Off Fall Time	tϝ	_	17.4	_			
Reverse Recovery Time	t _{RR}	_	45	_	ns I don divide doon.		
Reverse Recovery Charge	Q _{RR}	_	68	_	$_{\rm nC}$ $_{\rm lf} = 13A$, $_{\rm di/dt} = 100A/\mu s$		

 Device mounted on FR-4 substrate PCB, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PCB, 2oz copper, with 1inch square copper plate.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing. Notes:









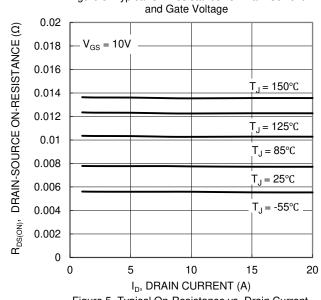


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

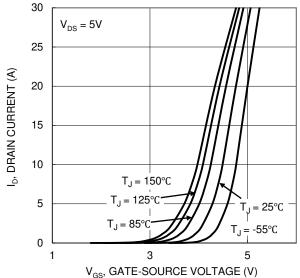
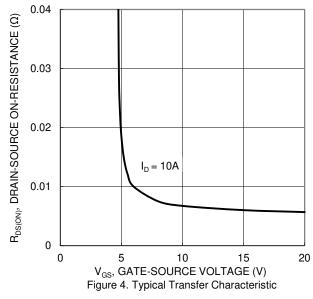


Figure 2. Typical Transfer Characteristic



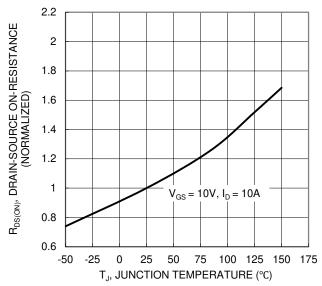


Figure 6. On-Resistance Variation with Junction Temperature





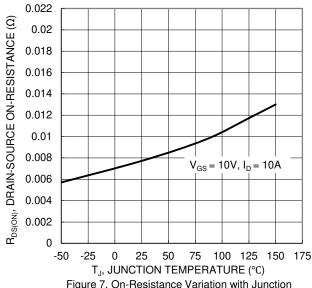


Figure 7. On-Resistance Variation with Junction Temperature

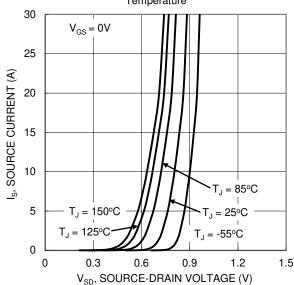
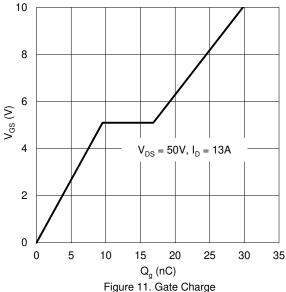
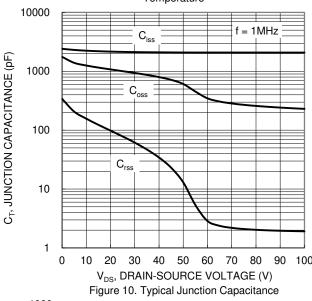


Figure 9. Diode Forward Voltage vs. Current



4 $V_{\text{GS}(\text{TH})},$ GATE THRESHOLD VOLTAGE (V) 3.5 $I_D = 1mA$ 3 2.5 2 $I_{D} = 250 \mu A$ 1.5 1 0.5 0 -50 -25 25 50 75 100 125 150 175 T_J , JUNCTION TEMPERATURE (°C)

Figure 8. Gate Threshold Variation vs. Junction Temperature



1000 R_{DS(ON)} 100ms 100 = 10ms ID, DRAIN CURRENT (A) 100µs 10 $T_{J(Max)} = 150$ T_C = 25℃ Single Pulse DUT on $P_W = 10s$ 1*MRP Board $V_{GS} = 10V$ 0.01 0.1 10 1000 V_{DS}, DRAIN-SOURCE VOLTAGE (V) Figure 12. SOA, Safe Operation Area



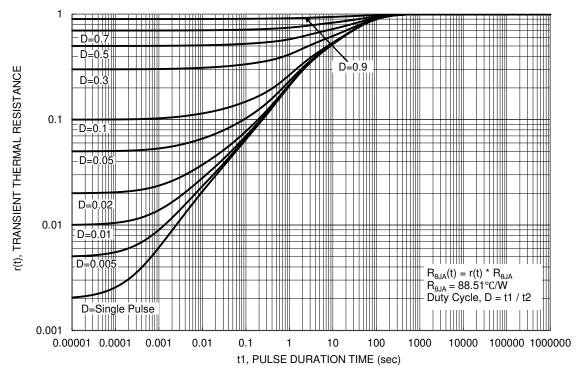
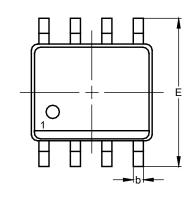


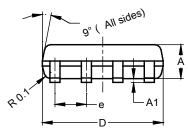
Figure 13. Transient Thermal Resistance

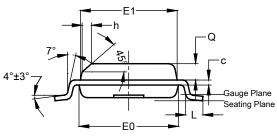


Package Outline Dimensions

 $Please see \ http://www.diodes.com/package-outlines.html \ for the \ latest \ version.$





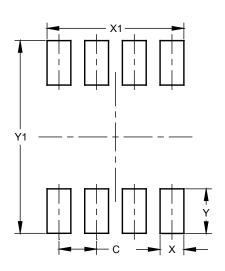


SO-8

SO-8					
Dim	Min	Max	Тур		
Α	1.40	1.50	1.45		
A1	0.10	0.20	0.15		
b	0.30	0.50	0.40		
С	0.15	0.25	0.20		
D	4.85	4.95	4.90		
Е	5.90	6.10	6.00		
E1	3.80	3.90	3.85		
E0	3.85	3.95	3.90		
е	_		1.27		
h	_		0.35		
L	0.62	0.82	0.72		
Ø	0.60	0.70	0.65		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



SO-8

Dimensions	Value (in mm)
С	1.27
Х	0.802
X1	4.612
Υ	1.505
V1	6.50



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