# Nch 600V 18A Power MOSFET

$V_{DSS}$	600V
R <sub>DS(on)</sub> (Max.)	0.286Ω
I <sub>D</sub>	±18A
P <sub>D</sub>	220W

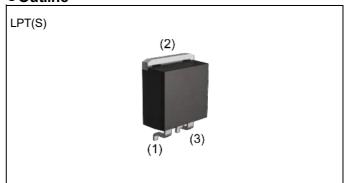
# ●Features

- 1) Fast reverse recovery time (trr)
- 2) Low on-resistance
- 3) Fast switching speed
- 4) Drive circuits can be simple
- 5) Pb-free plating; RoHS compliant

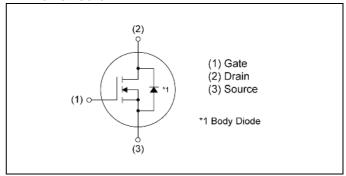
# Application

Switching applications

# Outline



# •Inner circuit



Packaging specifications

<u>_                               </u>	
Packing	Embossed Tape
Packing code	TL
Marking	R6018JNJ
Quantity (pcs)	1000

# ● **Absolute maximum ratings** (T<sub>a</sub> = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	$V_{DSS}$	600	V
Continuous drain current (T <sub>c</sub> = 25°C)	I <sub>D</sub> *1	±18	А
Pulsed drain current	I <sub>DP</sub> *2	±54	А
Gate - Source voltage	V <sub>GSS</sub>	±30	V
Avalanche current, single pulse	I <sub>AS</sub> *3	4.4	Α
Avalanche energy, single pulse	E <sub>AS</sub> *3	526	mJ
Power dissipation (T <sub>c</sub> = 25°C)	P <sub>D</sub>	220	W
Junction temperature	T <sub>j</sub>	150	°C
Operating junction and storage temperature range	T <sub>stg</sub>	-55 to +150	°C

### ●Thermal resistance

Downwortow	Cymah al	Values			1.1:4
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R <sub>thJC</sub>	-	-	0.57	°C/W
Thermal resistance, junction - ambient	R <sub>thJA</sub>	-	-	80	°C/W
Soldering temperature, wavesoldering for 10s	T <sub>sold</sub>	-	-	265	°C

# ●Electrical characteristics (T<sub>a</sub> = 25°C)

Darramatar	Cymah al	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 1mA	600	-	-	V
Zero gate voltage drain current	I <sub>DSS</sub>	$V_{DS} = 600V, V_{GS} = 0V$ $T_j = 25^{\circ}C$	-	-	100	μA
Gate - Source leakage current	I <sub>GSS</sub>	$V_{GS} = \pm 30V$ , $V_{DS} = 0V$	1	-	±100	nA
Gate threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 4.2 \text{mA}$	5.0	6.0	7.0	V
Static drain - source on - state resistance	R <sub>DS(on)</sub> *5	$V_{GS} = 15V, I_D = 9.0A$ $T_j = 25^{\circ}C$	-	0.220	0.286	Ω
Gate resistance	$R_{G}$	f = 1MHz, open drain	-	1.7	-	Ω

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Downwater	Cymah ol	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Uniil
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	1300	-	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 100V	-	70	1	
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	1.8	1	_
Effective output capacitance energy related	C <sub>o(er)</sub> *6	$C_{o(er)}^{*6}$ $V_{GS} = 0V$		63	-	pF
Effective output capacitance time related	C <sub>o(tr)</sub> *7	V <sub>DS</sub> = 0V to 480V	-	235	1	
Turn - on delay time	t <sub>d(on)</sub> *5	$V_{DD} \simeq 300V$ , $V_{GS} = 15V$	-	26	1	
Rise time	<b>t</b> <sub>r</sub> *5	I <sub>D</sub> = 9.0A	1	20	1	no
Turn - off delay time	t <sub>d(off)</sub> *5	$R_L \simeq 33.2\Omega$		50	1	ns
Fall time	<b>t</b> <sub>f</sub> *5	$R_G = 10\Omega$	-	12	-	

# ● Gate charge characteristics (T<sub>a</sub> = 25°C)

Darameter	Cymahal	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Total gate charge	$Q_g^{*5}$	V <sub>DD</sub> ≈ 300V	-	42	-	
Gate - Source charge	Q <sub>gs</sub> *5	I <sub>D</sub> = 18A	-	12	-	nC
Gate - Drain charge	Q <sub>gd</sub> *5	V <sub>GS</sub> = 15V	-	15	-	
Gate plateau voltage	V <sub>(plateau)</sub>	V <sub>DD</sub> ≈ 300V, I <sub>D</sub> = 18A	-	9.3	-	V

<sup>\*1</sup> Limited only by maximum temperature allowed.

<sup>\*2</sup> Pw  $\leq$  10µs, Duty cycle  $\leq$  1%

<sup>\*3</sup> L  $\simeq$  50mH,  $V_{DD}$  = 50V,  $R_G$  = 25 $\Omega$ , starting  $T_i$  = 25°C

<sup>\*4</sup> Tc=25°C

<sup>\*5</sup> Pulsed

<sup>\*6</sup> Co(er) is a fixed capacitance that gives the same stored energy as Coss while  $V_{DS}$  is rising from 0 to 80%  $V_{DSS}$ .

<sup>\*7</sup> Co(tr) is a fixed capacitance that gives the same charging time as Coss while  $V_{DS}$  is rising from 0 to 80%  $V_{DSS}$ .

# ● Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Parameter	Symbol	Conditions	Values			Unit	
- Farameter	Symbol	Conditions	Min.	Тур.	Max.	Offic	
Source current $I_S^{*1}$		T <sub>C</sub> = 25°C	-	-	18	Α	
Pulsed source current	I <sub>SP</sub> *2	1C - 23 C	-	-	54	Α	
Source-Drain voltage	V <sub>SD</sub> *5	$V_{GS} = 0V, I_{S} = 18A$	-	-	1.7	V	
Reverse recovery time	<b>t</b> <sub>rr</sub> *5		-	80	-	ns	
Reverse recovery charge	Q <sub>rr</sub> *5	$I_S = 18A$ di/dt = 100A/µs	-	280	-	nC	
Peak reverse recovery current	<sub>rr</sub> *5		-	7.5	-	Α	

Fig.1 Power Dissipation Derating Curve

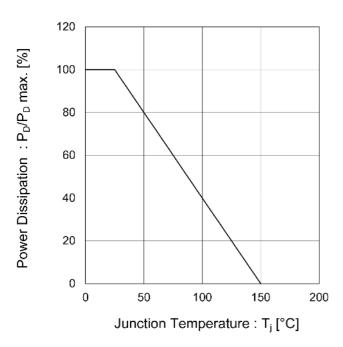


Fig.2 Drain Current Derating
Curve vs. Junction Temperature

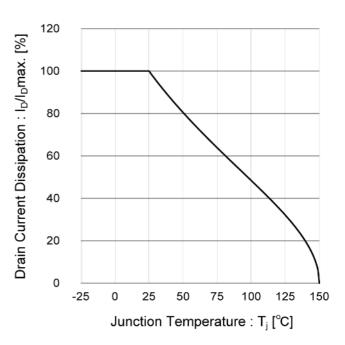


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

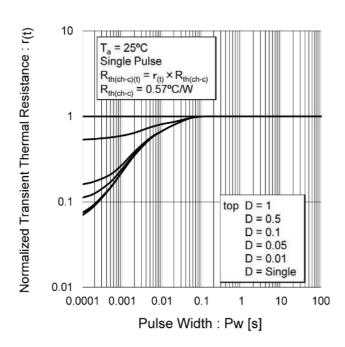


Fig.4 Maximum Safe Operating Area

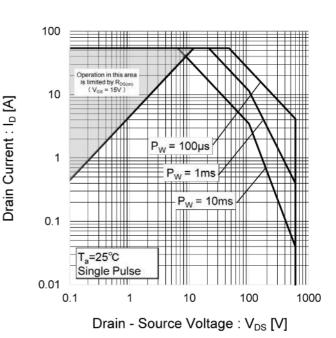


Fig.5 Avalanche Energy Derating
Curve vs. Junction Temperature

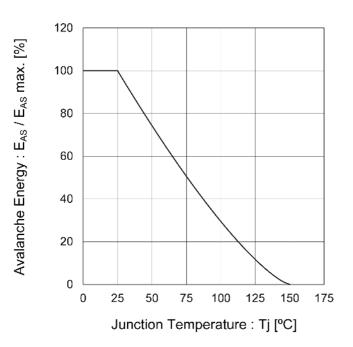


Fig.6 Normalized Breakdown Voltage vs. Junction Temperature

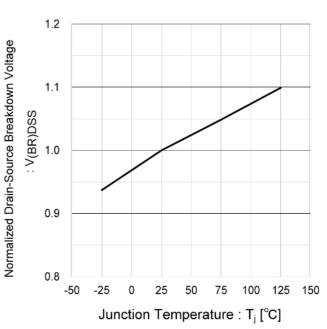


Fig.7 Typical Output Characteristics(I)

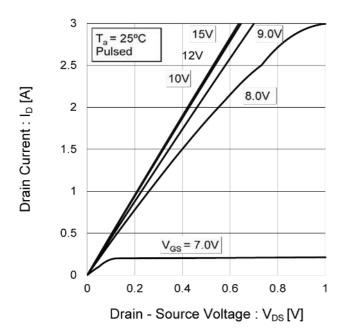
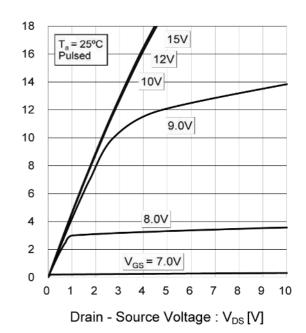


Fig.8 Typical Output Characteristics(II)



Drain Current: Ip [A]

Fig.9 Typical Transfer Characteristics

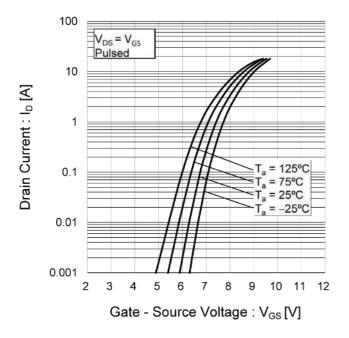


Fig.10 Normalized Gate Threshold .

Voltage vs Junction Temperature

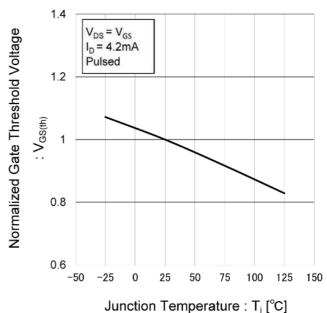


Fig.11 Static Drain - Source On - State Resistance vs. Gate Source Voltage

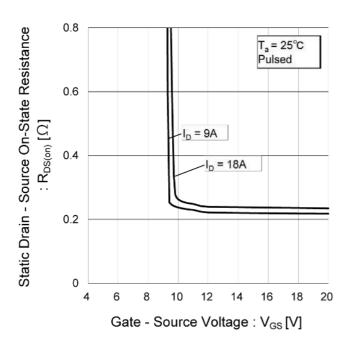


Fig.12 Normalized Static Drain - Source On - State Resistance vs. Junction Temperature

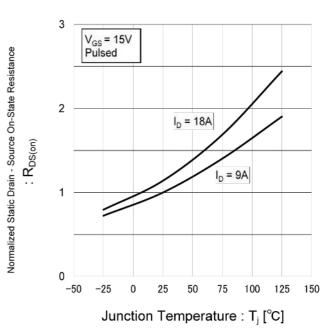


Fig.13 Static Drain - Source On - State Resistance vs. Drain Current(I)

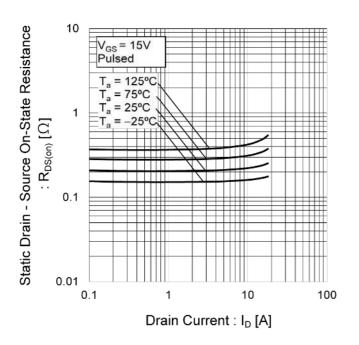


Fig.14 Typical Capacitance vs.
Drain - Source Voltage

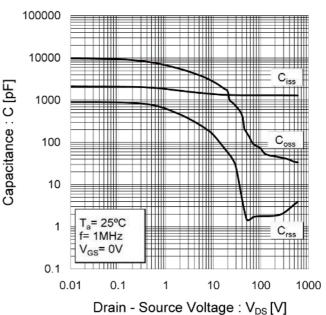


Fig.15 Typical Coss Stored Energy

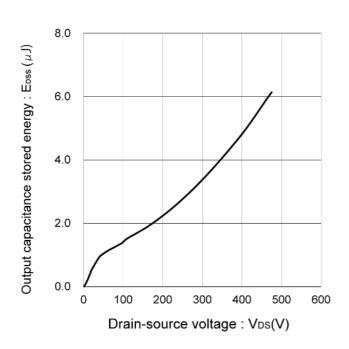
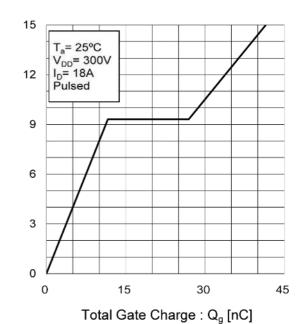


Fig.16 Dynamic Input Characteristics



Sate - Source Voltage : V<sub>GS</sub> [V]

Fig.17 Inverse Diode Forward Current vs. Source - Drain Voltage

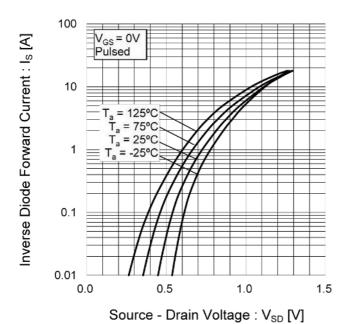
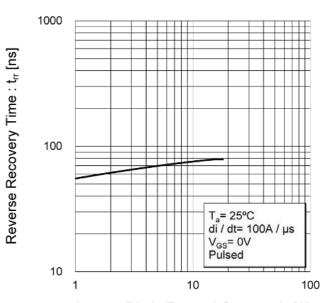


Fig.18 Reverse Recovery Time vs.
Inverse Diode Forward Current



Inverse Diode Forward Current : I<sub>S</sub> [A]

### Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

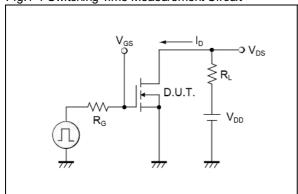


Fig.2-1 Gate Charge Measurement Circuit

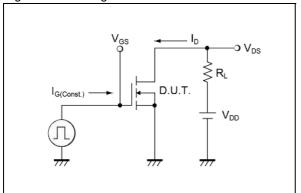


Fig.3-1 Avalanche Measurement Circuit

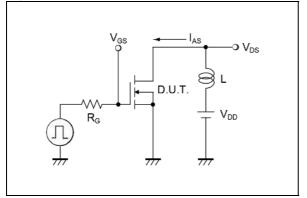


Fig.4-1 Diode Recovery Measurement Circuit

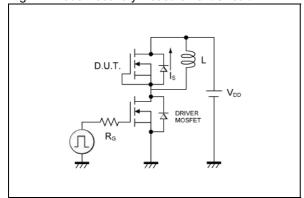


Fig.1-2 Switching Waveforms

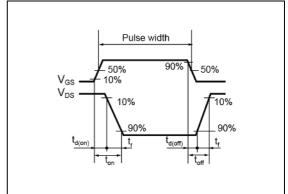


Fig.2-2 Gate Charge Waveform

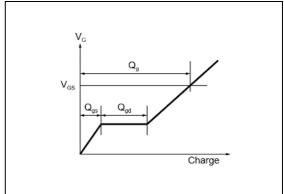


Fig.3-2 Avalanche Waveform

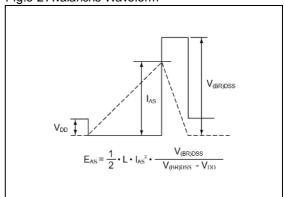
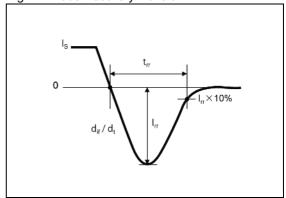
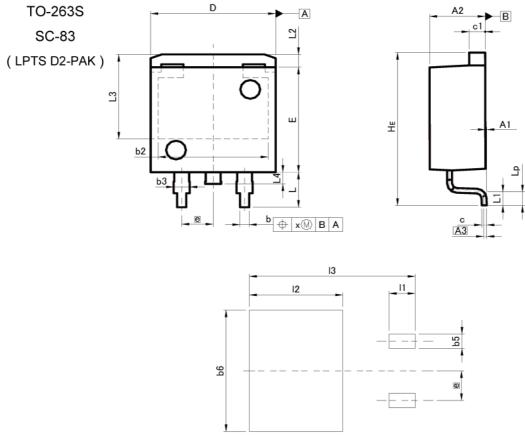


Fig.4-2 Diode Recovery Waveform



# Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM MILIMET		ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
A1	0.00	0.30	0.000	0.012
A2	4.30	4.70	0.169	0.185
A3	0.:	25	0.0	10
b	0.68	0.98	0.027	0.039
b2	8.8	90	0.3	50
b3	1.14	1.44	0.045	0.057
С	0.30	0.60	0.012	0.024
c1	1.10	1.50	0.043	0.059
D	9.80	10.40	0.386	0.409
E	8.80	9.20	0.346	0.362
е	2.	54	0.100	
HE	12.80	13.40	0.504	0.528
L	2.70	3.30	0.106	0.130
L1	1.	20	0.0	47
L2	1.	10	0.043	
L3	7.:	25	0.2	85
L4	1.0	00	0.0	39
Lp	0.90	1.50	0.035	0.059
Х		0.25	47	0.010
DIM L	MILIM	TERS	INC	HES

 DIM
 MILIMETERS
 INCHES

 MIN
 MAX
 MIN
 MAX

 b5
 1,23
 0,049

 b6
 10,40
 0,409

 11
 2,10
 0,083

 12
 7,55
 0,297

 13
 13,40
 0,528

Dimension in mm/inches



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CLASSⅢ	CL ACCTI	CLASS II b	СГУССШ
CLASSIV	CLASSII	CLASSⅢ	CLASSⅢ

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  - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
  may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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