



DMN15M5UCA6

N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BVsss	RSS(ON) Typ	Is _{Мах} Та = +25°С
12V	4.3mΩ @ V _{GS} = 3.8V	16.5A

Description

This new generation MOSFET is designed to minimize the on-state resistance (Rss(ON)) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

- Battery Management
- Load Switch
- Battery Protection

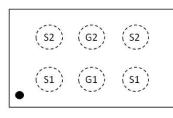
Features

- CSP with Footprint 2.14mm × 1.67mm
- Height = 0.11mm for Low Profile
- ESD Protection of Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. <u>https://www.diodes.com/quality/product-definitions/</u>

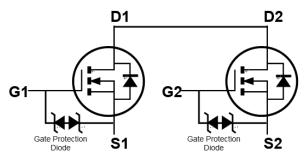
Mechanical Data

- Case: X4-DSN2117-6
- Terminal Connections: See Diagram Below
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiAu or NiPdAu. Solderable per MIL-STD-202, Method 208 @
- Weight: 0.0012 grams (Approximate)





X4-DSN2117-6



Top View



Ordering Information (Note 4)

Case	Packaging
X4-DSN2117-6	3000/Tape & Reel
	X4-DSN2117-6

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and

Lead-free.

Notes:

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

	X4-DSN2117-6
~ -	OF
$() \vdash$	YN
•	

ΥM

OF = Product Type Marking Code YM = Date Code Marking Y or \overline{Y} = Year (ex: H = 2020)

M or \overline{M} = Month (ex: 9 = September)

Date Code Key

Date Code Key												
Year	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	G	Н		J	К	L	М	N	0	Р	R	S
•••	-			°,		-			•	•		0
						-		-		·		
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Source-Source Voltage	Vsss	12	V		
Gate-Source Voltage			V _{GSS}	±10.5	V
Continuous Source Current (Note 5) $V_{GS} = 4.5V$	Steady State	T _A = +25°C T _A = +70°C	ls	16.5 13.0	А
Continuous Source Current (Note 5) V _{GS} = 2.5V	ls	11.5 9.5	А		
Pulsed Source Current (Note 6)		•	I _{SM}	90	А

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 7)	PD	1.2	W
Thermal Resistance, Junction to Ambient @TA = +25°C (Note 7)	Reja	105	°C/W
Power Dissipation (Note 5)	PD	2.0	W
Thermal Resistance, Junction to Ambient $@T_A = +25^{\circ}C$ (Note 5)	R _{0JA}	61	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)	• • • • • • • • • • • • • • • • • • • •		- 76		•		
Source-Source Breakdown Voltage	BVsss	12			V	$V_{GS} = 0V$, $I_{S} = 1mA$	
Zero Gate Voltage Drain Current TJ = +25°C	lsss	_	_	1	μA	Vss = 9.6V Vgs = 0V	
Gate-Source Leakage	lgss	—	—	±10	μA	$V_{GS} = \pm 8V, V_{SS} = 0V$	
Gale-Source Leakage	lgss			±1	μA	$V_{GS} = \pm 5V, V_{SS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	VGS(TH)	0.5	_	1.3	V	Vss = 10V, Is = 0.84mA	
		3.0	4.0	5.1		V _{GS} = 4.5V, I _S = 4A	
Static Source-Source On-Resistance	Deserve	3.2	4.3	5.5	mΩ	V _{GS} = 3.8V, I _S = 4A	
Static Source-Source On-Resistance	Rss(ON)	3.5	4.8	6.8	11122	V _{GS} = 3.1V, I _S = 4A	
		3.8	5.9	10.0		$V_{GS} = 2.5V, I_S = 4A$	
Diode Forward Voltage	Vss	—	—	1.2	V	$V_{GS} = 0V$, $I_S = 6A$	
DYNAMIC CHARACTERISTICS (Note 9)						·	
Input Capacitance	Ciss	—	59				
Output Capacitance	Coss		417	_	pF	$V_{SS} = 10V, V_{GS} = 0V,$ f = 1.0MHz	
Reverse Transfer Capacitance	Crss		12			T = 1.0WHZ	
Total Gate Charge	Qg		36.6	_			
Gate-Source Charge	Q _{gs}	_	7.3	_	nC	$V_{SS} = 10V, V_{GS} = 4V,$	
Gate-Drain Charge	Q _{gd}	—	13.4	—	no	$I_{S} = 4A$	
Gate Charge at VTH	Qg(TH)	_	4.2	_			
Turn-On Delay Time	t _{D(ON)}		430				
Turn-On Rise Time	tR	—	956	—	20	$V_{SS} = 6V, V_{GS} = 4V,$	
Turn-Off Delay Time	tD(OFF)		3890		ns	Is = 4A	
Turn-Off Fall Time	tF	_	1910				

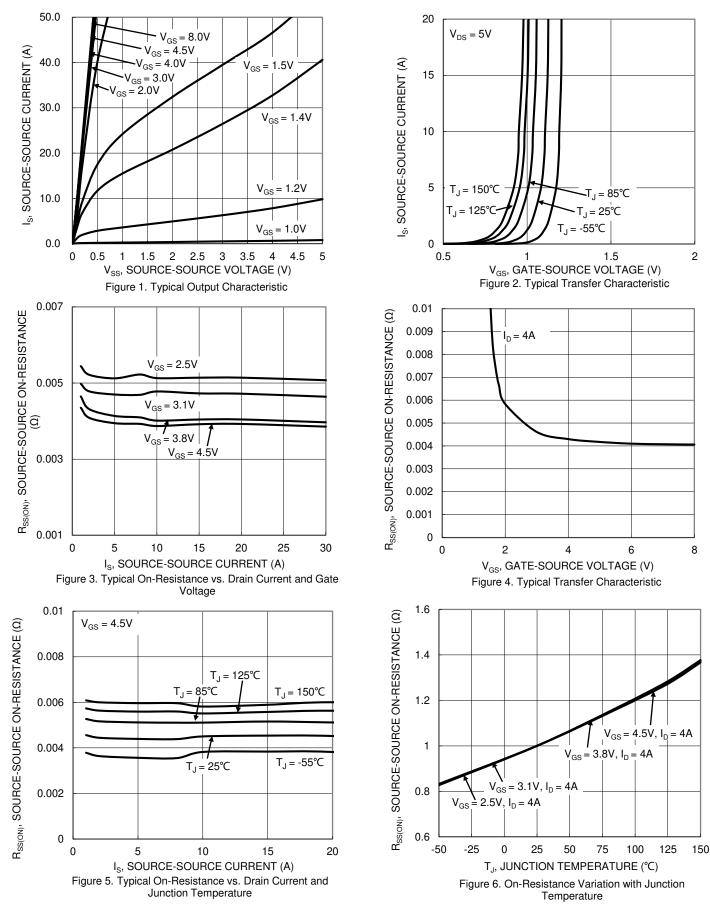
Notes:

Device mounted on FR-4 material with 1inch² (6.45cm²), 2oz. (0.071mm thick) Cu.
Repetitive rating, pulse width limited by junction temperature.
Device mounted on FR-4 PCB with minimum recommended pad layout, single sided.

Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to production testing.

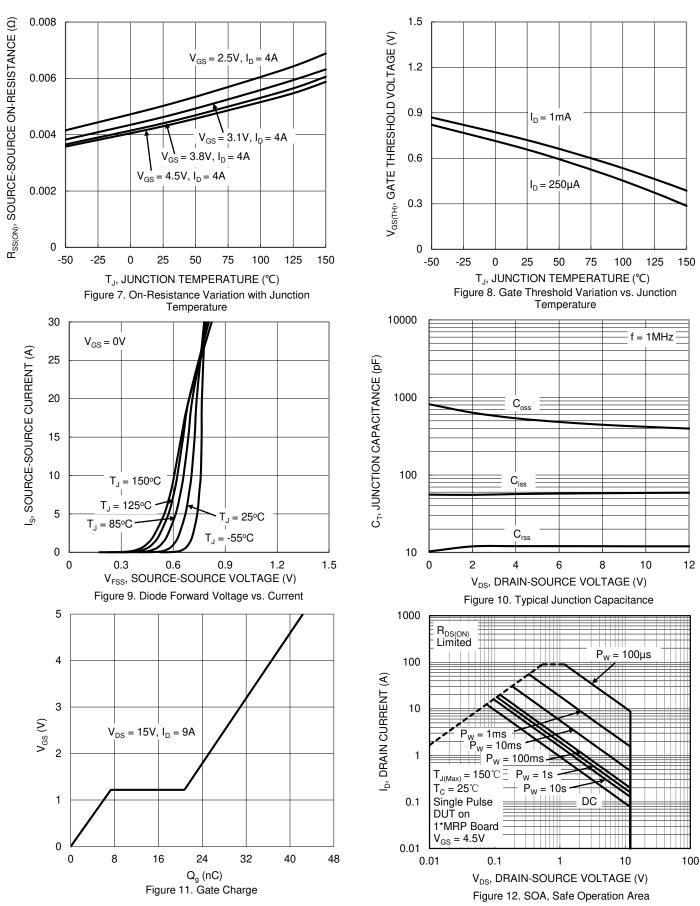


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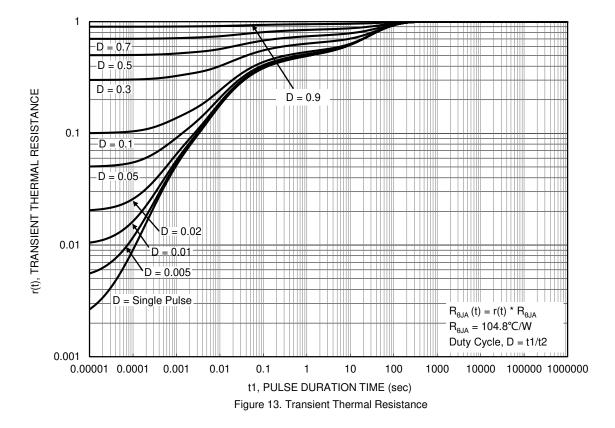




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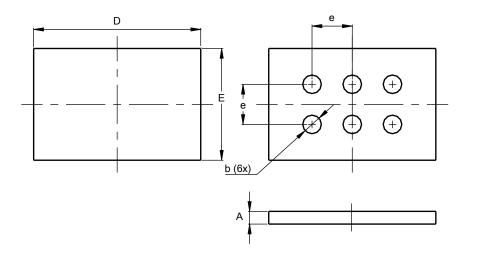




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

X4-DSN2117-6

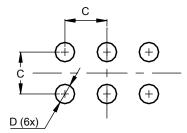


X4-DSN2117-6						
Dim	Min	Max	Тур			
Α	0.09	0.16	0.11			
b	0.27	0.33	0.30			
D	2.10	2.18	2.14			
Е	1.63	1.71	1.67			
е	-	-	0.65			
All	All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.





Dimensions	Value (in mm)
С	0.65
D	0.30



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