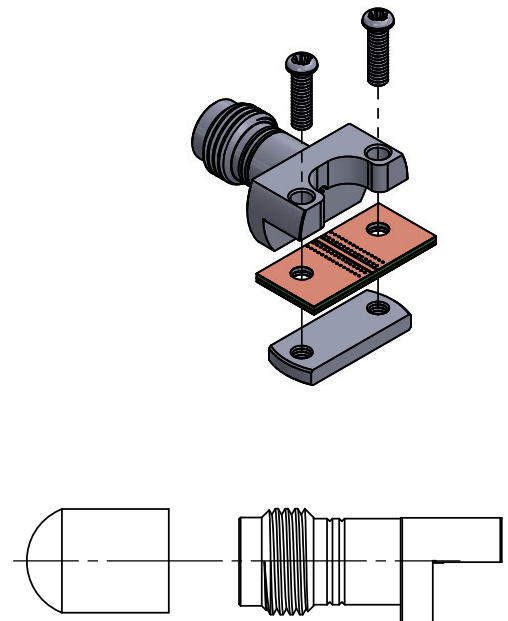
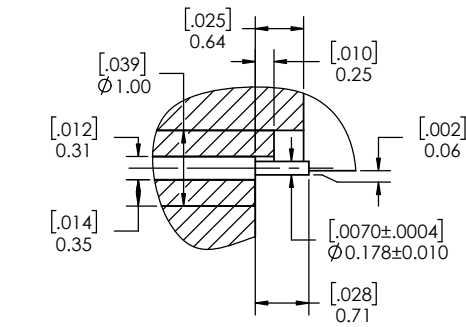
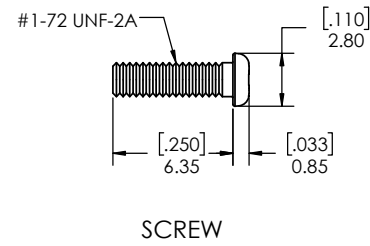
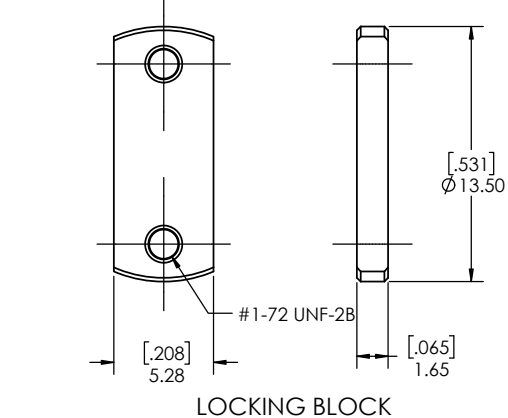
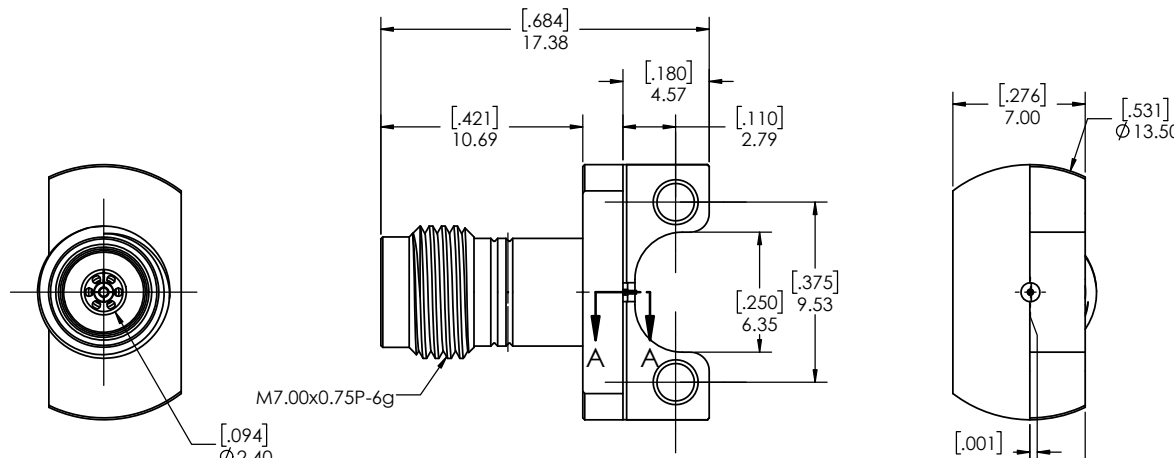




REVISIONS			
REV.	DESCRIPTION	DATE	APPROVED
-	INITIAL RELEASE	07/03/2018	MM
1	UPDATED PART NUMBER	10/24/2018	DL
2	ADDED SHEET 2 PCB DEFINITION	2/20/2019	PV
3	ADDED SECTION VIEW	3/7/2019	PV
4	ADDED SHEET 3	3/19/2019	PV
5	REVISED MODEL	11/19/2019	PV



NOTE(S):
 1. These characteristics are typical and for reference.
 2. DYH: 60-20009-44070
 3. See sheets 2 and 3 for PCB definition.

SECTION A-A
SCALE 20 : 1

PROTECTION CAP

MATERIAL(S) :	ELECTRICAL(S) :	MECHANICAL(S) :	ENVIRONMENTAL(S) :
Body: Stainless Steel Center Conductor: Beryllium Copper Insulator: Insulator 1: PCTFE, white Insulator 2: PTFE, white RoHS Compliant Protective Cap: Soft PVC Color: Blue	Impedance: 50 Ohms Nominal Frequency Range: DC to 50 GHz VSWR: 1.3:1 max at 50 GHz Working Voltage: 400 Vrms max @ Sea Level Dielectric Withstand Voltage: 500 Vrms max. Insulation Resistance: 5000 Megohms min. Contact Resistance: Initial: Center Contact: 1.5 Milliohms max Outer Contact: 0.8 Milliohms max	Mating Characteristics: Interface per MIL-STD-348 Force to Engage & Disengage: Torque: 2 inch-pounds max Longitudinal Force: NA Connector Durability: 500 Cycles min. Permeability: Less than 2.0 mu. Center Contact Retention: Axial Force: 6 pounds min. Radial Force: NA	Temperature Range: -65°C to +165°C Moisture Resistance: MIL-STD-202, Method 103, Test Condition B Corrosion: MIL-STD-202, Method 101, Test Condition B Vibration: MIL-STD-202, Method 204, Test Condition A Shock: MIL-STD-202, Method 213, Test Condition 1

FINISH(ES) :
Body: Passivated Center Conductor: Gold Plating

APPLICABLE CARLISLE IT DOCUMENTS			TOLERANCES AND NOTES		APPROVAL	INITIALS	DATE
WORK STANDARD	PROD INSTRUC	ASSY INSTRUC	EXCEPT AS NOTED		DRAWN BY	MM	07.03.18
NA	NA	NA	THIRD ANGLE PROJECTION		CHECKED BY	KM	07.03.18
NOTICE THIS DRAWING EMBODIES A CONFIDENTIAL PROPRIETARY DESIGN ORIGINATED BY CARLISLE INTERCONNECT TECHNOLOGIES & ALL DESIGN, MANUFACTURING, REPRODUCTION, USE & SALE RIGHTS REGARDING THE SAME ARE EXPRESSLY RESERVED. IT IS SUBMITTED UNDER A CONFIDENTIAL RELATIONSHIP FOR A SPECIFIED PURPOSE & THE RECIPIENT AGREES BY ACCEPTING THIS DRAWING NOT SUPPLY OR DISCLOSE ANY INFORMATION REGARDING IT TO ANY UNAUTHORIZED PERSON TO INCORPORATE IN OTHER PROJECTS ANY SPECIAL FEATURES PECULIAR TO THIS DESIGN. ALL PATENT RIGHTS HERETO ARE EXPRESSLY RESERVED BY CARLISLE INTERCONNECT TECHNOLOGIES, GERRITON, CALIFORNIA 90703			SCALE 5:1		DESIGN ENG		
			DIMENSIONS ARE IN [INCHES] MM		APPR BY		
			ANGLES ±2°				
			.XX DECIMALS ±.063				
			.XXX DECIMALS ±.01				

Dongguan City, Guangdong P.R. China 523533

TITLE: 2.40mm STRAIGHT JACK, EDGE MOUNT (SOLDERLESS)

SCALE: 5:1

SUB-DIRECTORY/OUTLINE/ SHEET 1 OF 3

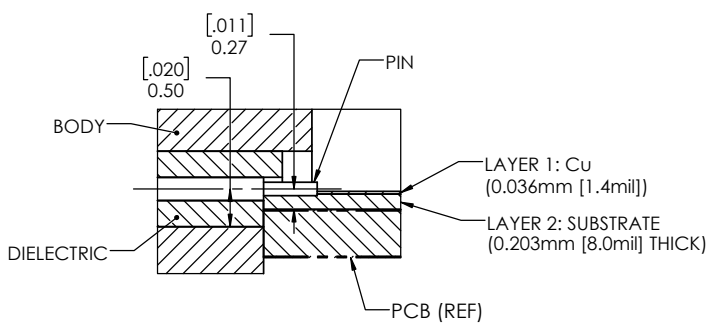
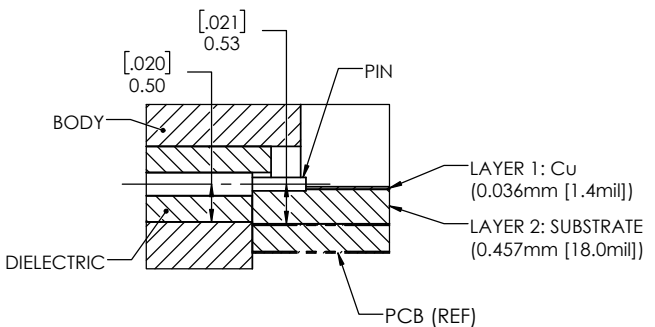
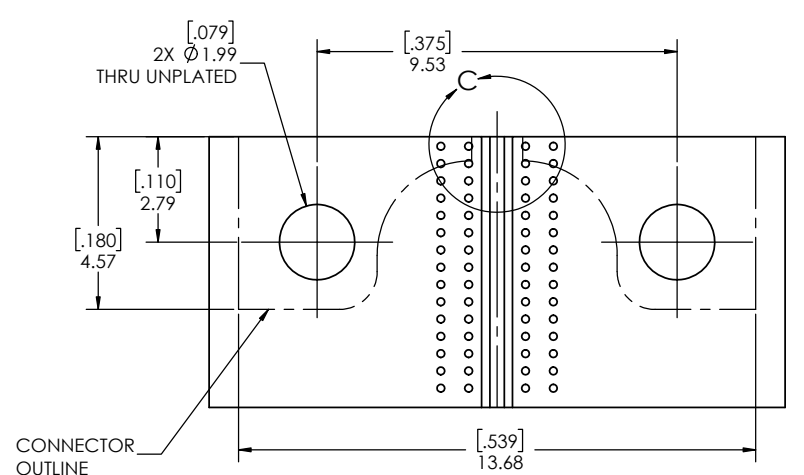
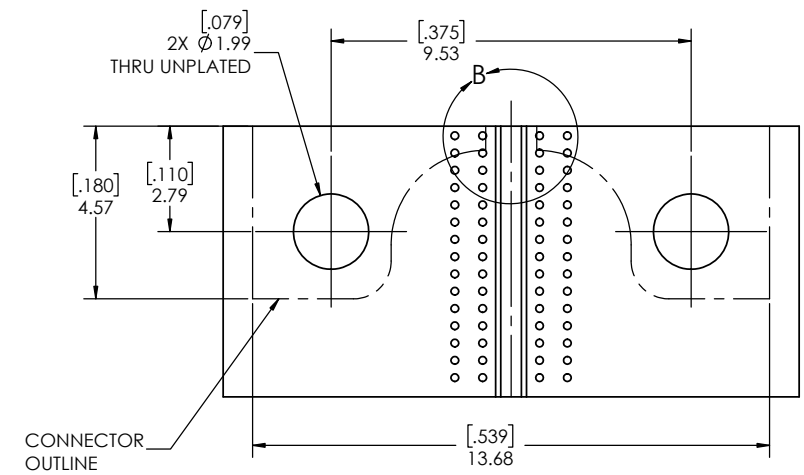
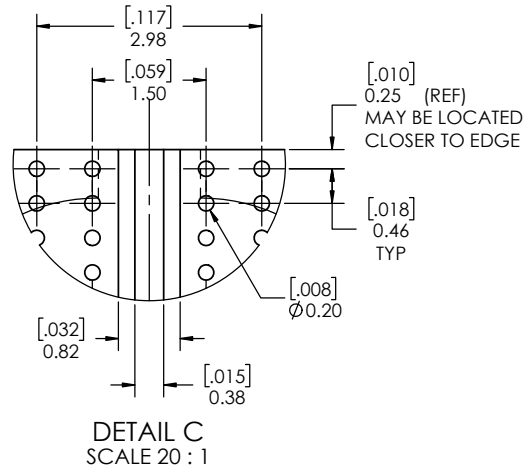
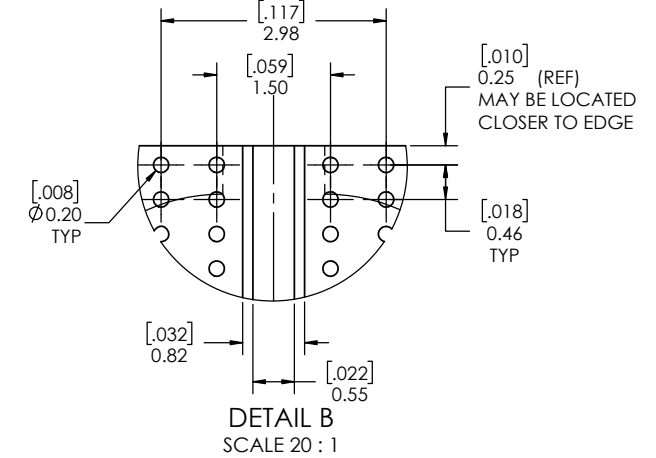
SIZE: C DRAWING NO. TMB-E4F2-1L1 REV. 5

4

3

2

1



PCB LAYOUT OPTION 1
0.457mm [18.0mil] THICK SUBSTRATE (LAYER 2)

PCB LAYOUT
Standard board stackup
(Reference)

PCB LAYOUT OPTION 2
0.203mm [8.0mil] THICK SUBSTRATE (LAYER 2)

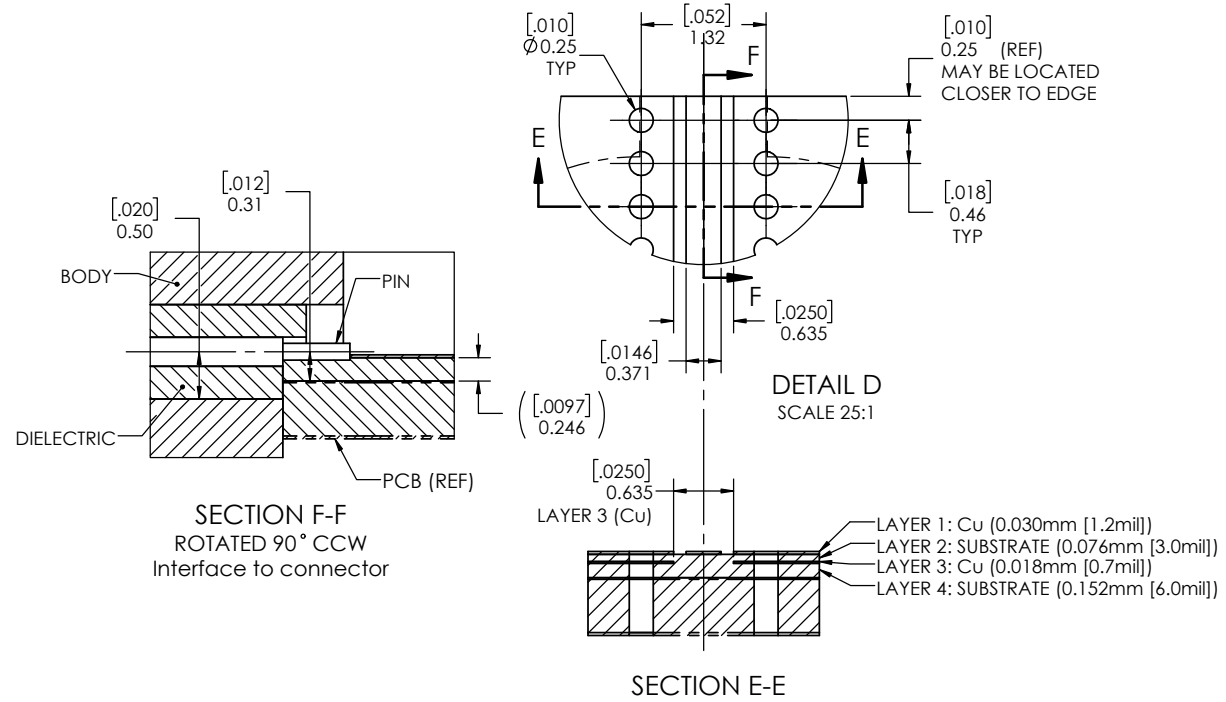
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SIZE C	DRAWING NO. TMB-E4F2-1L1	REV. 5

4

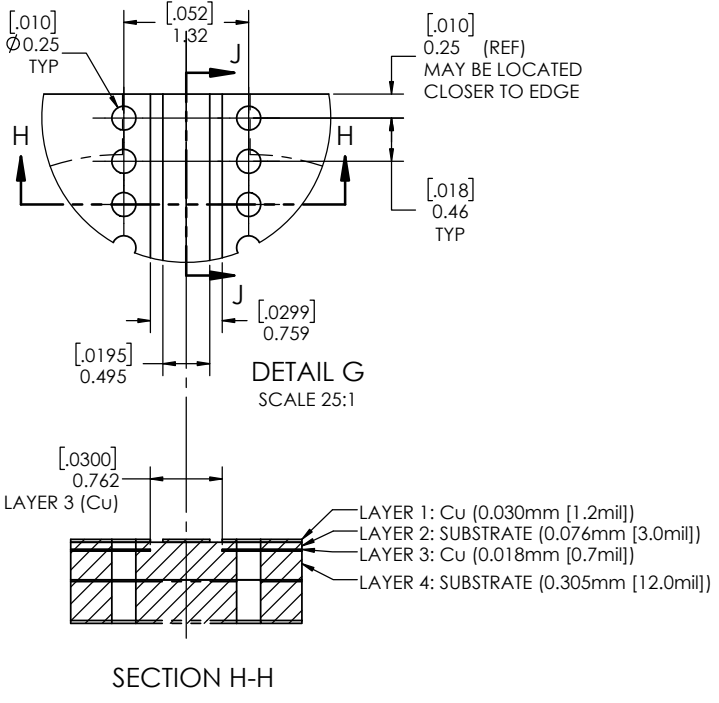
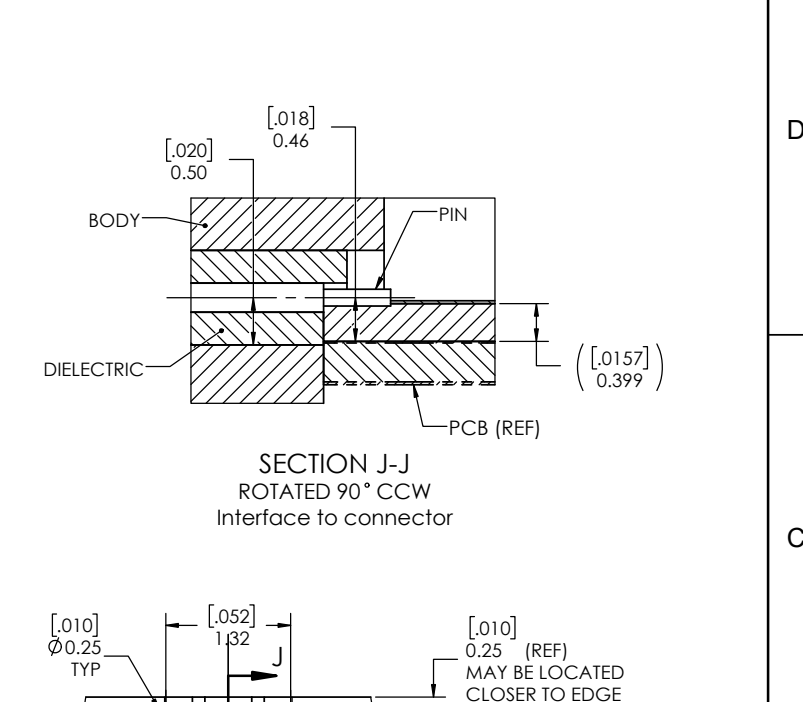
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2

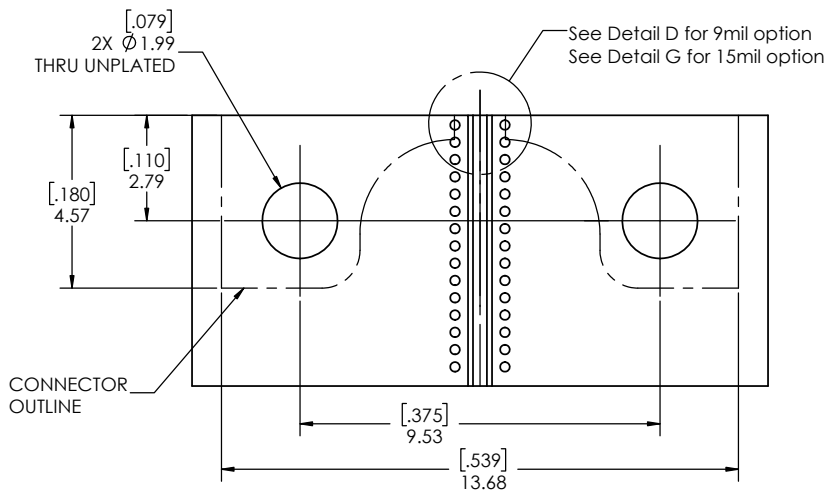
1



PCB 9mil Option
 0.246mm [9.7mil] Dielectric
 SUBSTRATE LAYERS 2 AND 4



PCB 15mil Option
 0.399mm [15.7mil] Dielectric
 SUBSTRATE LAYERS 2 AND 4



PCB LAYOUT
Non-standard board stackup
(Reference)

SCALE	SUB-DIRECTORY/	SHEET 3 OF 3
10:1		
SIZE	CAGE CODE	DRAWING NO.
C		TMB-E4F2-1L1
		REV. 5