



DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Features

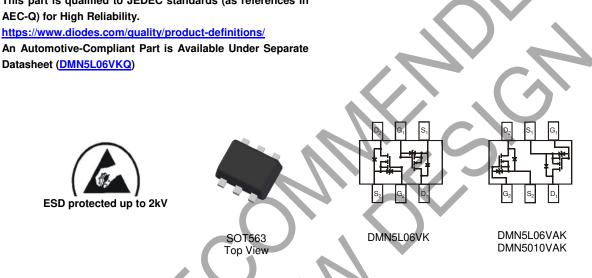
- **Dual N-Channel MOSFET**
- Low On-Resistance
- Very Low Gate Threshold Voltage, 1.0V Max
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- ESD Protected up to 2kV
- This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

https://www.diodes.com/guality/product-definitions/

Datasheet (DMN5L06VKQ)

Mechanical Data

- Package: SOT563
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 C3
- Weight: 0.006 grams (Approximate)



Ordering Information (Note 4)

Davit Musek av	Pastraus	Packing			
Part Number	Package	Qty.	Carrier		
DMN5L06VK-7	SOT563	3,000	Tape & Reel		
DMN5L06VK-7A	SOT563	3,000	Tape & Reel		
DMN5L06VK-13	SOT563	10,000	Tape & Reel		
DMN5L06VK-13A	SOT563	10,000	Tape & Reel		
DMN5L06VAK-7	SOT563	3,000	Tape & Reel		
DMN5L06VAK-7A	SOT563	3,000	Tape & Reel		
DMN5L06VAK-13	SOT563	10,000	Tape & Reel		
DMN5L06VAK-13A	SOT563	10,000	Tape & Reel		
DMN5010VAK-7	SOT563	3,000	Tape & Reel		
DMN5010VAK-7A	SOT563	3,000	Tape & Reel		
DMN5010VAK-13	SOT563	10,000	Tape & Reel		
DMN5010VAK-13A	SOT563	10,000	Tape & Reel		

Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

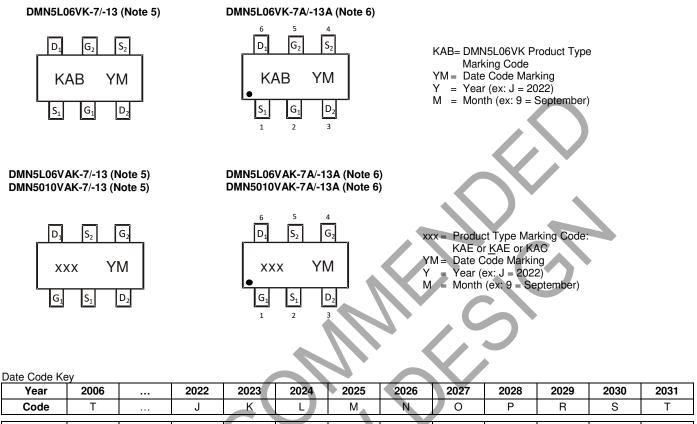
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information (Notes 5 & 6)



Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

Notes: 5. Package is non-polarized. Parts may be on reel in orientation illustrated, 180° rotated, or mixed (both ways). 6. Part number with suffix 7A and 13A designates devices marked with a Pin 1 indicator. There is no other difference between both devices.





Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Value	Unit
Drain Source Voltage		VDSS	50	V
Drain-Gate Voltage $R_{GS} \le 1.0M\Omega$		V _{DGR}	50	V
Gate-Source Voltage	Continuous Pulsed	Vgss	±20 ±40	V
Drain Current (Note 7)	Continuous Pulsed	I _D I _{DM}	280 1.5	mA A

Thermal Characteristics

Symbol	Value	Unit
PD	250	mW
Reja	500	°C/W
TJ, TSTG	-55 to +150	°C
	PD Reja	PD 250 Reja 500

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

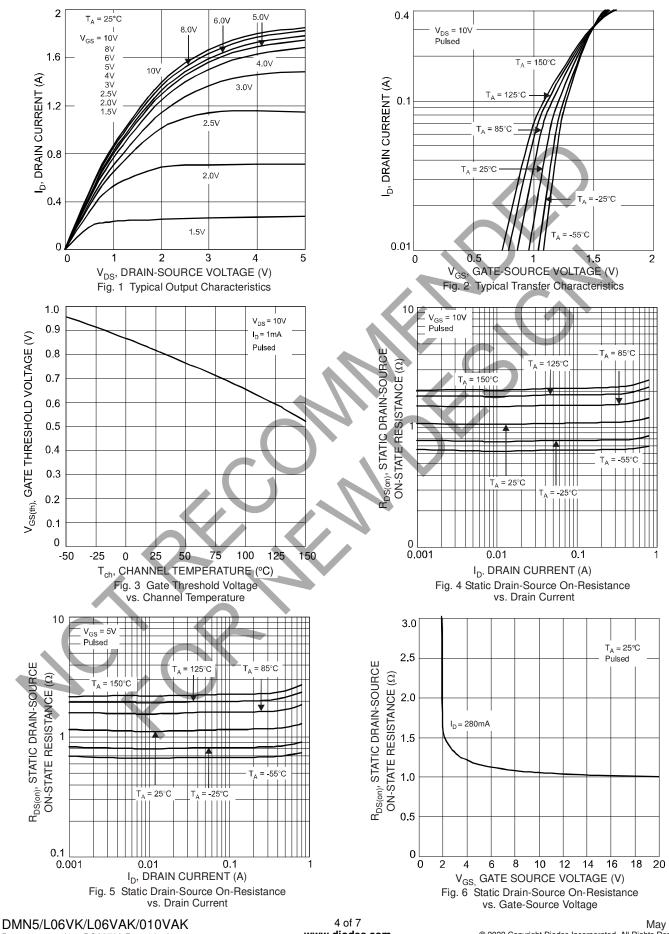
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 8)	1					Ŧ		
Drain-Source Breakdown Voltage		BVDSS	50	_		V	$V_{GS} = 0V, I_D = 10\mu A$	
Zero Gate Voltage Drain Current	@ T _C = +25°C	IDSS	—		60	nA	$V_{DS} = 50V, V_{GS} = 0V$	
Gate-Body Leakage		lgss			1 500 50	μA nA nA		
ON CHARACTERISTICS (Note 8)								
Gate Threshold Voltage @TJ = +0°C	@T _J = +25°C to +85°C (Note 9)	VGS(TH)	0.49 0.30		1.0 1.2	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-Resistance		RDS(ON)			3.0 2.5 2.0	Ω		
On-State Drain Current		ID(ON)	0.5	1.4	_	Α	Vgs = 10V, Vds = 7.5V	
Forward Transconductance		Y _{fs}	200	—	_	mS	V _{DS} =10V, I _D = 0.2A	
Source-Drain Diode Forward Voltage		Vsd	0.5	—	1.4	V	Vgs = 0V, ls = 115mA	
DYNAMIC CHARACTERISTICS (Note 9)								
Input Capacitance		Ciss	_	—	50	pF		
tput Capacitance		Coss	_	—	25	pF	V _{DS} = 25V, V _{GS} = 0V f = 1.0MHz	
Reverse Transfer Capacitance		C _{rss}		_	5.0	pF		

 Notes:
 7. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

 8. Short duration pulse test used to minimize self-heating effect.
 9. Guaranteed by design. Not subject to product testing.



DMN5/L06VK/ L06VAK/010VAK



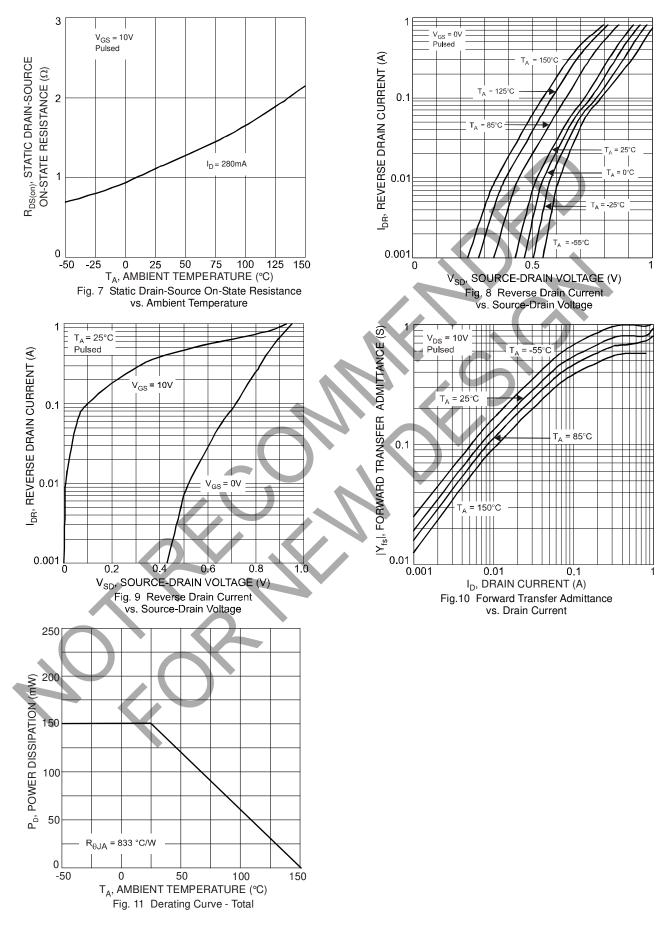
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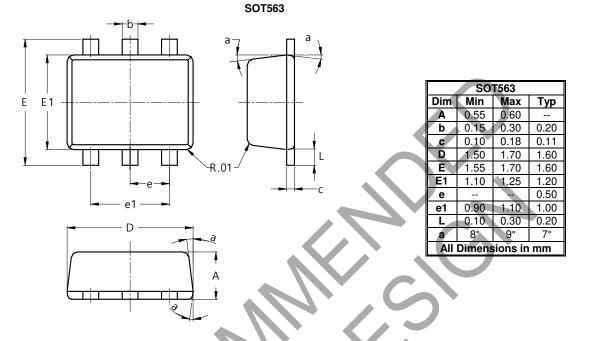
DMN5/L06VK/ L06VAK/010VAK





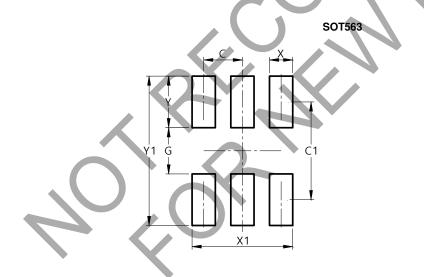
Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	0.500
C1	1.270
G	0.600
Х	0.300
X1	1.300
Y	0.670
Y1	1.940



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