

Features and Benefits

- Quasi-resonant topology IC ⇒ Low EMI noise and soft switching
- Bottom-skip mode ⇒ Improved system efficiency over the entire output load by avoiding increase of switching frequency
- Auto-Standby mode ⇒ Lowers input power at very light output load condition
- Avalanche-guaranteed MOSFET \Rightarrow Improves system-level reliability and does not require V_{DSS} derating
- 650 V / 1.70 Ω
- Various protections ⇒ Improved system-level reliability

Continued on the next page...

Package: 6-pin TO-220



Not to scale

Description

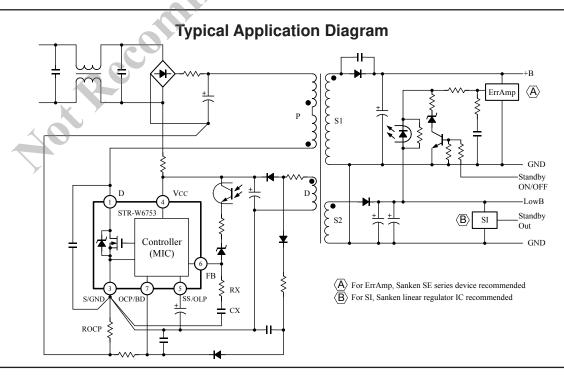
The STR-W6753 is a quasi-resonant topology IC designed for SMPS applications. It shows lower EMI noise characteristics than conventional PWM solutions, especially at greater than 2 MHz. It also provides a soft-switching mode to turn on the internal MOSFET at close to zero voltage ($V_{\rm DS}$ bottom point) by use of the resonant characteristic of primary inductance and a resonant capacitor.

The package is a fully molded TO-220, which contains the controller chip (MIC) and MOSFET, enabling output power up to 58 W with universal input or 120 W with a 230 VAC input. The bottom-skip mode skips the first bottom of $V_{\rm DS}$ and turns on the MOSFET at the second bottom point, to minimize an increase of operating frequency at light output load, improving system-level efficiency over the entire load range.

There are two standby modes available to reduce the input power under very light load conditions. The first is Auto-Standby mode, which is internally triggered by periodic sensing, and the other is a manual standby mode, which is executed by clamping the secondary output. In general applications, the manual standby mode reduces the input power further compared to Auto-Standby mode.

The soft-start mode minimizes surge voltage and reduces power stress to the MOSFET and to the secondary rectifying

Continued on the next page...



STR-W6753

Quasi-Resonant Topology **Primary Switching Regulators**

Features and Benefits (continued)

- Pulse-by-pulse drain overcurrent limiting
- Overvoltage protection (bias winding voltage sensing), with latch
- Overload protection with latch
- Maximum on-time limit

Description (continued)

diodes during the start-up sequence. Various protections such as overvoltage, overload, overcurrent, maximum on-time protections and avalanche-energy-guaranteed MOSFET secure good systemlevel reliability.

Applications include the following:

- Set Top Box
- LCD PC monitor, LCD TV
- Printer, Scanner
- SMPS power supplies

Selection Guide

| Coloction datas | | |
|-----------------|---------|--|
| Part Number | Package | |
| STR-W6753 | TO-220 | |

| Selection Guide | | | | |
|--|-----------------------|---|-------------|------|
| Part Number | Packag | e | | |
| STR-W6753 | TO-220 | | | |
| Absolute Maximum Ratings at T _A = | | ROL | | |
| Parameter | Symbol | Conditions | Rating | Unit |
| Drain Current ¹ | I _D peak | Single pulse | 11.2 | Α |
| Maximum Switching Current ² | I _{Dmax} | $T_A = -20^{\circ}C \text{ to } 125^{\circ}C$ | 11.2 | Α |
| Single Pulse Avalanche Energy ³ | | Single pulse, V_{DD} = 99 V, L = 20 mH, I_{Lpeak} = 3.6 A | 145 | mJ |
| Input Voltage for Controller (MIC) | V _{CC} | | 35 | V |
| SS/OLP Terminal Voltage | V _{SSOLP} | | -0.5 to 6.0 | V |
| FB Terminal Inflow Current | | 7 | 10 | mA |
| FB Terminal Voltage V _F | | I _{FB} within the limits of I _{FB} | -0.5 to 9.0 | V |
| OCP/BD Terminal Voltage | V _{OCPBD} | | -1.5 to 5.0 | V |
| MOSEET Bower Dissipation4 | Po. | With infinite heatsink | 26.0 | W |
| MOSFET Power Dissipation ⁴ | | Without heatsink | 1.3 | W |
| Controller (MIC) Power Dissipation | P _{D2} | V _{CC} × I _{CC} | 0.8 | W |
| Operating Internal Leadframe Temperature | \cup T _F | Recommended operation temperature, see cautions | -20 to 115 | °C |
| Operating Ambient Temperature | T _{OP} | | -20 to 115 | °C |
| Storage Temperature | T _{stg} | | -40 to 125 | °C |
| Channel Temperature | T _{ch} | | 150 | °C |

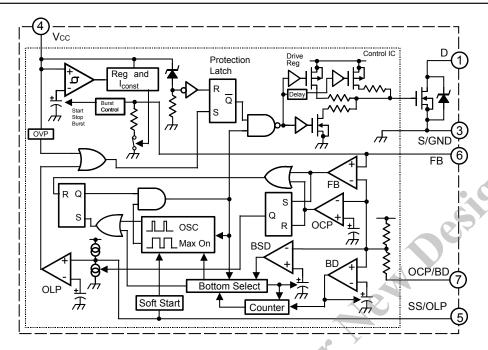
¹Refer to figure 2

All performance characteristics given are typical values for circuit or system baseline design only and are at the nominal operating voltage and an ambient temperature, TA, of 25°C, unless otherwise stated.

²I_{DMAX} is the drain current determined by the drive voltage of the IC and the threshold voltage, V_{th}, of the MOSFET

³Refer to figure 3

⁴Refer to figure 5



Terminal List Table

| Number | Name | Description | Functions |
|--------|--------|---|--|
| 1 | D | Drain | MOSFET drain |
| 2 | NC | Clipped | No connection |
| 3 | S/GND | Source/ground terminal | MOSFET source and ground |
| 4 | VCC | Power supply terminal | Input of power supply for control circuit |
| 5 | SS/OLP | Soft Start/Overload Protection terminal | Input to set delay for Overload protection and Soft Start operation |
| 6 | FB | Feedback terminal | Input for Constant Voltage Control and Burst (intermittent) Mode oscillation control signals |
| 7 | OCP/BD | Overcurrent Protection/Bottom Detection | Input for overcurrent detection and bottom detection signals |

Figure 1 – MOSFET Safe Operating Area Derating Curve

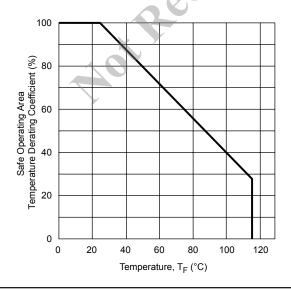


Figure 2 – MOSFET Safe Operating Area Drain Current versus Voltage at T_A = 25°C, Single Pulse

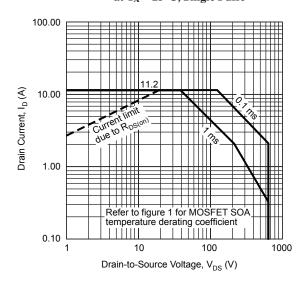


Figure 3 – MOSFET Avalanche Energy Derating Curve V_{DD} = 99 V, L = 20 mH, I_L = 3.6 A

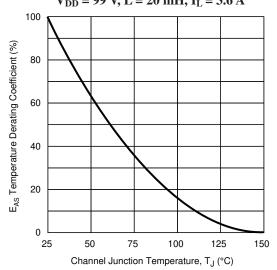


Figure 4 – Transient Thermal Resistance

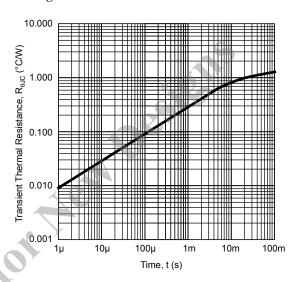
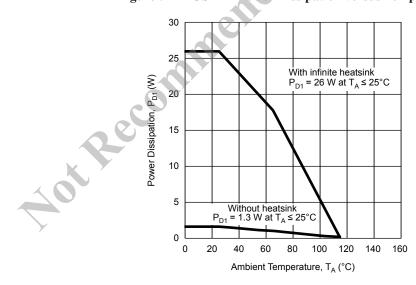


Figure 5 – MOSFET Power Dissipation versus Temperature



STR-W6753

Quasi-Resonant Topology Primary Switching Regulators

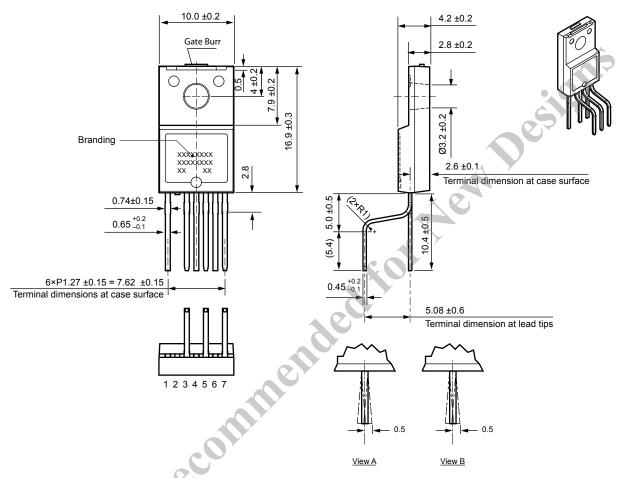
ELECTRICAL CHARACTERISTICS

| Characteristic | Symbol | Test Conditions | Min. | Тур. | Max. | Units |
|---|-----------------------------|---|------------|--------|--------|-------|
| ELECTRICAL CHARACTERISTICS for Controller (| MIC) ¹ , valid a | t $T_A = 25$ °C, $V_{CC} = 20$ V, unless other | rwise spec | cified | | |
| Power Supply Start-up Operation | | | | | | |
| Operation Start Voltage | V _{CC(ON)} | V _{CC} = 0→20 V | 16.3 | 18.2 | 19.9 | V |
| Operation Stop Voltage | V _{CC(OFF)} | V _{CC} = 20→8.8 V | 8.8 | 9.7 | 10.6 | V |
| Circuit Current In Operation | I _{CC(ON)} | | - | - (| 6 | mA |
| Circuit Current In Non-Operation | I _{CC(OFF)} | V _{CC} = 15 V | _ | - | 100 | μA |
| Oscillation Frequency | f _{osc} | | 19 | 22 | 25 | kHz |
| Soft Start Operation Stop Voltage | V _{SSOLP(SS)} | V _{SS/OLP} increasing | 1.1 | 1.2 | 1.4 | V |
| Soft Start Operation Charging Current | I _{SSOLP(SS)} | V _{SS/OLP} = 0 V | -710 | -550 | -390 | μΑ |
| Normal Operation | | | | | | |
| Bottom-Skip Operation Threshold Voltage 1 | V _{OCPBD(BS1)} | | -0.720 | -0.665 | -0.605 | V |
| Bottom-Skip Operation Threshold Voltage 2 | V _{OCPBD(BS2)} | | -0.485 | -0.435 | -0.385 | V |
| Overcurrent Detection Threshold Voltage | V _{OCPBD(LIM)} | V _{OCP/BD} falling | -0.995 | -0.940 | -0.895 | V |
| OCP/BD Terminal Outflow Current | I _{OCPBD} | V _{OCP/BD} = -0.95 V | -250 | -100 | -40 | μA |
| Quasi-Resonant Operation Threshold Voltage 1 | V _{OCPBD(TH1)} | V _{OCP/BD} falling | 0.28 | 0.40 | 0.52 | V |
| Quasi-Resonant Operation Threshold Voltage 2 | V _{OCPBD(TH2)} | V _{OCP/BD} rising | 0.67 | 0.80 | 0.93 | V |
| FB Terminal Threshold Voltage | V _{FB(OFF)} | V _{FB} rising | 1.32 | 1.45 | 1.58 | V |
| FB Terminal Inflow Current (Normal Operation) | I _{FB(ON)} | V _{FB} = 1.6 V | 600 | 1000 | 1400 | μA |
| Standby Operation | | CO | | | | |
| Standby Operation Start Voltage | V _{CC(S)} | V _{CC} = 0→15 V, V _{FB} = 1.6 V | 10.3 | 11.1 | 12.1 | V |
| Standby Operation Start Voltage Interval | V _{CC(SK)} | $V_{CC(SK)} = V_{CC(S)} - V_{CC(OFF)}$ | 1.10 | 1.35 | 1.65 | V |
| Standby Non-Operation Circuit Current | Jcc(s) | V _{CC} = 10.2 V, V _{FB} = 1.6 V | _ | 20 | 56 | μΑ |
| FB Terminal Inflow Current, Standby Operation | I _{FB(S)} | V _{CC} = 10.2 V, V _{FB} = 1.6 V | _ | 4 | 14 | μΑ |
| FB Terminal Threshold Voltage, Standby Operation | V _{FB(S)} | V _{CC} = 15 V, V _{FB} rising | 0.55 | 1.10 | 1.50 | V |
| Minimum On Time | t _{ON(MIN)} | | 0.5 | 0.8 | 1.2 | μs |
| Protection Operation | | | | | | |
| Maximum On Time | t _{ON(MAX)} | | 27.5 | 32.5 | 39.0 | μs |
| Overload Protection Operation Threshold Voltage | V _{SSOLP(OLP)} | | 4.0 | 4.9 | 5.8 | V |
| Overload Protection Operation Charging Current | I _{SSOLP(OLP)} | V _{SS/OLP} = 2.5 V | -16 | -11 | -6 | μΑ |
| Overvoltage Protection Operation Voltage | V _{CC(OVP)} | V _{CC} = 0→30 V | 25.5 | 27.7 | 29.9 | V |
| Latch Circuit Holding Current ² | I _{CC(H)} | V _{CC(OFF)} - 0.3 V | _ | 45 | 140 | μΑ |
| Latch Circuit Release Voltage ² | V _{CC(La.OFF)} | V _{CC} = 30→6 V, OVP operating | 6.0 | 7.2 | 8.5 | V |
| ELECTRICAL CHARACTERISTICS for MOSFET, valid at T _A = 25°C, unless otherwise specified | | | | | | |
| Drain-to-Source Breakdown Voltage | V _{DSS} | Ι _D = 300 μΑ | 650 | _ | _ | V |
| Drain Leakage Current | I _{DSS} | V _{DS} = 650 V | - | _ | 300 | μΑ |
| On Resistance | R _{DS(on)} | I _D = 1.4 A | _ | _ | 1.7 | Ω |
| Switching Time | t _f | | _ | _ | 400 | ns |
| Thermal Resistance | R _{0ch-F} | Channel to internal frame | _ | _ | 2.0 | °C/W |

¹Current polarity with respect to the IC: positive current indicates current sink at the terminal named, negative current indicates source at the terminal named.

²The latch circuit means a circuit operated OVP and OLP.

PACKAGE DIMENSIONS, TO-220



Gate burr: 0.3 mm (max.) Terminal core material: Cu

Terminal treatment: Ni plating and solder dip

Leadform: 2003

Weight (approximate): 2.3 g

Dimensions in millimeters

Drawing for reference only

Branding codes (exact appearance at manufacturer discretion):

1st line, type: STR 2nd line, subtype: W6753 3rd line, lot: YM DD

Where: Y is the last digit of the year of manufacture

M is the month (1 to 9, O, N, D)

DD is the 2-digit date



Leadframe plating Pb-free. Device composition includes high-temperature solder (Pb >85%), which is exempted from the RoHS directive.

STR-W6753

Quasi-Resonant Topology Primary Switching Regulators

Because reliability can be affected adversely by improper storage environments and handling methods, please observe the following cautions.

Cautions for Storage

- Ensure that storage conditions comply with the standard temperature (5°C to 35°C) and the standard relative humidity (around 40% to 75%); avoid storage locations that experience extreme changes in temperature or humidity.
- Avoid locations where dust or harmful gases are present and avoid direct sunlight.
- Reinspect for rust on leads and solderability of the products that have been stored for a long time.

Cautions for Testing and Handling

When tests are carried out during inspection testing and other standard test periods, protect the products from power surges from the testing device, shorts between the product pins, and wrong connections. Ensure all test parameters are within the ratings specified by Sanken for the products.

Remarks About Using Silicone Grease with a Heatsink

- When silicone grease is used in mounting the products on a heatsink, it shall be applied evenly and thinly. If more silicone grease than required is applied, it may produce excess stress.
- Volatile-type silicone greases may crack after long periods of time, resulting in reduced heat radiation effect. Silicone greases with low consistency (hard grease) may cause cracks in the mold resin when screwing the products to a heatsink.

Our recommended silicone greases for heat radiation purposes, which will not cause any adverse effect on the product life, are indicated below:

| Туре | Suppliers |
|--------|--------------------------------------|
| G746 | Shin-Etsu Chemical Co., Ltd. |
| YG6260 | Momentive Performance Materials Inc. |
| SC102 | Dow Corning Toray Co., Ltd. |

Cautions for Mounting to a Heatsink

When the flatness around the screw hole is insufficient, such
as when mounting the products to a heatsink that has an
extruded (burred) screw hole, the products can be damaged,
even with a lower than recommended screw torque. For
mounting the products, the mounting surface flatness should
be 0.05 mm or less.

- Please select suitable screws for the product shape. Do not
 use a flat-head machine screw because of the stress to the
 products. Self-tapping screws are not recommended. When
 using self-tapping screws, the screw may enter the hole
 diagonally, not vertically, depending on the conditions of hole
 before threading or the work situation. That may stress the
 products and may cause failures.
- Recommended screw torque: 0.588 to 0.785 N

 m (6 to 8 kgf

 cm).
- For tightening screws, if a tightening tool (such as a driver) hits the products, the package may crack, and internal stress fractures may occur, which shorten the lifetime of the electrical elements and can cause catastrophic failure. Tightening with an air driver makes a substantial impact. In addition, a screw torque higher than the set torque can be applied and the package may be damaged. Therefore, an electric driver is recommended.

When the package is tightened at two or more places, first pre-tighten with a lower torque at all places, then tighten with the specified torque. When using a power driver, torque control is mandatory.

Soldering

- When soldering the products, please be sure to minimize the working time, within the following limits:
 260±5°C 10±1 s (Flow, 2 times)
 380±10°C 3.5±0.5 s (Soldering iron, 1 time)
- Soldering should be at a distance of at least 2.0 mm from the body of the products.

Electrostatic Discharge

- When handling the products, the operator must be grounded. Grounded wrist straps worn should have at least 1 M Ω of resistance from the operator to ground to prevent shock hazard, and it should be placed near the operator.
- Workbenches where the products are handled should be grounded and be provided with conductive table and floor mats
- When using measuring equipment such as a curve tracer, the equipment should be grounded.
- When soldering the products, the head of soldering irons or the solder bath must be grounded in order to prevent leak voltages generated by them from being applied to the products.
- The products should always be stored and transported in Sanken shipping containers or conductive containers, or be wrapped in aluminum foil.

- The contents in this document are subject to changes, for improvement and other purposes, without notice. Make sure that this is the latest revision of the document before use.
- Application and operation examples described in this document are quoted for the sole purpose of reference for the use of the products herein and Sanken can assume no responsibility for any infringement of industrial property rights, intellectual property rights or any other rights of Sanken or any third party which may result from its use.
- Although Sanken undertakes to enhance the quality and reliability of its products, the occurrence of failure and defect of semiconductor products at a certain rate is inevitable. Users of Sanken products are requested to take, at their own risk, preventative measures including safety design of the equipment or systems against any possible injury, death, fires or damages to the society due to device failure or malfunction.
- Sanken products listed in this document are designed and intended for the use as components in general purpose electronic equipment or apparatus (home appliances, office equipment, telecommunication equipment, measuring equipment, etc.).
- When considering the use of Sanken products in the applications where higher reliability is required (transportation equipment and its control systems, traffic signal control systems or equipment, fire/crime alarm systems, various safety devices, etc.), and whenever long life expectancy is required even in general purpose electronic equipment or apparatus, please contact your nearest Sanken sales representative to discuss, prior to the use of the products herein.
- The use of Sanken products without the written consent of Sanken in the applications where extremely high reliability is required (aerospace equipment, nuclear power control systems, life support systems, etc.) is strictly prohibited.
- In the case that you use Sanken products or design your products by using Sanken products, the reliability largely depends on the degree of derating to be made to the rated values. Derating may be interpreted as a case that an operation range is set by derating the load from each rated value or surge voltage or noise is considered for derating in order to assure or improve the reliability. In general, derating factors include electric stresses such as electric voltage, electric current, electric power etc., environmental stresses such as ambient temperature, humidity etc. and thermal stress caused due to self-heating of semiconductor products. For these stresses, instantaneous values, maximum values and minimum values must be taken into consideration.
- In addition, it should be noted that since power devices or IC's including power devices have large self-heating value, the degree of derating of junction temperature affects the reliability significantly.
- When using the products specified herein by either (i) combining other products or materials therewith or (ii) physically, chemically or otherwise processing or treating the products, please duly consider all possible risks that may result from all such uses in advance and proceed therewith at your own responsibility.
- Anti radioactive ray design is not considered for the products listed herein.
- Sanken assumes no responsibility for any troubles, such as dropping products caused during transportation out of Sanken's distribution network.
- The contents in this document must not be transcribed or copied without Sanken's written consent.