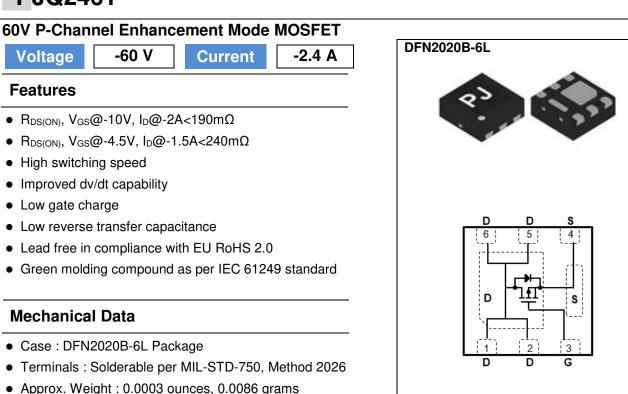


Voltage

Features

PJQ2461



• Low reverse transfer capacitance

Lead free in compliance with EU RoHS 2.0

-60 V

• R_{DS(ON)}, V_{GS}@-10V, I_D@-2A<190mΩ • $R_{DS(ON)}$, V_{GS} @-4.5V, I_D @-1.5A<240m Ω

Current

• Green molding compound as per IEC 61249 standard

Mechanical Data

• High switching speed • Improved dv/dt capability

• Low gate charge

- Case : DFN2020B-6L Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0003 ounces, 0.0086 grams

Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		VDS	-60	V	
Gate-Source Voltage		V _{GS}	<u>+</u> 20		
Continuous Drain Current (Note 4)	T _A =25°C		-2.4		
	T _A =70°C	lo	-1.9	А	
Pulsed Drain Current (Note 1)		Ідм	-9.6		
	T _A =25°C	_	2	w	
Power Dissipation	T _A =70°C	PD P	1.3		
Single Pulse Avalanche Energy (Note 6)		Eas	32	mJ	
Operating Junction and Storage Temperature Range		TJ,TSTG	-55~150	٥C	
Typical Thermal Resistance - Junction to Ambient ^(Note 4,5)		Reja	62.5	°C/W	



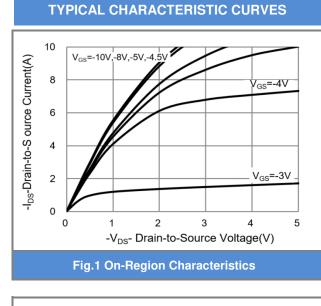
Electrical Characteristics (T_A=25°C unless otherwise noted)

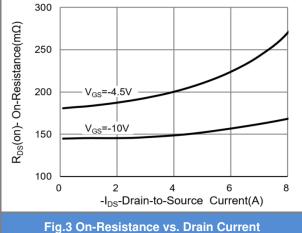
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	BV_{DSS} $V_{GS}=0V$, $I_D=-250uA$	-60	-	-	v
Gate Threshold Voltage	$V_{\text{GS(th)}}$	V _{DS} =V _{GS} , I _D =-250uA	-1.0	-1.88	-2.5	
Drain-Source On-State Resistance	$R_{\text{DS(on)}}$	V_{GS} =-10V, I_{D} =-2A	-	140	190	mΩ
		V _{GS} =-4.5V, I _D =-1.5A	-	190	240	
Zero Gate Voltage Drain Current	IDSS	V _{DS} =-60V, V _{GS} =0V	-	-	-1	uA
Gate-Source Leakage Current	lgss	V _{GS=+} 20V, V _{DS} =0V	-	-	<u>+</u> 100	nA
Dynamic (Note 7)						
Total Gate Charge	Qg	V _{DS} =-30V, I _D =-2A, V _{GS} =-10V ^(Note 1,2)	-	8.3	-	nC
Gate-Source Charge	Qgs		-	1.8	-	
Gate-Drain Charge	Q_{gd}		-	1.6	-	
Input Capacitance	Ciss	V _{DS} =-30V, V _{GS} =0V, f=1MHZ	-	430	-	pF ns
Output Capacitance	Coss		-	33	-	
Reverse Transfer Capacitance	Crss		-	29	-	
Turn-On Delay Time	td _(on)	V_{DD} =-30V, I _D =-1A, V_{GS} =-10V, R_{G} =6 Ω ^(Note 1,2)	-	5.1	-	
Turn-On Rise Time	tr		-	20	-	
Turn-Off Delay Time	td _(off)		-	36	-	
Turn-Off Fall Time	tf		-	11	-	
Drain-Source Diode				•		•
Maximum Continuous Drain-Source	la la		-	-	-1.5	A
Diode Forward Current	ls					
Diode Forward Voltage	V_{SD}	Is=-1A, V _{GS} =0V	-	-0.78	-1	v

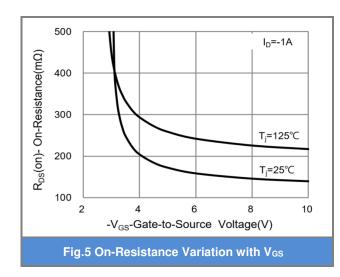
NOTES :

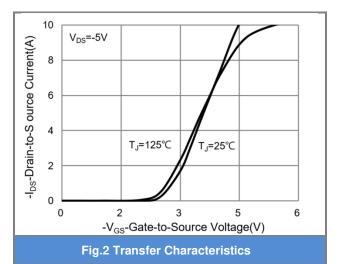
- 1. Pulse width \leq 300 us, Duty cycle \leq 2%.
- 2. Essentially independent of operating temperature typical characteristics.
- Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C. Ratings are based on low frequency and duty cycles to keep initial T_J =25°C.
- 4. The maximum current rating is package limited.
- R_{€JA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
- 6. The test condition is L=1mH, I_{AS}=-8A, V_{DD}=-25V, V_{GS}=-10V
- 7. Guaranteed by design, not subject to production testing.











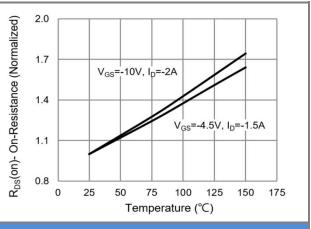
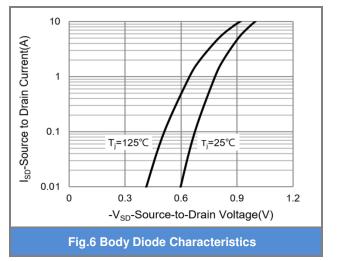
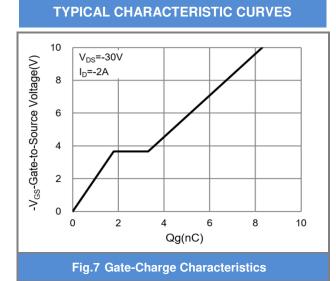
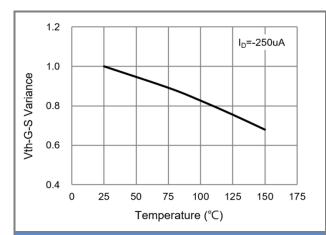


Fig.4 On-Resistance vs. Junction temperature

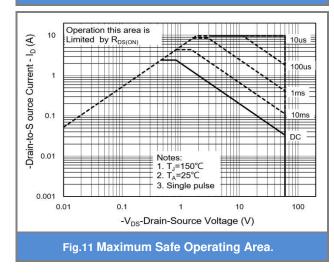


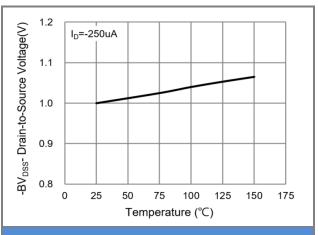














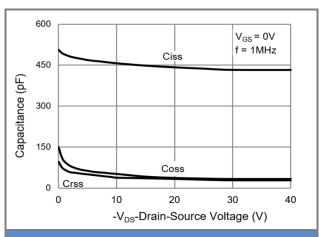
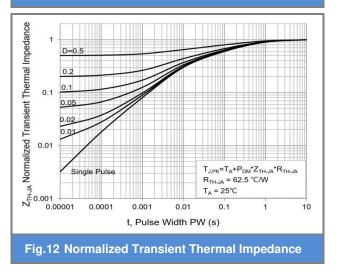


Fig.10 Capacitance vs. Drain-Source Voltage

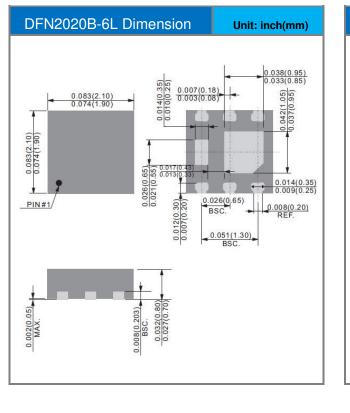


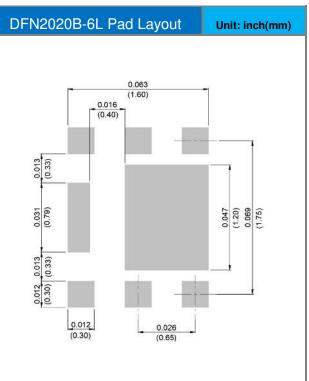


Part No. Packing Code Version

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJQ2461_R1_00001	DFN2020B-6L	3K pcs / 7" reel	461	Halogen free RoHS compliant

Packaging Information & Mounting Pad Layout







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