

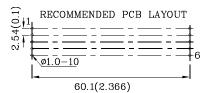
57mm (2.3") SINGLE DIGIT NUMERIC DISPLAY

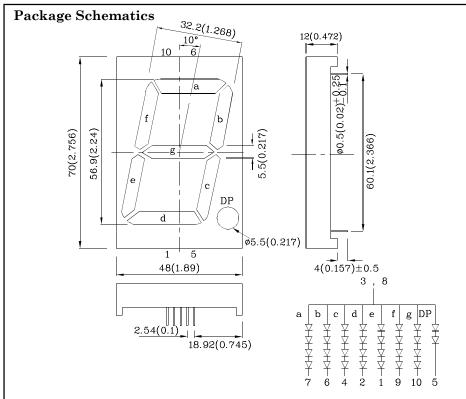
Features

- Low power consumption
- ullet Robust package
- I.C. Compatible
- Standard configuration: Gray face w/ white segments
- Optional black face provides superior color contrast
- RoHS Compliant









Notes:

1. All dimensions are in millimeters (inches), Tolerance is $\pm 0.25 (0.01")$ unless otherwise noted.

2. Specifications are subject to change without notice.

Absolute Maximum Ratings (T _A =25°C)		MDK (AlGaInP)	Unit	
Reverse Voltage (Per Chip)	V_{R}	5	V	
Forward Current I_{F}		30 (30)	mA	
Forward Current (Peak) 1/10 Duty Cycle 0.1ms Pulse Width(Dp)	ifs	185 (185)	mA	
Power Dissipation (Per Chip)	P_D	150	mW	
Operating Temperature	$T_{\rm A}$	-40 ~ +85	°C	
Storage Temperature	Tstg	-40 ~ +85	-0	
Lead Solder Temperature [2mm Below Package Base]	260°C For 3-5 Seconds			

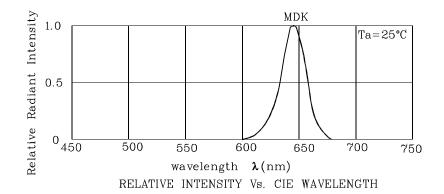
Operating Characteristics (T _A =25°C)		MDK (AlGaInP)	Unit
Forward Voltage (Typ.) (Dp) (I _F =10mA)	V_{F}	7.4 (3.7)	V
Forward Voltage (Max.) (Dp) (I _F =10mA)	V_{F}	V _F 10 (5.0)	
Reverse Current (Max.) (Per Chip) (V _R =5V)	I_R	10	uA
Wavelength of Peak Emission CIE127-2007* (Typ.) (I _F =10mA)	λΡ	645*	nm
Wavelength of Dominant Emission CIE127-2007* (Typ.) (I _F =10mA)	λD	630*	nm
Spectral Line Full Width At Half-Maximum (Typ.) (I _F =10mA)	$\triangle \lambda$	28	nm
Capacitance (Typ.) (V _F =0V, f=1MHz)	С	35	pF

Part Number	Emitting Color	Emitting Material	CIE127 (I _F =10n	7-2007*	Wavelength CIE127-2007* nm λP	Description
			min.	typ.		
XDMDK57A	Red	AlGaInP	255000 52000*	439990 119990*	645*	Common Anode, Rt. Hand Decimal

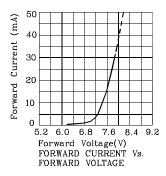
^{*}Luminous intensity value and wavelength are in accordance with CIE127-2007 standards. Mar 10.2014

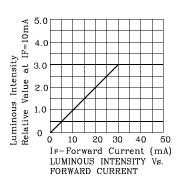
XDSB7716 V1-X Layout: Maggie L.

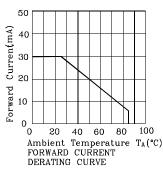


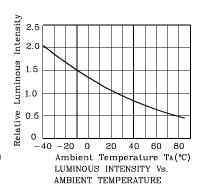


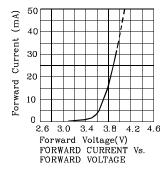
❖ MDK

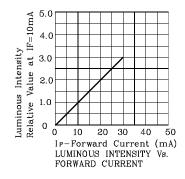


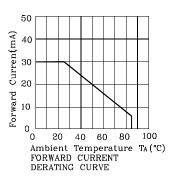


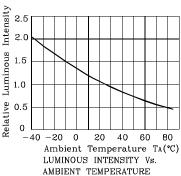




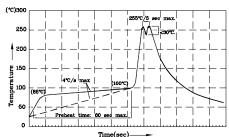








Wave Soldering Profile for Thru-Hole Products (Pb-Free Components)



- pre-heat temperature of 105°C or less (as measured attached to the LED pins) prior to immersion in the maximum solder bath temperature of 260°C oldering temperature between 245°C ~ 255°C for 3 se
- 3.Do not apply stress to the epoxy resin while the temperature is above 4.Fixtures should not incur stress on the component when mounting and
- Adving soldering process

 SAC 305 solder alloy is recommended.

 6.No more than one wave soldering pass.

 7.During wave soldering, the PCS top-surface temperature should be kept below 105°C.

Remarks:

If special sorting is required (e.g. binning based on forward voltage, luminous intensity / luminous flux, or wavelength),

the typical accuracy of the sorting process is as follows:

- 1. Wavelength: +/-1nm
- 2. Luminous Intensity / Luminous Flux: +/-15%
- 3. Forward Voltage: +/-0.1V

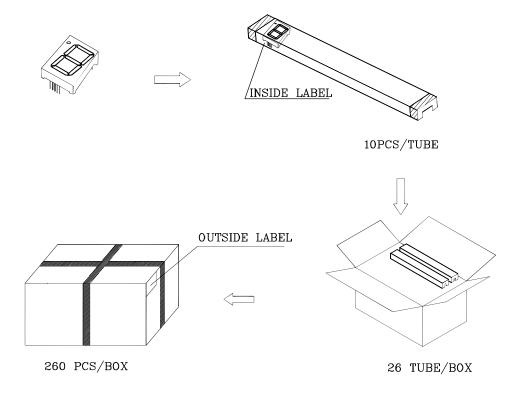
Note: Accuracy may depend on the sorting parameters.



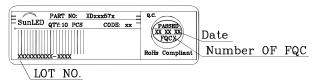


PACKING & LABEL SPECIFICATIONS

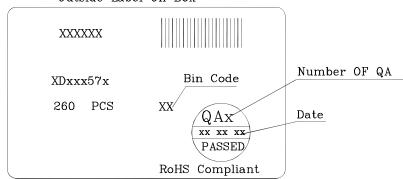
www.SunLEDusa.com



Inside Label On IC-tube



Outside Label On Box



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- 2. Contents within this document are subject to improvement and enhancement changes without notice.
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- 6. Additional technical notes are available at http://www.SunLEDusa.com/TechnicalNotes.asp

Mar 10,2014