

To our customers,

Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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MOS FIELD EFFECT TRANSISTOR
2SK1485

**N-CHANNEL MOS FIELD EFFECT TRANSISTOR
 FOR SWITCHING**

DESCRIPTION

The 2SK1485, N-channel vertical type MOS FET is a switching device which can be driven directly by the output of ICs having a 5 V power source.

As the MOS FET has low on-state resistance and excellent switching characteristics, it is suitable for driving actuators such as motors, relays, and solenoids.

FEATURES

- Directly driven by ICs having a 5 V power source.
- Low on-state resistance
 $R_{DS(on)1} = 1.2 \Omega \text{ MAX. (} V_{GS} = 4.0 \text{ V, } I_D = 0.5 \text{ A)}$
 $R_{DS(on)2} = 0.8 \Omega \text{ MAX. (} V_{GS} = 10 \text{ V, } I_D = 0.5 \text{ A)}$
- Complementary to 2SJ199.

ABSOLUTE MAXIMUM RATINGS (T_A = 25°C)

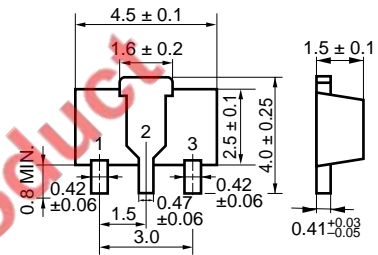
Drain to Source Voltage (V _{GS} = 0 V)	V _{DSS}	100	V
Gate to Source Voltage (V _{DS} = 0 V)	V _{GSS}	±20	V
★ Drain Current (DC) (T _c = 25°C)	I _{D(DC)}	±1.0	A
Drain Current (pulse) ^{Note1}	I _{D(pulse)}	±2.0	A
Total Power Dissipation (T _A = 25°C) ^{Note2}	P _T	2.0	W
Channel Temperature	T _{ch}	150	°C
Storage Temperature	T _{stg}	-55 to +150	°C

Notes1. PW ≤ 10 ms, Duty Cycle ≤ 50%

2. Mounted on ceramic board of 16 cm² × 0.7 mm

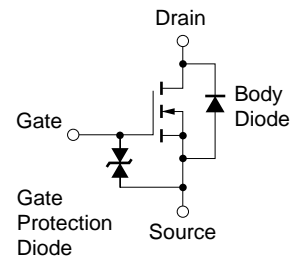
Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

PACKAGE DRAWING (Unit : mm)



1. Source
 2. Drain
 3. Gate
 MARK : NC

EQUIVALENT CIRCUIT

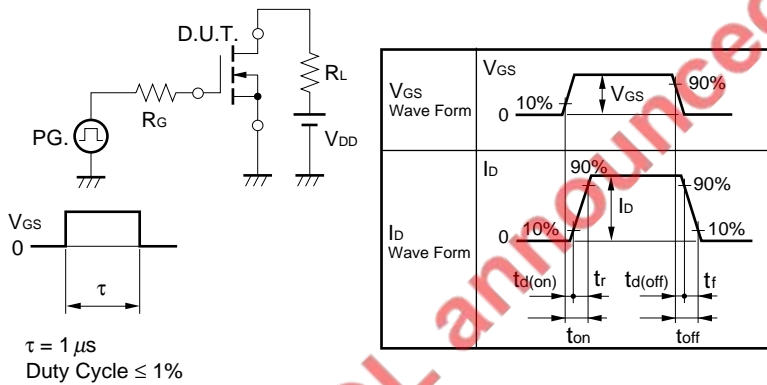


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ELECTRICAL CHARACTERISTICS (T_A = 25°C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 100 V, V _{GS} = 0 V			10	μA
Gate Leakage Current	I _{GSS}	V _{GS} = ±20 V, V _{DS} = 0 V			±10	μA
Gate Cut-off Voltage	V _{GS(off)}	V _{DS} = 10 V, I _D = 1 mA	0.8	1.2	2.0	V
Forward Transfer Admittance	y _{fs}	V _{DS} = 10 V, I _D = 0.5 A	0.4			S
Drain to Source On-state Resistance	R _{DS(on)1}	V _{GS} = 4.0 V, I _D = 0.5 A		0.6	1.2	Ω
	R _{DS(on)2}	V _{GS} = 10 V, I _D = 0.5 A		0.5	0.8	Ω
Input Capacitance	C _{iss}	V _{DS} = 10 V		230		pF
Output Capacitance	C _{OSS}	V _{GS} = 0 V		80		pF
Reverse Transfer Capacitance	C _{rss}	f = 1 MHz		12		pF
Turn-on Delay Time	t _{d(on)}	V _{DD} = 25 V, I _D = 0.5 A		14		ns
Rise Time	t _r	V _{GS} = 10 V		14		ns
Turn-off Delay Time	t _{d(off)}	R _G = 10 Ω		370		ns
Fall Time	t _f			65		ns

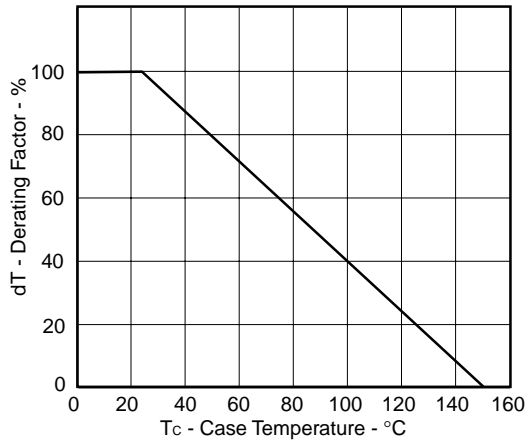
SWITCHING TIME



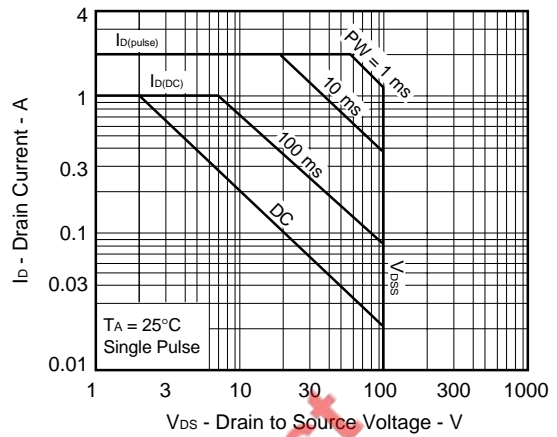
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TYPICAL CHARACTERISTICS (T_A = 25°C)

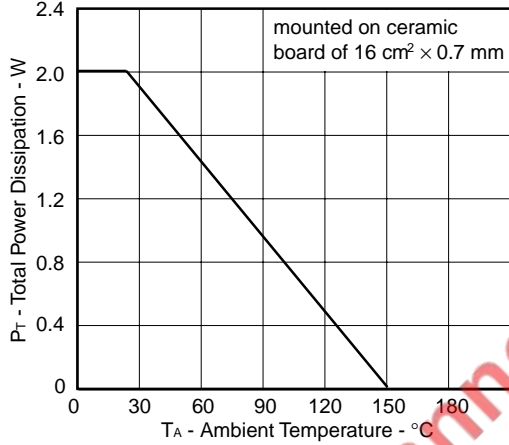
DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA



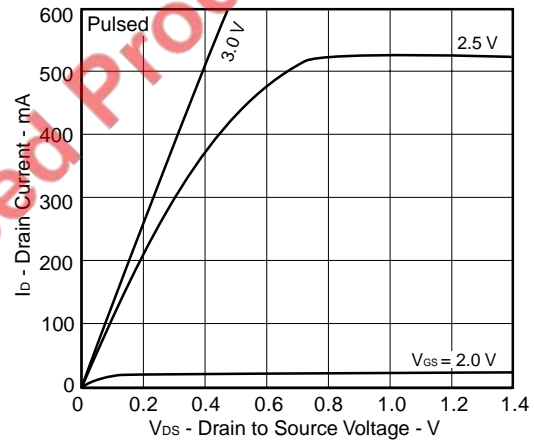
FORWARD BIAS SAFE OPERATING AREA



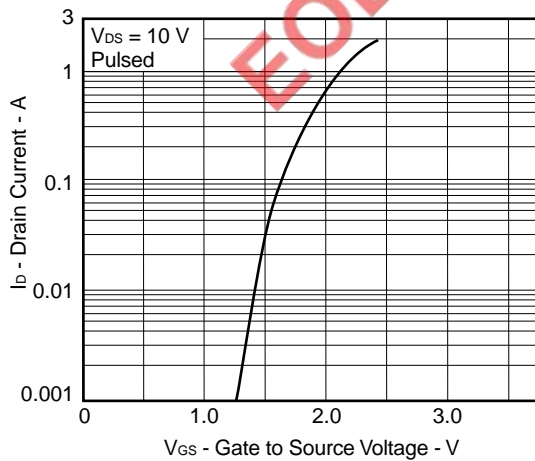
TOTAL POWER DISSIPATION vs. AMBIENT TEMPERATURE



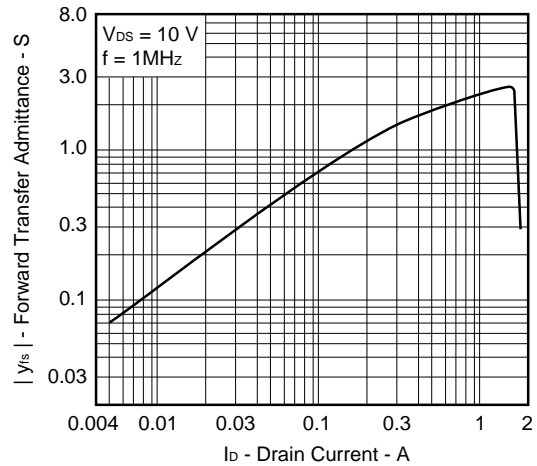
DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE

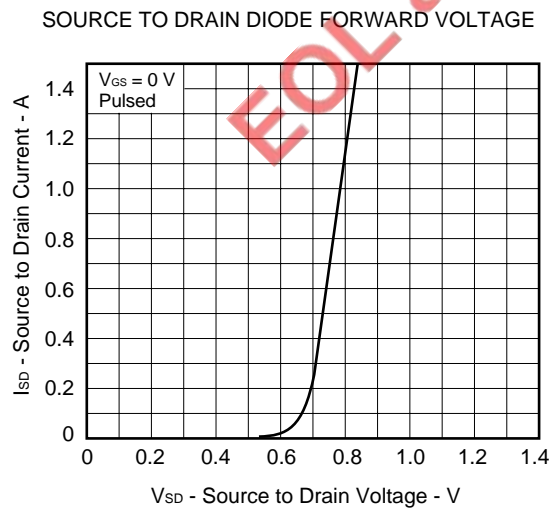
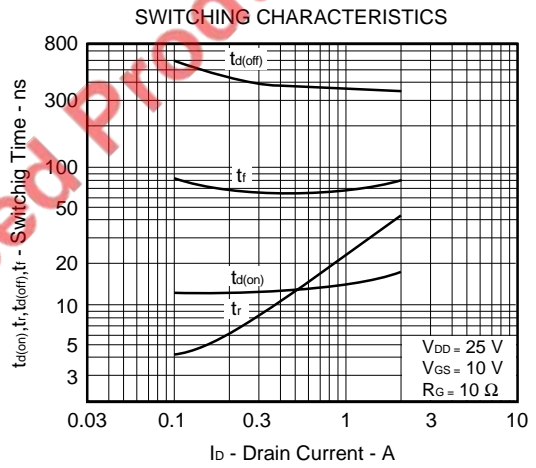
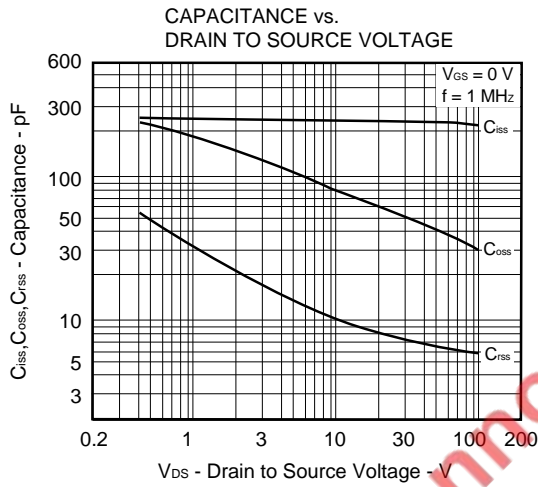
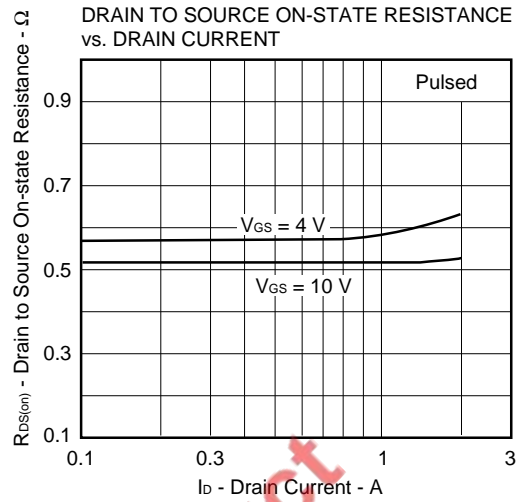
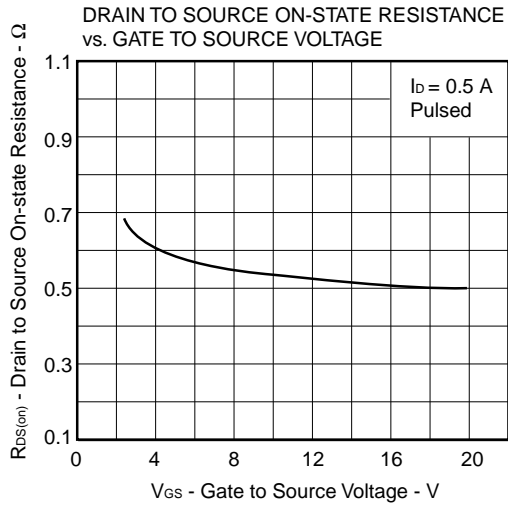


TRANSFER CHARACTERISTICS



FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT





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