



60V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D T _C = +25°C	
60V	$6m\Omega @ V_{GS} = 10V$	100A	
60 V	$10m\Omega @ V_{GS} = 4.5V$	85A	

Features

- 100% Unclamped Inductive Switching Ensures More Reliable and Robust End Application
- Low Input Capacitance
- Low Input/Output Leakage
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

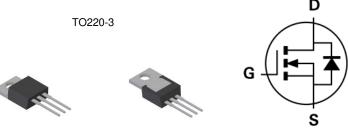
Description and Applications

This new generation MOSFET features low on-resistance and fast switching, making it ideal for high-efficiency power management applications.

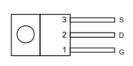
- Engine Management Systems
- Body Control Electronics
- DC-DC Converters

Mechanical Data

- Case: TO220-3
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Terminals: Matte Tin Finish Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (63)
- Terminal Connections: See Diagram Below
- Weight: 1.85 grams (Approximate)



Bottom View



Equivalent Circuit

Top View Pin Out Configuration

Ordering Information (Note 4)

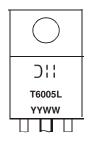
Top View

Part Number	Case	Packaging
DMT6005LCT	TO220-3	50 Pieces/Tube

Notes: 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.

- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



Dili=Manufacturer's Marking
T6005L = Product Type Marking Code
YYWW = Date Code Marking
YY or YY = Last Digit of Year (ex: 16 = 2016)
WW or WW = Week Code (01 to 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Drain-Source Voltage	V_{DSS}	60	V	
Gate-Source Voltage	V_{GSS}	±20	V	
Continuous Durin Comment (Nata C)	T _C = +25°C		100	А
Continuous Drain Current (Note 6)	$T_C = +70$ °C	ΙD	80	
Maximum Continuous Body Diode Forward Current (Note 6)	T _C = +25°C	Is	83	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	130	Α	
Avalanche Current, L=0.1mH	I _{AS}	29.5	Α	
Avalanche Energy, L=0.1mH	Eas	43.5	mJ	

Thermal Characteristics

Characteristic	Symbol	Value	Units	
Total Power Dissipation (Note 5)	$T_A = +25$ °C	P_{D}	2.3	W
Thermal Resistance, Junction to Ambient (Note 5)		$R_{\theta JA}$	52.8	°C/W
Total Power Dissipation (Note 6)	$T_C = +25^{\circ}C$	P_{D}	104	W
Thermal Resistance, Junction to Case (Note 6)	R _{0JC}	1.2	°C/W	
Operating and Storage Temperature Range	T _{J,} T _{STG}	-55 to +150	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

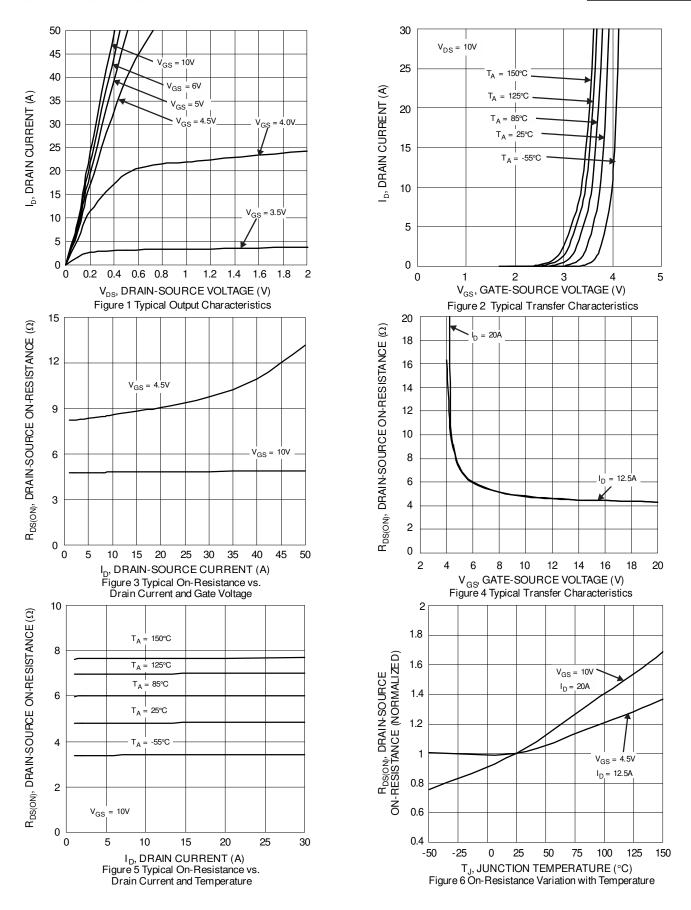
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	60	1	_	٧	$V_{GS} = 0V$, $I_D = 1mA$	
Zero-Gate Voltage Drain Current	I _{DSS}	_	-	1	μΑ	$V_{DS} = 48V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(TH)}	1	1	3	٧	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance		_	4.5	6	mΩ	$V_{GS} = 10V, I_D = 20A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	_	8.8	10	mΩ	$V_{GS} = 4.5V, I_D = 12.5A$	
Diode Forward Voltage	V_{SD}	_	_	1.2	V	$V_{GS} = 0V, I_{S} = 20A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	CISS	_	2,962	_		$V_{DS} = 30V$, $V_{GS} = 0V$, $f = 1MHz$	
Output Capacitance	Coss	_	965	_	рF		
Reverse Transfer Capacitance	C _{RSS}	_	60	_			
Gate Resistance	Rg	_	0.66	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = 10V)	Q_{G}	_	47.1	_			
Total Gate Charge (V _{GS} = 4.5V)	Q_{G}	_	23.1	_	nC	V 20V I 50A	
Gate-Source Charge	Q_{GS}	_	10.2	_	IIC	$V_{DD} = 30V, I_{D} = 50A$	
Gate-Drain Charge	Q_{GD}	_	12.5	_			
Turn-On Delay Time	t _{D(ON)}	_	8.3	_		$V_{DD} = 30V, V_{GS} = 10V,$ $I_{D} = 30A, R_{G} = 3.3\Omega$	
Turn-On Rise Time	t _R	_	9.4	_			
Turn-Off Delay Time	t _{D(OFF)}	_	22	_	ns		
Turn-Off Fall Time	t _F	_	8.9	_			
Reverse Recovery Time	t _{RR}	_	40.4	_	ns I con II/II dona/		
Reverse Recovery Charge	Q _{RR}	_	49.7	_	nC	$I_F = 30A$, di/dt = 100A/ μ s	

Notes: 5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.

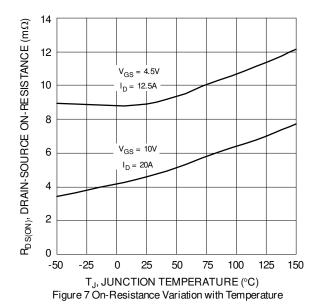
6. Device mounted on infinite heat sink.

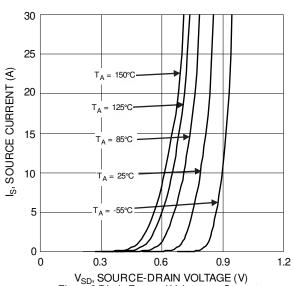
7. Short duration pulse test used to minimize self-heating effect. 8. Guaranteed by design. Not subject to product testing.

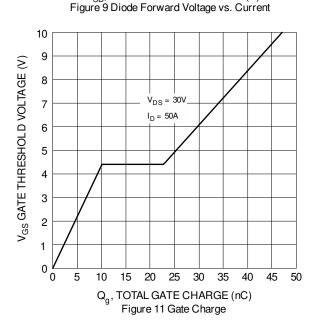












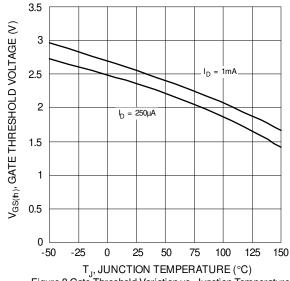
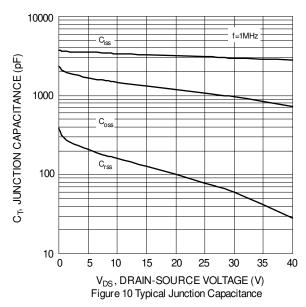
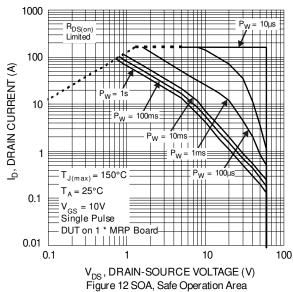
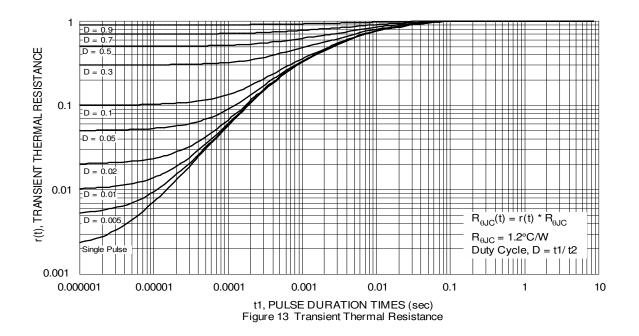


Figure 8 Gate Threshold Variation vs. Junction Temperature



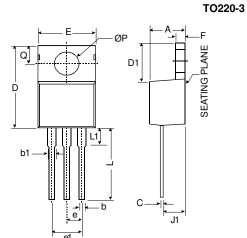






Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



TO220-3				
Dim	Min	Max		
Α	3.55	4.85		
b	0.51	1.14		
b1	1.14	1.78		
С	0.31	1.14		
D	14.20	16.50		
D1	5.84	6.86		
Е	9.70	10.70		
е	2.79	2.99		
e1	4.83	5.33		
F	0.51	1.40		
J1	2.03	2.92		
L	12.72	14.72		
L1	3.66	6.35		
Р	3.53	4.09		
Q	2.54	3.43		
All Dimensions in mm				



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