

# FDP2570/FDB2570

# 150V N-Channel PowerTrench® MOSFET

### **General Description**

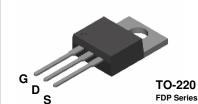
This N-Channel MOSFET has been designed specifically for switching on the primary side in the isolated DC/DC converter application. Any application requiring a 150V MOSFETs with low on-resistance and fast switching will benefit.

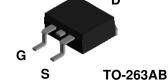
These MOSFETs feature faster switching and lower gate charge than other MOSFETs with comparable  $RDS_{(\text{ON})}$  specifications.

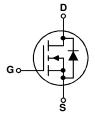
The result is a MOSFET that is easy and safer to drive (even at very high frequencies), and DC/DC power supply designs with higher overall efficiency.

#### **Features**

- 22 A, 150 V.  $R_{DS(ON)} = 80 \text{ m}\Omega$  @  $V_{GS} = 10 \text{ V}$   $R_{DS(ON)} = 90 \text{ m}\Omega$  @  $V_{GS} = 6 \text{ V}$
- Low gate charge (40nC typical)
- · Fast switching speed
- High performance trench technology for extremely low R<sub>DS(ON)</sub>
- 175°C maximum junction temperature rating







Absolute Maximum Ratings T<sub>A</sub>=25°C unless otherwise noted

Symbol	Parameter		Ratings	Units
V <sub>DSS</sub>	Drain-Source Voltage		150	V
$V_{GSS}$	Gate-Source Voltage		± 20	V
I <sub>D</sub>	Drain Current - Continuous	(Note 1)	22	А
	- Pulsed	(Note 1)	50	Α
P <sub>D</sub>	Total Power Dissipation @ T <sub>C</sub> = 25°C		93	W
	Derate above 25°C		0.63	W°/C
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range		-65 to +175	°C

**FDB Series** 

### **Thermal Characteristics**

$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	1.6	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	62.5	°C/W

**Package Marking and Ordering Information** 

Device Marking	Device	Reel Size	Tape width	Quantity
FDB2570	FDB2570	13"	24mm	800 units
FDP2570	FDP2570	Tube	n/a	45 units

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Drain-Sc	ource Avalanche Ratings (Note	1)	I	l	l	I
W <sub>DSS</sub>	Single Pulse Drain-Source Avalanche Energy	$V_{DD} = 75 \text{ V}, \qquad I_{D} = 11 \text{ A}$			375	mJ
I <sub>AR</sub>	Maximum Drain-Source Avalanche Current				11	Α
Off Char	acteristics					
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	150			V
ΔBV <sub>DSS</sub> ΔT <sub>J</sub>	Breakdown Voltage Temperature Coefficient	$I_D = 250 \mu A$ , Referenced to $25^{\circ}C$		154		mV/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{DS} = 120 \text{ V},  V_{GS} = 0 \text{ V}$			1	μΑ
I <sub>GSSF</sub>	Gate-Body Leakage, Forward	$V_{GS} = 20 \text{ V}, \qquad V_{DS} = 0 \text{ V}$			100	nA
I <sub>GSSR</sub>	Gate-Body Leakage, Reverse	$V_{GS} = -20 \text{ V},  V_{DS} = 0 \text{ V}$			-100	nA
On Char	acteristics (Note 2)					
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS}=V_{GS},I_D=250\;\mu A$	2	2.6	4	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate Threshold Voltage Temperature Coefficient	$I_D$ = 250 $\mu$ A, Referenced to 25°C		<b>-</b> 7		mV/°C
R <sub>DS(on)</sub>	Static Drain-Source	$V_{GS} = 10 \text{ V}, \qquad I_{D} = 11 \text{ A}$		61	80	mΩ
	On–Resistance	$V_{GS} = 6.0 \text{ V}, \qquad I_{D} = 10 \text{ A}$		63	90	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 11 A, T <sub>J</sub> = 125°C		127	175	
I <sub>D(on)</sub>	On–State Drain Current	$V_{GS} = 10 \text{ V}, \qquad V_{DS} = 10 \text{ V}$	25			A
<b>g</b> FS	Forward Transconductance	$V_{DS} = 10 \text{ V}, \qquad I_{D} = 11 \text{ A}$		39		S
Dynamic	Characteristics					
C <sub>iss</sub>	Input Capacitance	$V_{DS} = 75 \text{ V}, \qquad V_{GS} = 0 \text{ V},$		1911		pF
Coss	Output Capacitance	f = 1.0 MHz		106		pF
$C_{rss}$	Reverse Transfer Capacitance			33		pF
Switchin	ng Characteristics (Note 2)					
t <sub>d(on)</sub>	Turn-On Delay Time	$\begin{array}{cccccccccccccccccccccccccccccccccccc$		12	22	ns
t <sub>r</sub>	Turn-On Rise Time	$V_{GS} = 10 \text{ V}, \qquad R_{GEN} = 6 \Omega$		5	10	ns
t <sub>d(off)</sub>	Turn-Off Delay Time			33	53	ns
t <sub>f</sub>	Turn-Off Fall Time			23	37	ns
Qg	Total Gate Charge	$V_{DS} = 75 \text{ V}, \qquad I_{D} = 11 \text{ A},$		40	56	nC
Q <sub>gs</sub>	Gate-Source Charge	V <sub>GS</sub> = 10 V		7		nC
Q <sub>gd</sub>	Gate-Drain Charge			12		nC
	ource Diode Characteristics	and Maximum Ratings		•	•	
Is	Maximum Continuous Drain–Source	Diode Forward Current			22	Α
V <sub>SD</sub>	Drain-Source Diode Forward Voltage	$V_{GS} = 0 \text{ V},  I_S = 11 \text{ A}$ (Note 2)		0.83	1.3	V

#### Notes

- 1. Calculated continuous current based on maximum allowable junction temperature.
- 2. Pulse Test: Pulse Width < 300 $\mu$ s, Duty Cycle < 2.0%

# **Typical Characteristics**

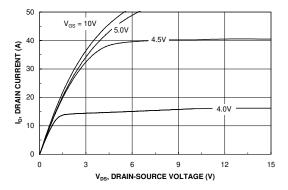


Figure 1. On-Region Characteristics.

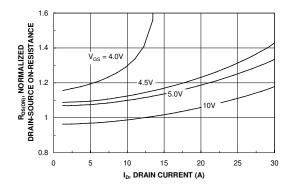


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage.

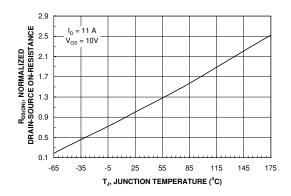


Figure 3. On-Resistance Variation with Temperature.

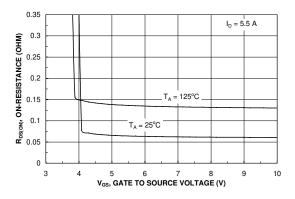


Figure 4. On-Resistance Variation with Gate-to-Source Voltage.

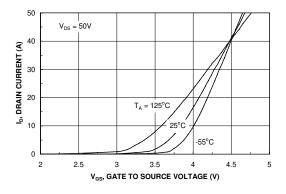


Figure 5. Transfer Characteristics.

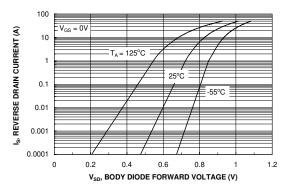
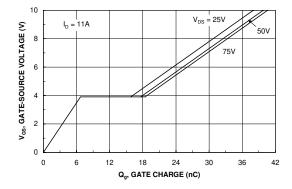


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.

# **Typical Characteristics**



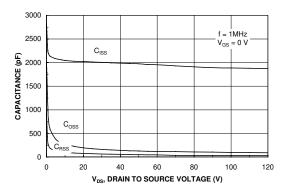
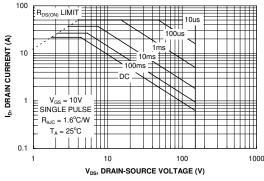


Figure 7. Gate Charge Characteristics.



Figure 8. Capacitance Characteristics.



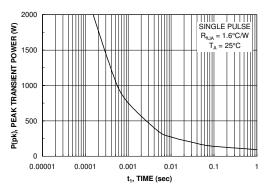


Figure 9. Maximum Safe Operating Area.



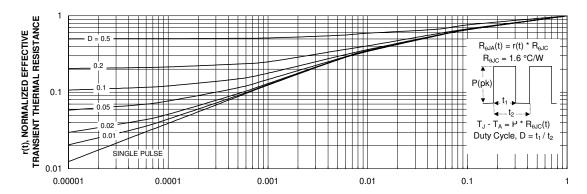


Figure 11. Transient Thermal Response Curve.

Thermal characterization performed using the conditions described in Note 1. Transient thermal response will change depending on the circuit board design.

### **TRADEMARKS**

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACEx™	FAST ®	OPTOPLANAR™	STAR*POWER™
Bottomless™	FASTr™	PACMAN™	Stealth™
CoolFET™	FRFET™	$POP^{TM}$	SuperSOT™-3
CROSSVOLT™	GlobalOptoisolator™	Power247™	SuperSOT™-6
DenseTrench™	GTO™	PowerTrench®	SuperSOT™-8
DOME™	HiSeC™	QFET™	SyncFET™
EcoSPARK™	ISOPLANAR™	QS™	TinyLogic™
E <sup>2</sup> CMOS <sup>TM</sup>	LittleFET™	QT Optoelectronics™	TruTranslation™
EnSigna™	MicroFET™	Quiet Series™	UHC™
FACT™	MICROWIRE™	SILENT SWITCHER®	UltraFET®
FACT Quiet Series™	OPTOLOGIC™	SMART START™	VCX™

STAR\*POWER is used under license

#### **DISCLAIMER**

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

### LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

- 1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the
- 2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

### PRODUCT STATUS DEFINITIONS

#### **Definition of Terms**

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.

Rev. H3