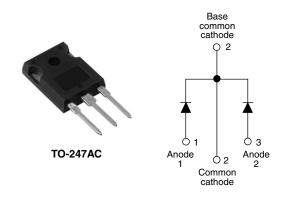
## Vishay High Power Products

# Ultrafast Rectifier, 2 x 15 A FRED Pt<sup>™</sup>



SHA

PRODUCT SUMMARY				
t <sub>rr</sub>	60 ns			
I <sub>F(AV)</sub>	2 x 15 A			
V <sub>R</sub>	400 V			

#### FEATURES

- Ultrafast recovery time
- Low forward voltage drop
- · Low leakage current
- 175 °C operating junction temperature
- Designed and qualified for industrial level

#### **DESCRIPTION/APPLICATIONS**

FRED Pt<sup>™</sup> series are the state of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, dc-to-dc converters as well as freewheeling diodes in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

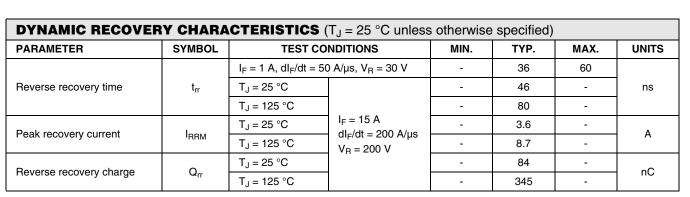
ABSOLUTE MAXIMUM RATINGS					
PARAMETER		ETER SYMBOL TEST CONDITIONS		VALUES	UNITS
Peak repetitive reverse voltage		V <sub>RRM</sub>		400	V
Average rectified forward current	per leg			15	٨
	total device		Rated V <sub>R</sub> , T <sub>C</sub> = 149 °C	30	
Non-repetitive peak surge current per leg		I <sub>FSM</sub>	T <sub>C</sub> = 25 °C	200	A
Peak repetitive forward current per leg		I <sub>FRM</sub>	Rated $V_R$ , $T_C$ = 149 °C, square wave, 20 kHz	30	
Operating junction and storage temperatures		T <sub>J</sub> , T <sub>Stg</sub>		- 65 to 175	°C

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	400	-	-	
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 15 A	-	1.17	1.25	V
		I <sub>F</sub> = 15 A, T <sub>J</sub> = 150 °C	-	0.93	1.12	
Reverse leakage current		$V_R = V_R$ rated	-	0.3	10	μA
	I <sub>R</sub>	$T_J = 150 \ ^{\circ}C, \ V_R = V_R \ rated$	-	30	500	
Junction capacitance	CT	V <sub>R</sub> = 400 V - 28		-	pF	
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	12	-	nH

## 30CPU04

# Vishay High Power Products

#### Ultrafast Rectifier, 2 x 15 A FRED Pt<sup>™</sup>



THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 65	-	175	°C
Thermal resistance, junction to case per leg	R <sub>thJC</sub>		-	0.8	1.5	
Thermal resistance, junction to ambient per leg	R <sub>thJA</sub>	Typical socket mount	-	-	40	°C/W
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.4	-	
Weight			-	6.0	-	g
			-	0.21	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf ⋅ cm (lbf ⋅ in)
Marking device		Case style TO-247AC		30C	PU04	•

/ISHA



I<sub>F</sub> - Instantaneous Forward Current (A) 100

10

1

0.1

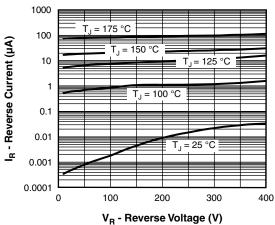
## 30CPU04

Ultrafast Rectifier, 2 x 15 A FRED  $Pt^{TM}$  Vishay High Power Products

T<sub>J</sub> = 175 °C

T<sub>J</sub> = 150 °C

T<sub>J</sub> = 25 °C



V<sub>F</sub> - Forward Voltage Drop (V) Fig. 1 - Typical Forward Voltage Drop Characteristics

0.2 0.4 0.6 0.8 1.0 1.2 1.4 1.6 1.8 2.0

Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

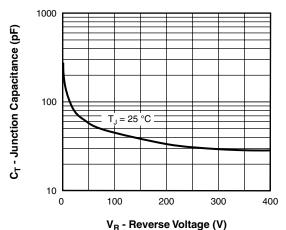


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

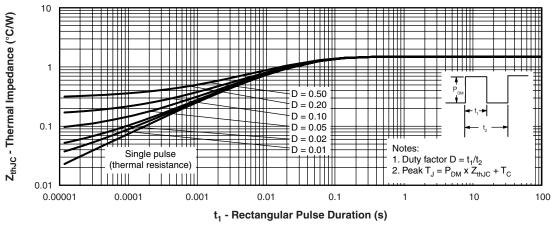


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics

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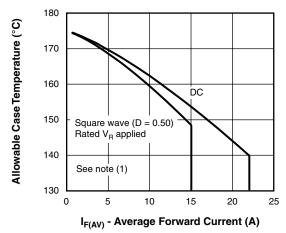
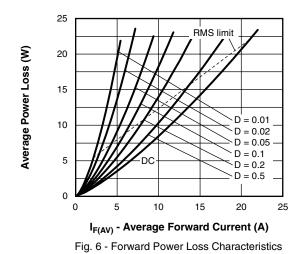
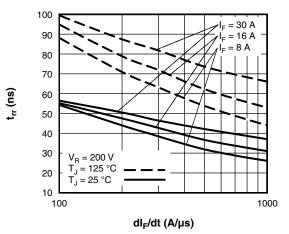


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current



#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC};$   $Pd = Forward power loss = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)};$  $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D); I_R \text{ at } V_{R1} = Rated V_R$ 



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Fig. 7 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

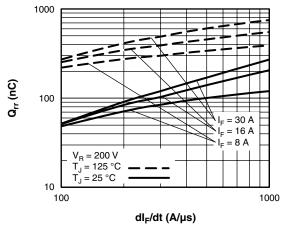
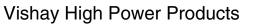


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt



#### Ultrafast Rectifier, 2 x 15 A FRED Pt<sup>™</sup>



 $V_{R} = 200 V$   $L = 70 \mu H$  D.U.T. D.U.T.

Fig. 9 - Reverse Recovery Parameter Test Circuit

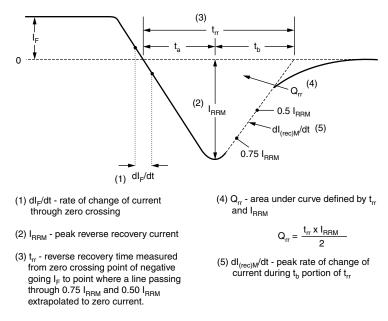
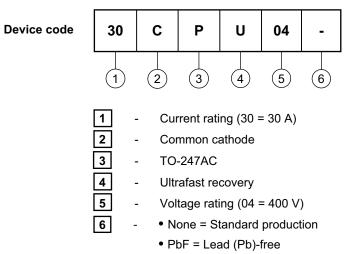


Fig. 10 - Reverse Recovery Waveform and Definitions

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#### ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS		
Dimensions http://www.vishay.com/doc?95223		
Part marking information	http://www.vishay.com/doc?95226	



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