

# EVQ3426-L-00A

6A, 35V Boost Converter with Programmable Switching Frequency and UVLO Evaluation Board

### **DESCRIPTION**

The MPQ3426 is a current-mode step-up converter with a 6A,  $90m\Omega$  internal switch that provides a highly efficient regulator with a fast MPQ3426 response. The features programmable frequency of up to 2MHz that allows for easy filtering and reduces noise. An external compensation pin gives the user flexibility in setting loop dynamics, and uses small, low-ESR, ceramic output capacitors. Soft-start results in a small inrush current and can be programmed with an external capacitor. The MPQ3426 operates from an input voltage as low as 3.2V and can generate up to a 35V output.

The MPQ3426's features include under-voltage lockout, current limiting, and thermal overload protection. The MPQ3426 is available in a low profile 14-pin 3mm×4mm QFN package with an exposed pad.

### **ELECTRICAL SPECIFICATIONS**

Parameter	Symbol	Value	Units
Input Voltage	V <sub>IN</sub>	6-9	V
Output Voltage	Vout	12	V
Output Current	I <sub>OUT</sub>	2	Α

### **FEATURES**

- 6A, 90mΩ, 45V Power MOSFET
- Uses Very Small Capacitors and Inductors
- Wide Input Range: 3.2V to 22V
- Output Voltage as High as 35V
- Programmable fsw: 300kHz to 2MHz
- Programmable UVLO, Soft-Start, UVLO Hysteresis
- Micropower Shutdown <1µA</li>
- Thermal Shutdown 160°C
- Available in 14-Pin 3mm×4mm QFN Package

### **APPLICATIONS**

- Telecom—Power Supplies
- Audio—Microphone and Tuner Bias
- Automotive

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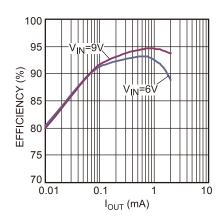
### **EVQ3426-L-00A EVALUATION BOARD**



(L x W x H) 2.5" x 2.5" x 0.4" (6.35cm x 6.35cm x 1.0cm)

	(Giocom & Giocom &				
Board Number		IC Number			
	EVQ3426-L-00A	MPQ3426DL			

#### **Efficiency**





### **QUICK START GUIDE**

The output voltage of this board is set to 12V. The frequency is set to 600kHz. The board layout accommodates most commonly used inductors and output capacitors.

- 1. Preset the power supply to  $6V \le VIN \le 9V$ .
- 2. Turn the power supply off.
- 3. Connect the power supply terminals to:
  - a. Positive (+): VIN
  - b. Negative (-): GND
- 4. Connect the load to:
  - a. Positive (+): VOUT
  - b. Negative (-): GND
- 5. Make sure the CN1 jumper is installed
- 6. Turn the power supply on after making the connections.
- 7. The MPQ3426 is enabled on the evaluation board once VIN is applied.
- 8. The output voltage VOUT can be changed by varying R2. Calculate the new value using the formula: R2 =  $(\frac{V_{OUT}}{V_{FR}} 1) \times R3$

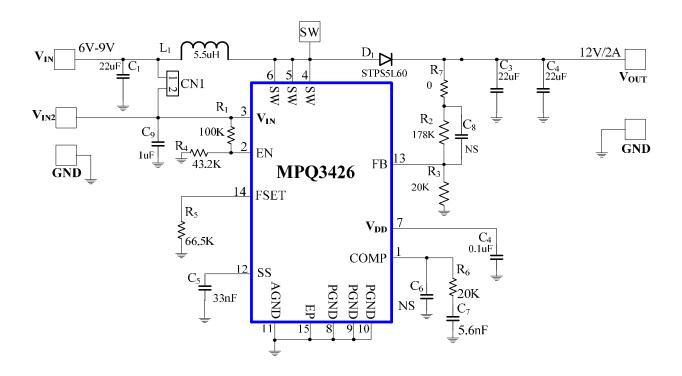
Where VFB = 1.225V and R3=20k $\Omega$ 

9. The frequency can be changed by adjusting R5. The formula is:  $F_{\text{SET}} = 23 \times (\text{R5}^{-0.86})$ 

Where FSET is in MHz and R5 is in  $k\Omega$ 



# **EVALUATION BOARD SCHEMATIC**





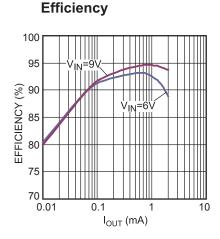
# **EVQ3426-L-00A BILL OF MATERIALS**

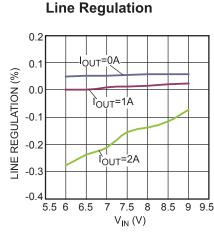
Qty	Ref	Value	Description	Package	Manufacturer	Manufacturer P/N
1	C1	22µF	Ceramic Cap., 50V, 10%, X7R	1210	muRata	GRM32ER71H226ME15L
1	C2	0.1μF	Ceramic Cap., 25V, 10%, X7R	0603	muRata	GCJ188R71E104KA12D
2	C3,C4	22µF	Ceramic Cap., 25V, 10%, X5R	1210	muRata	GRM21BR61E226ME44L
1	C5	33nF	Ceramic Cap., 16V, 10%, X7R	0603	muRata	GRM188R71C333KA01D
	C6,C8	NS		0603		
1	C7	5.6nF	Ceramic Cap., 25V, 10%, X7R	0603	muRata	GRM188R71H562KA01D
1	C9	1µF	Ceramic Cap., 25V, 10%, X7R	0603	muRata	GCM188R71E105KA64D
1	CN1		2 Pin Header, 2.54mm	2.54mm	Sullins	PCC02SAAN
1	CN1	SHUNT	Short Jumper, 2.54mm	2.54mm	Sullins	STC02SYAN
1	D1	STPS5L6 0	Schottky Diode, 60V, 5A	SMB	ST Microelec	STPS5L60U
0	J1	NS				
1	L1	5.5µH	Inductor, Idc10A, Rdc 10.3mΩ	10.5x10. 5mm	Wurth	744325550
1	R1	100k	Film Res., 5%	0603	Yageo	RC0603JR-07100KL
1	R2	178k	Film Res., 1%	0603	Yageo	RC0603FR-07178L
2	R3, R6	20k	Film Res., 1%	0603	Yageo	RC0603FR-0720KL
1	R4	43.2k	Film Res., 5%	0603	Yageo	RC0603JR-0743K2L
1	R5	66.5k	Film Res., 5%	0603	Yageo	RC0603JR-0766K5L
1	R7	0Ω	Film Res., 5%	0603	Yageo	RC0603JR-070RL
1	U1		Boost Converter	QFN14,3 x4mm	MPS	MPQ3426 R12

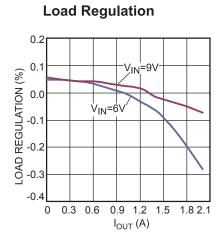


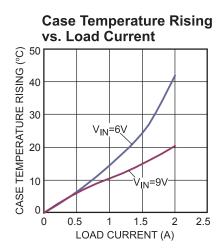
### **EVB TEST RESULTS**

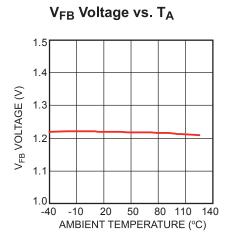
#### Performance waveforms are tested on the evaluation board.

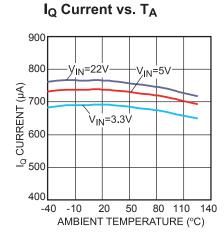


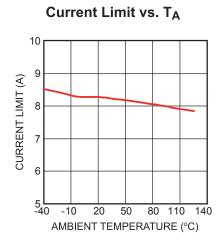


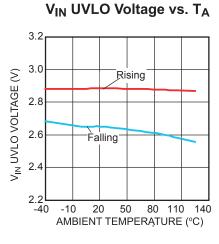


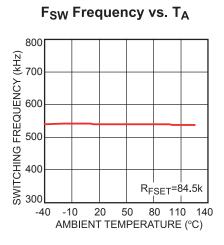








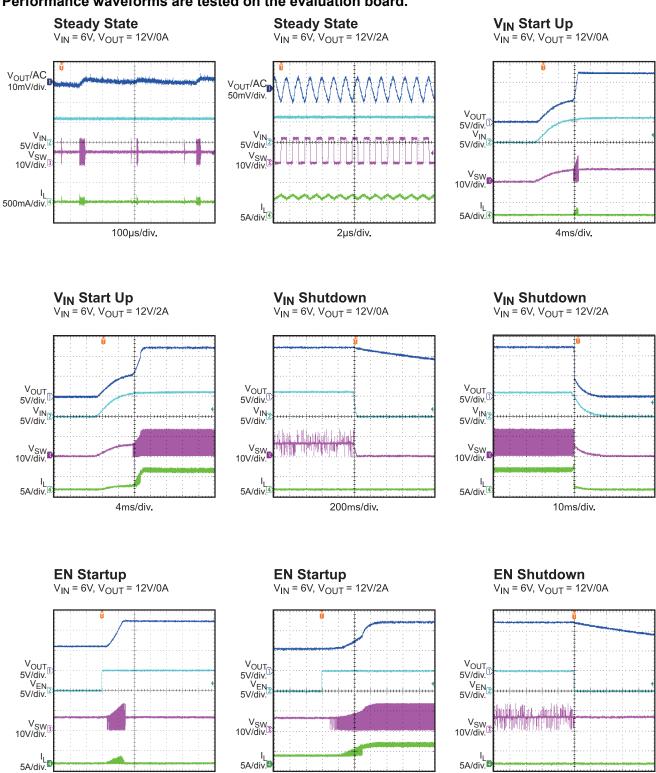






# **EVB TEST RESULTS** (continued)

#### Performance waveforms are tested on the evaluation board.



1ms/div.

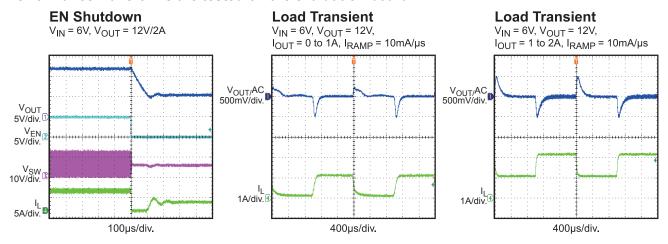
200ms/div.

1ms/div.



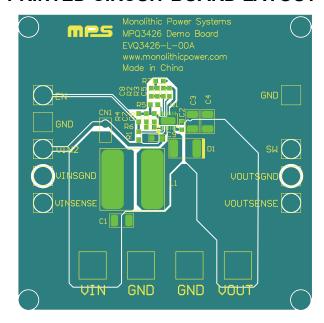
# **EVB TEST RESULTS** (continued)

Performance waveforms are tested on the evaluation board.





### PRINTED CIRCUIT BOARD LAYOUT



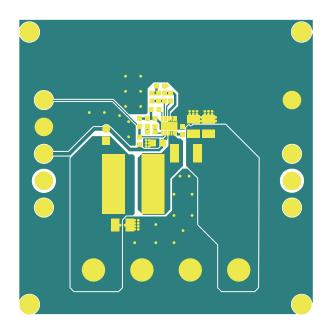


Figure 1—Top Silk Layer

Figure 2—Top Layer

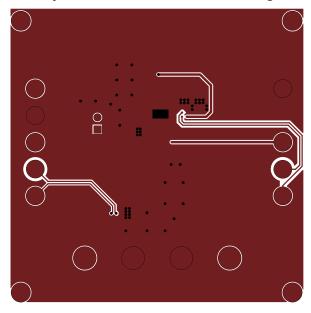


Figure 3—Bottom Layer

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