LA1654C

Monolithic Linear IC Time Code Reception IC



Overview

The LA1654C time code reception IC receives long-wave time standard broadcasts (such as the Japanese JJY and German DCF77 standards) and detects and outputs the time code superposed on the long-wave signal. Applications can automatically correct their clock's time setting by using the time code received by the LA1654C. Note that the LA1654C is a bare chip product that is not packaged.

Function

• RF amplifier, rectifier, detector, time code output, and standby circuit.

Features

- Low-voltage operation (operating V_{CC} as low as 1.5V).
- Standby mode current drain less than or equal to 0.05μA. Japan : JJY 40/60kHz Germany : DCF77 77.5kHz

Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V _{CC} max		5.0	V
Allowable power dissipation	Pd max	Ta ≤ 70°C	10	mW
Operating temperature	Topr		-20 to +70	°C
Storage temperature	Tstg		-40 to +125	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

Recommended Operating Conditions at Ta = 25°C

Deservator	Cirrenti e l	Conditions	Conditions			
Parameter	Symbol	Conditions	min	typ	max	Unit
Recommended supply voltage	V _{CC}		1.5		3.0	V
Operating supply voltage range	V _{CC} op		1.1		3.6	V

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

ORDERING INFORMATION

See detailed ordering and shipping information on page 5 of this data sheet.

Operating Characteristics at $Ta = 25^{\circ}C$, $V_{CC} = 3.0V$

* : Packaged in a VSON16 package and measured using the SON11T016-001-MF socket (Yamaichi Electronics Co., Ltd.)

Overall Characteristics

Deventer	Queen had			Ratings		
Parameter	Symbol	Conditions	min	typ	max	Unit
Quiescent current	Icco	No input, PAD15 = 0V, PAD10 = 3V	30	37	50	μA
Standby mode current drain	ISTB	PAD15 = 3.0V			0.05	μA

AGC Amplifier Input Characteristics

Parameter	C: make al	Conditions		Ratings		Unit kΩ
Parameter	Symbol	Conditions	min	typ	max	
Input impedance	Zl	PAD1		800		kΩ
Input frequency range	F _{IN}	PAD1	37.5		80.0	kHz
Minimum input voltage	V _{MIN}	PAD1 input level			1	μVrms
Maximum input voltage	V _{MAX}	PAD1 input level	100			mVrms

TCO Output Characteristics - Input signal = PAD1, fin = 40kHz, PAD10 = 3V, PAD15 = 0V

Parameter	Symbol	Conditions		Ratings		Unit	
Parameter	Symbol	Conditions		typ	max	Unit	
High-level output voltage	VOH	PAD11 output level	2.9		3.0	V	
Low-level output voltage	V _{OL}	PAD11 output level	0		0.1	V	
Output pulse width (500 ms input)	T500	V _{IN} = 0 to 100dBµV, AM modulation (1Hz square wave, duty = 50%, 10:1 modulation)	400	520	600	ms	
Output pulse width (800 ms input)	T800	V_{IN} = 0 to 100dBµV, AM modulation (1Hz square wave, duty = 80%, 10:1 modulation)	600	730	800	ms	
Output pulse width (200 ms input)	T200	V_{IN} = 0 to 100dBµV, AM modulation (1Hz square wave, duty = 20%, 10:1 modulation)	200	300	400	ms	

STB Control Characteristics

Parameter	Cumbol	Conditions		Ratings		Unit
Parameter	Symbol	Conditions	min	typ	max	
Standby on voltage	V _{SH}	PAD15 DC voltage	2.9		3.0	V
Standby off voltage	V _{SL}	PAD15 DC voltage	0		0.1	V
High-level pin input current	ISH	PAD15 = 3V			0.1	μA
Low-level pin input current	I _{SL}	PAD15 = 0V			0.3	μA

HOLD Control Characteristics - PAD15 = 0V

Doromotor	C: make al	Oradiliana		Ratings		Unit V
Parameter	Symbol	Conditions	min	typ	max	
Hold on voltage	V _{HL}	PAD10 DC voltage	0		0.1	V
Hold off voltage	V _{HH}	PAD10 DC voltage	2.9		3.0	V
High-level pin input current	Інн	PAD10 = 3V			0.1	μA
Low-level pin input current	I _{HL}	PAD10 = 0V			0.3	μA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

Chip Specifications

Parameter	Conditions	Ratings	Unit
Chip size		1.26×2.00	mm ²
Chip thickness		330(±20)	μm
Pad size		127.5×127.5	μm²
Pad opening		105×105	μm²

PAD Coordinates

PAD	X-Axis	Y-Axis	PAD	X-Axis	Y-Axis
P1	902	151	TEST PAD	776.5	1849
P2	1109	299.5	P9	368.5	1849
P3A	1109	717.5	P10	151	1747
P3B	1109	508.5	P11	151	1600
P4	1109	926.5	P12	151	1453
P5	1109	1073.5	P13	151	547
P6A	1109	1282.5	P14	151	400
P6B	1109	1491.5	P15	151	253
P7	1109	1700.5	P16	368.5	151
P8	926	1849			

Notes

1. The left upper corner of the Pad Layout Diagram on the following page is the origin, the X axis increases to the right and the Y axis increases in the downward direction.

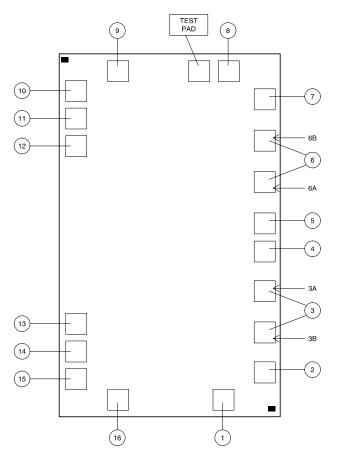
2. Units : μm

3. The pad coordinates give the coordinate values of the center of the pads.

4. Both of each of the pairs P3A/P3B (V_CC) and P6A/P6B (ground) must be bonded.

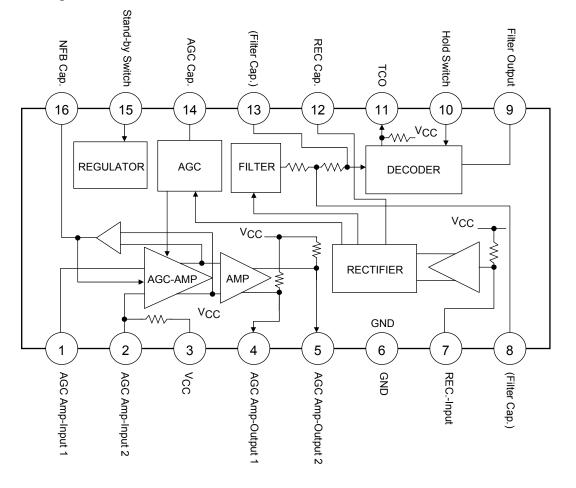
5. The test pads must not be connected (NC).

Pad Layout Diagram



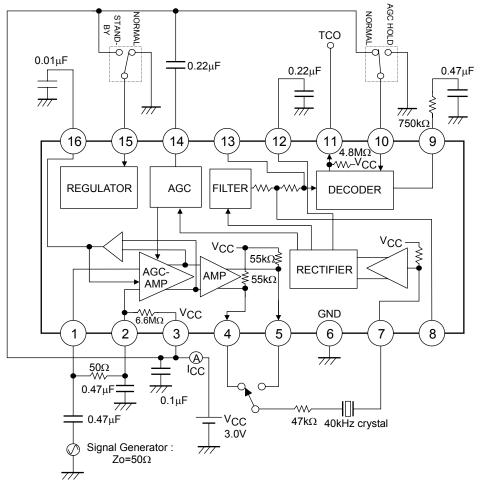
PCA00620

Block Diagram



PCA00621

Test Circuit Diagram



ORDERING INFORMATION

Device	Package	Shipping (Qty / Packing)
LA1654C-X1	Chip (Pb-Free)	2250 / Waffle Pack

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